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April 1st, 2010 Renesas Electronics Corporation

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HA1630D01/02/03 Series

Ultra-Small Low Voltage Operation CMOS Dual Operational Amplifier

REJ03D0800-0200 Rev.2.00 Feb 07, 2007

Description

The HA1630D01/02/03 are dual CMOS Operational Amplifiers realizing low voltage operation, low input offset voltage and low supply current. In addition to a low operating voltage from 1.8V, these device output can achieve full swing output voltage capability extending to either supply. Available in an ultra-small TSSOP-8 and MMPAK-8 package that occupy more small area against the SOP-8.

Features

Low power and single supply operation
 Low input offset voltage
 $V_{DD} = 1.8$ to 5.5 V
 $V_{IO} = 4.0$ mV Max

• Low supply current (per channel) $I_{DD} = 15 \mu A \text{ Typ (HA1630D01)}$

 $I_{DD} = 50 \mu A \text{ Typ (HA1630D02)}$ $I_{DD} = 100 \mu / A \text{ Typ (HA1630D03)}$

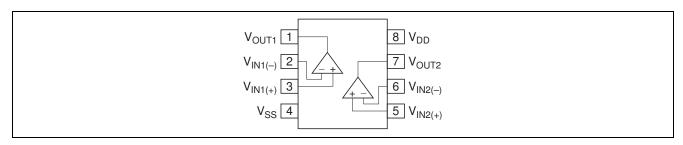
Maximum output voltage $V_{OH} = 2.9 \text{ V Min (at } V_{DD} = 3.0 \text{ V})$

• Low input bias current $I_{IB} = 1 \text{ pA Typ}$

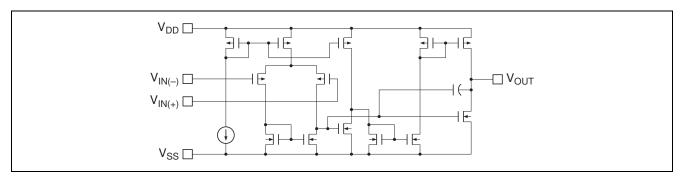
Ordering Information

| Type No. | Package Name | Package Code |
|-------------|--------------|--------------|
| HA1630D01T | | |
| HA1630D02T | TTP-8DA | PTSP0008JC-B |
| HA1630D03T | | |
| HA1630D01MM | | |
| HA1630D02MM | MMPAK-8 | PLSP0008JC-A |
| HA1630D03MM | | |

Pin Arrangement



Equivalent Circuit (per one channel)



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

| Items | Symbol | Ratings | Unit | Note |
|----------------------------|-----------------|------------------------|------|--------------------|
| Supply voltage | V_{DD} | 7 | V | |
| Differential input voltage | $V_{IN(diff)}$ | $-V_{DD}$ to $+V_{DD}$ | V | |
| Input voltage | V _{IN} | -0.3 to $+V_{DD}$ | V | *1 |
| Power dissipation | P _T | 240/145 | mW | TTP-8DA/MMPAK-8 *2 |
| Operating temp. Range | Topr | -40 to +85 | °C | |
| Storage temp. Range | Tstg | -55 to +125 | ℃ | |

Notes: 1. Do not apply Input Voltage exceeding V_{DD} or 7 V.

Electrical Characteristics

 $(V_{DD} = 3.0 \text{ V}, \text{Ta} = 25^{\circ}\text{C})$

| Items | Symbol | Min | Тур | Max | Unit | Test Condition |
|------------------------------|---------------------|-------------|---------|-----|------|--------------------------------------|
| Input offset voltage | V _{IO} | _ | _ | 4.0 | mV | Vin = 1.5 V |
| Input offset current | I _{IO} | _ | (1.0) | _ | рА | Vin = 1.5 V |
| Input bias current | I _{IB} | _ | (1.0) | _ | рА | Vin = 1.5 V |
| Output high voltage | V _{OH} | 2.9 | _ | _ | V | $R_L = 1 M\Omega$ |
| Output source current | Io source | 6 | 12 | _ | μА | V _{OH} = 2.5 V (HA1630D01) |
| | | 25 | 50 | _ | | V _{OH} = 2.5 V (HA1630D02) |
| | | 50 | 100 | _ | | V _{OH} = 2.5 V (HA1630D03) |
| Output low voltage | V _{OL} | _ | _ | 0.1 | V | $R_L = 1 M\Omega$ |
| Output sink current | I _{O SINK} | _ | (8.0) | _ | mA | V _{OL} = 0.5 V (HA1630D01) |
| | | _ | (1.0) | _ | | V _{OL} = 0.5 V (HA1630D02) |
| | | _ | (1.2) | _ | | V _{OL} = 0.5 V (HA1630D03) |
| Common mode input voltage | V _{CM} | -0.1 to 2.1 | _ | _ | V | |
| range | | | | | | |
| Slew rate | SR | _ | (0.125) | _ | V/μs | C _L = 20 pF (HA1630D01) |
| | | _ | (0.50) | _ | | C _L = 20 pF (HA1630D02) |
| | | | (1.00) | _ | | C _L = 20 pF (HA1630D03) |
| Voltage gain | A _V | 60 | 80 | _ | dB | |
| Gain bandwidth product | BW | _ | (200) | _ | kHz | C _L = 20 pF (HA1630D01) |
| | | _ | (680) | _ | | C _L = 20 pF (HA1630D02) |
| | | _ | (1200) | _ | | C _L = 20 pF (HA1630D03) |
| Power supply rejection ratio | PSRR | 60 | 80 | _ | dB | |
| Common mode rejection ratio | CMRR | 60 | 80 | _ | dB | |
| Supply current | I _{DD} | _ | 30 | 60 | μΑ | $R_L = \infty \text{ (HA1630D01)}$ |
| | | _ | 100 | 200 | | $R_L = \infty \text{ (HA1630D02)}$ |
| | | _ | 200 | 400 | | $R_{L} = \infty \text{ (HA1630D03)}$ |

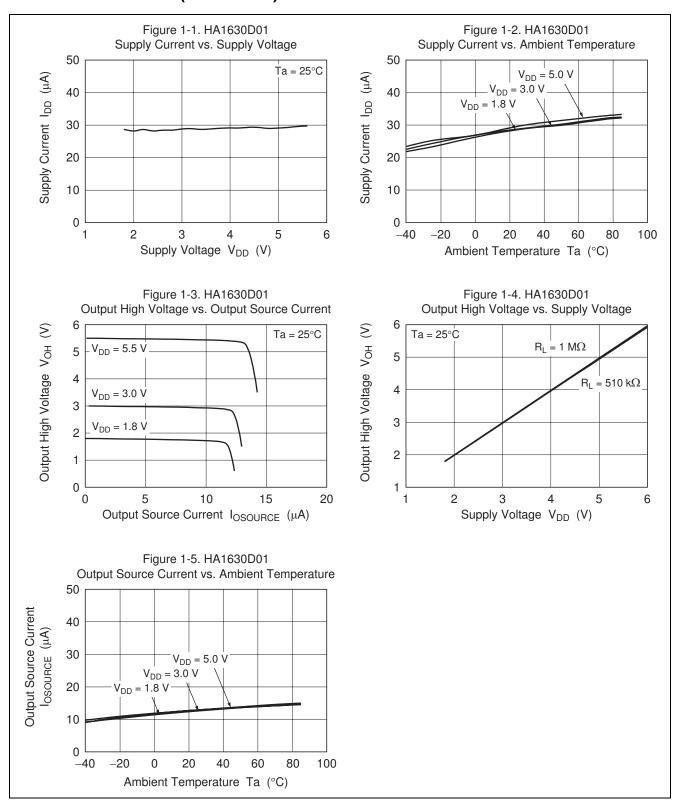
Note: 1. (): Design specification

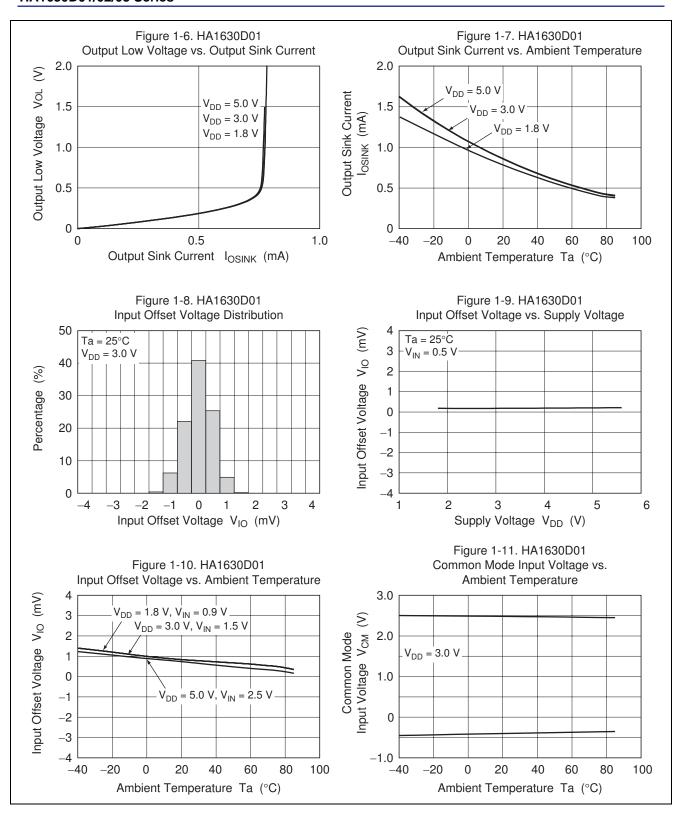
^{2.} The value of PTSP0008JC-B (TTP-8DAV) / PLSP0008JC-A (MMPAK-8). It computes from heat resistance θ ja = 520 °C/W, and 690 °C/W each other.

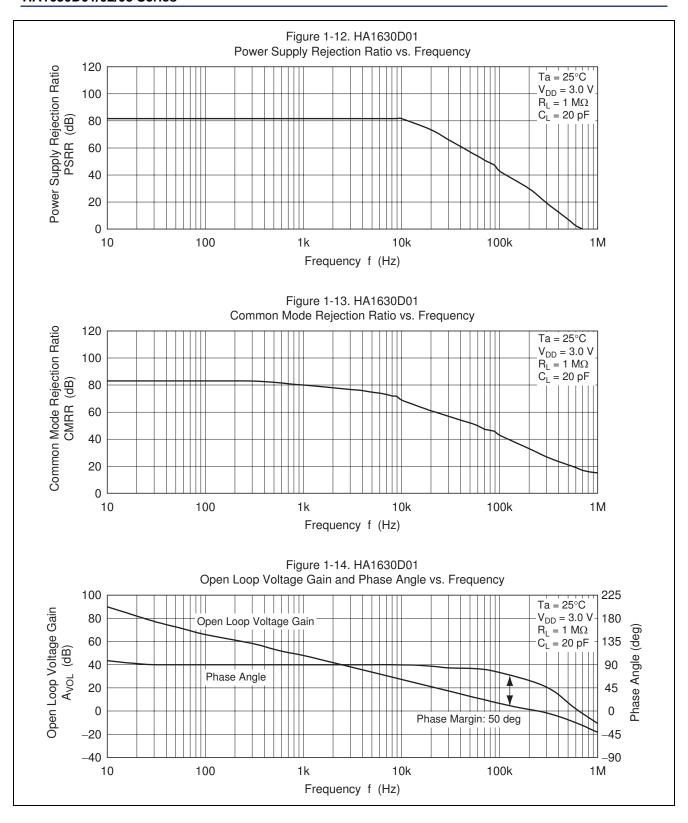
Table of Graphs

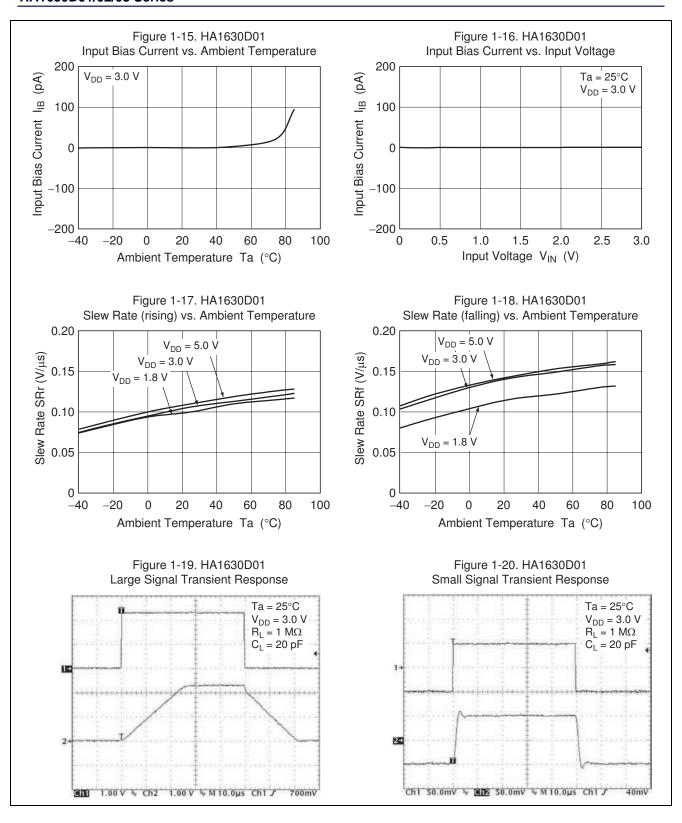
| Electric | cal Characte | printing | HA1630D01 | HA1630D02 | HA1630D03 | Test Circuit |
|---------------------------------|-----------------------|---------------------------------|-----------|-----------|-----------|-----------------|
| | | | Figure | Figure | Figure | |
| Supply current | I_{DD} | vs Supply voltage | 1-1 | 2-1 | 3-1 | 2 |
| | | vs Ambient temperature | 1-2 | 2-2 | 3-2 | _ |
| Output high voltage | V_{OH} | vs Output source current | 1-3 | 2-3 | 3-3 | 4 |
| | | vs Supply voltage | 1-4 | 2-4 | 3-4 | |
| Output source current | I _{O SOURCE} | vs Ambient temperature | 1-5 | 2-5 | 3-5 | 6 |
| Output low voltage | V_{OL} | vs Output sink current | 1-6 | 2-6 | 3-6 | 5 |
| Output sink current | I _{O SINK} | vs Ambient temperature | 1-7 | 2-7 | 3-7 | 6 |
| Input offset voltage | V_{IO} | Distribution | 1-8 | 2-8 | 3-8 | 1 |
| | | vs Supply voltage | 1-9 | 2-9 | 3-9 | |
| | | vs Ambient temperature | 1-10 | 2-10 | 3-10 | |
| Common mode input voltage range | V _{CM} | vs Ambient temperature | 1-11 | 2-11 | 3-11 | 7 |
| Power supply rejection ratio | PSRR | vs Frequency | 1-12 | 2-12 | 3-12 | 1 |
| Common mode rejection ratio | CMRR | vs Frequency | 1-13 | 2-13 | 3-13 | 7 |
| Voltage gain & phase angle | A _V | vs Frequency | 1-14 | 2-14 | 3-14 | 10 |
| Input bias current | I _{IB} | vs Ambient temperature | 1-15 | 2-15 | 3-15 | 3 |
| | | vs Input voltage | 1-16 | 2-16 | 3-16 | |
| Slew Rate (rising) | SRr | vs Ambient temperature | 1-17 | 2-17 | 3-17 | 9 |
| Slew Rate (falling) | SRf | vs Ambient temperature | 1-18 | 2-18 | 3-18 | |
| Slew rate | | Large signal transient response | 1-19 | 2-19 | 3-19 | |
| | | Small signal transient response | 1-20 | 2-20 | 3-20 | |
| Total harmonic distortion + | (0 dB) | vs. Output voltage p-p | _ | 2-21 | 3-21 | 8 |
| noise | (40 dB) | vs. Output voltage p-p | _ | 2-22 | 3-22 | |
| Maximum p-p output voltage | | vs Frequency | 1-21 | 2-23 | 3-23 | |
| Voltage noise density | | vs Frequency | 1-22 | 2-24 | 3-24 | |
| Channel separation | | vs Frequency | 1-23 | 2-25 | 3-25 | |

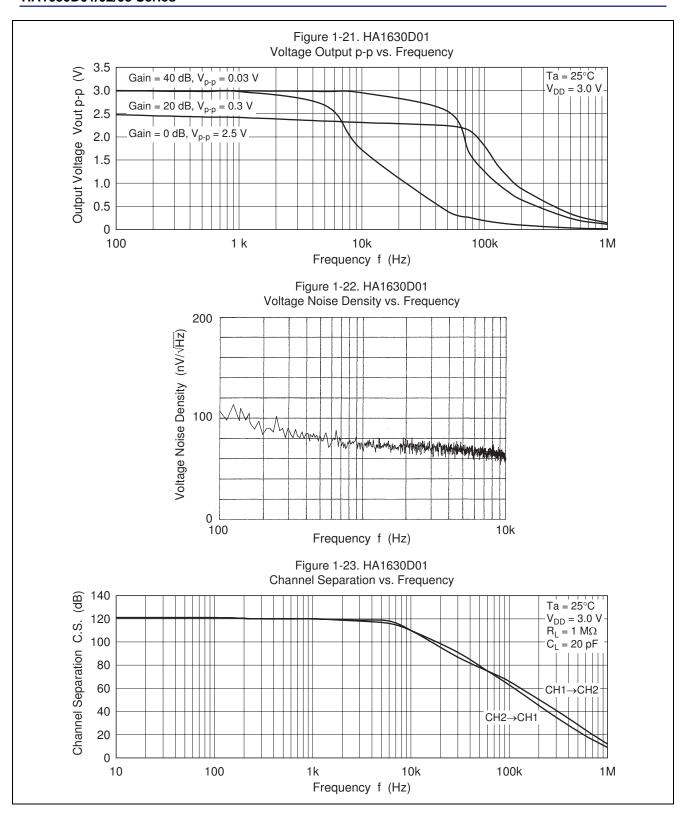
Main Characteristics (HA1630D01)



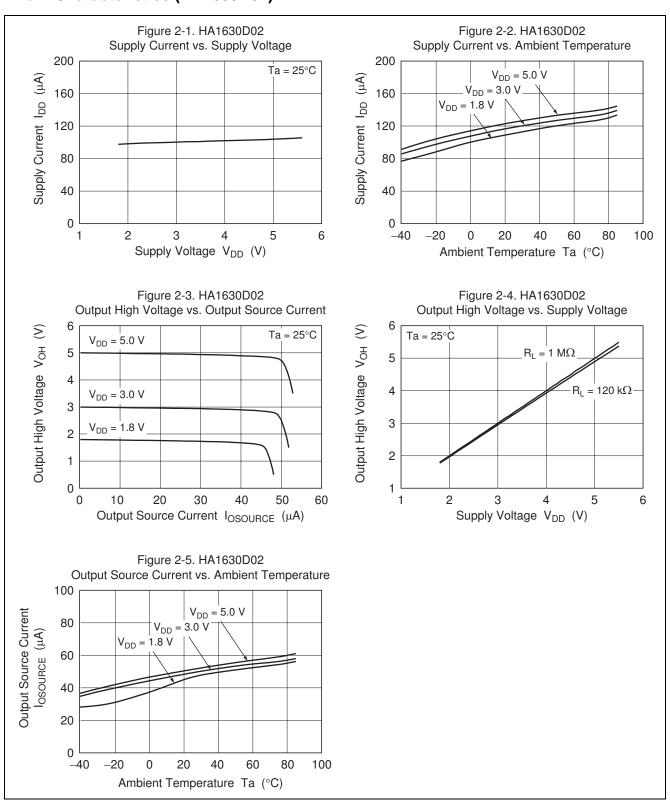


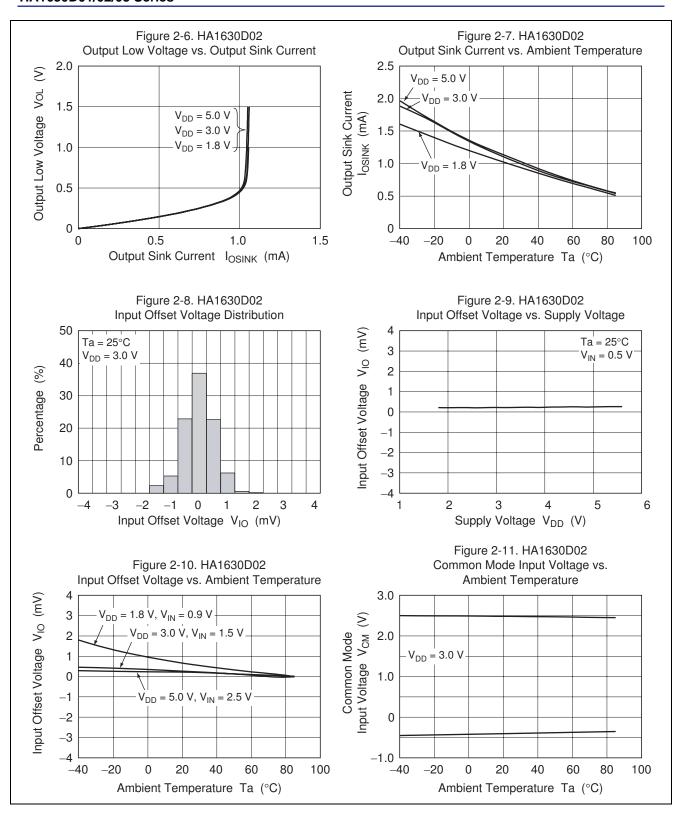


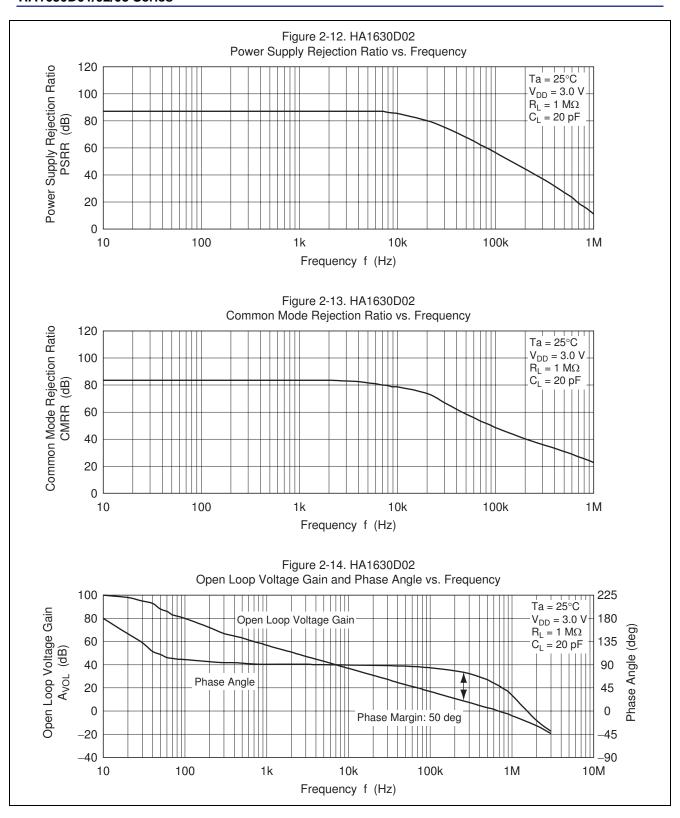


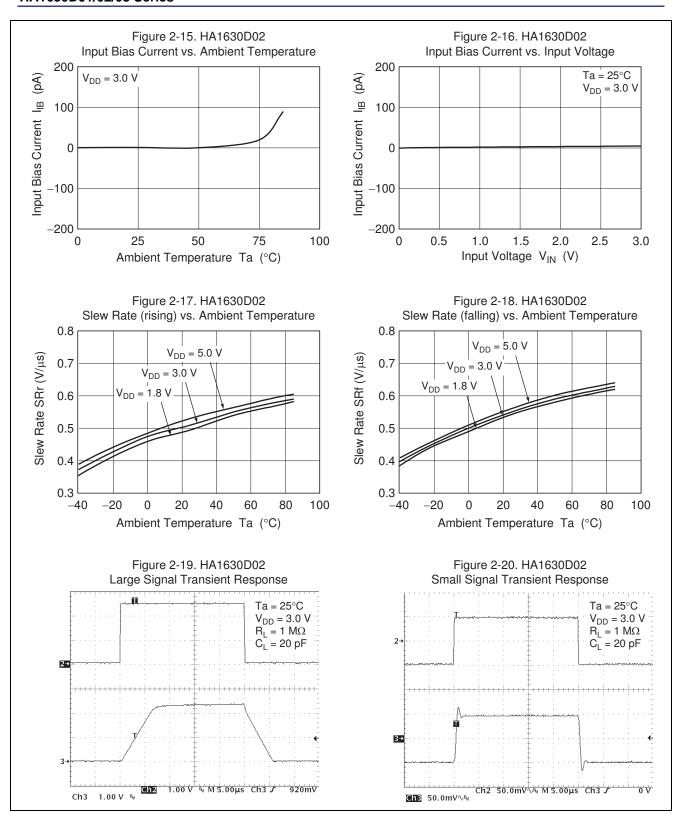


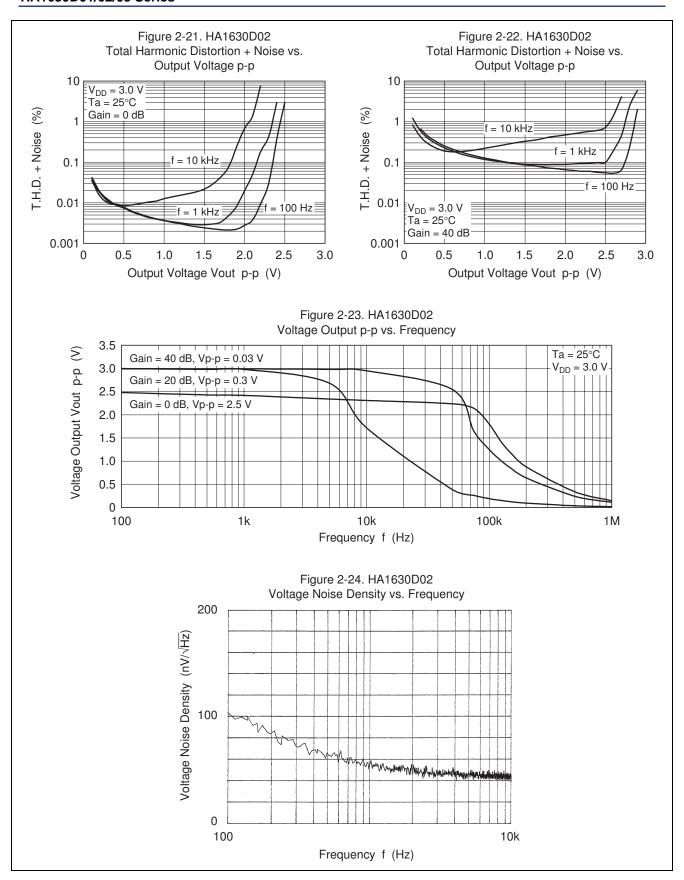
Main Characteristics (HA1630D02)

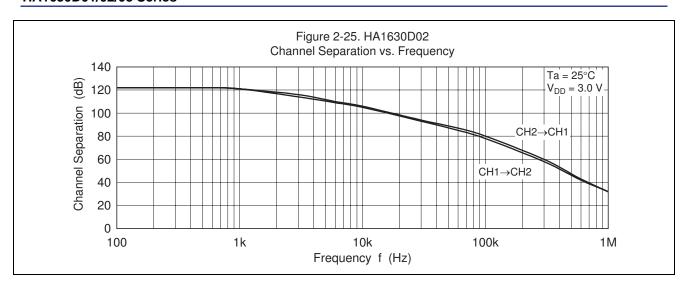




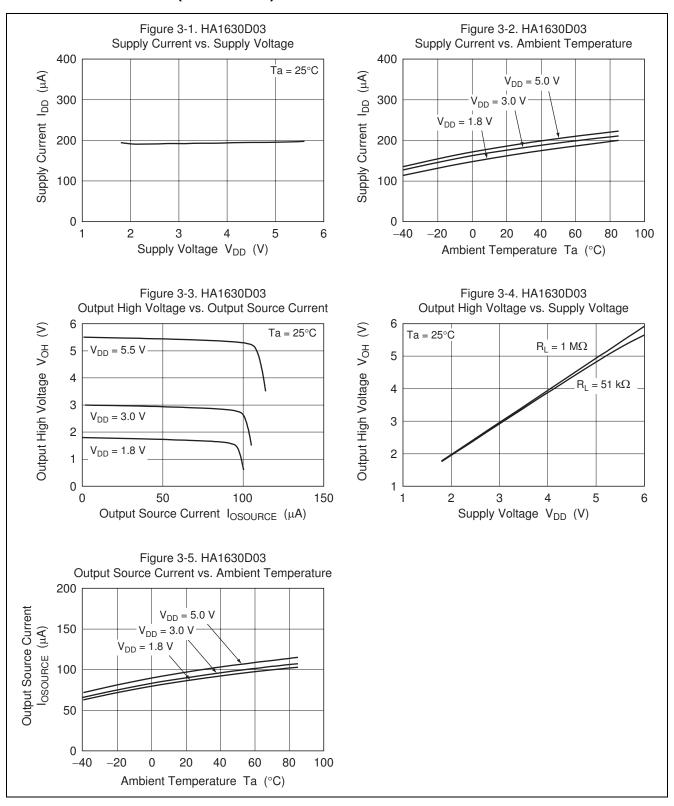


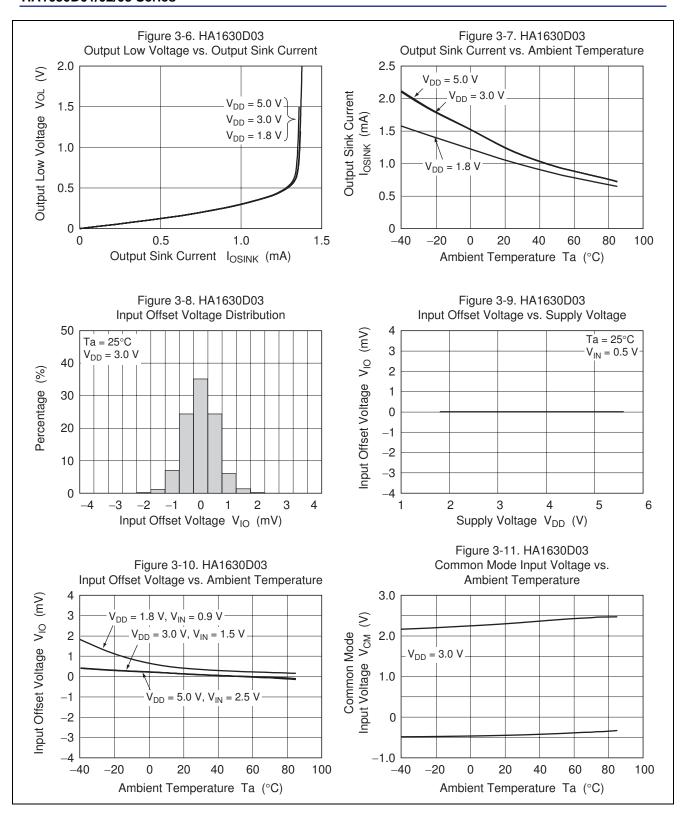


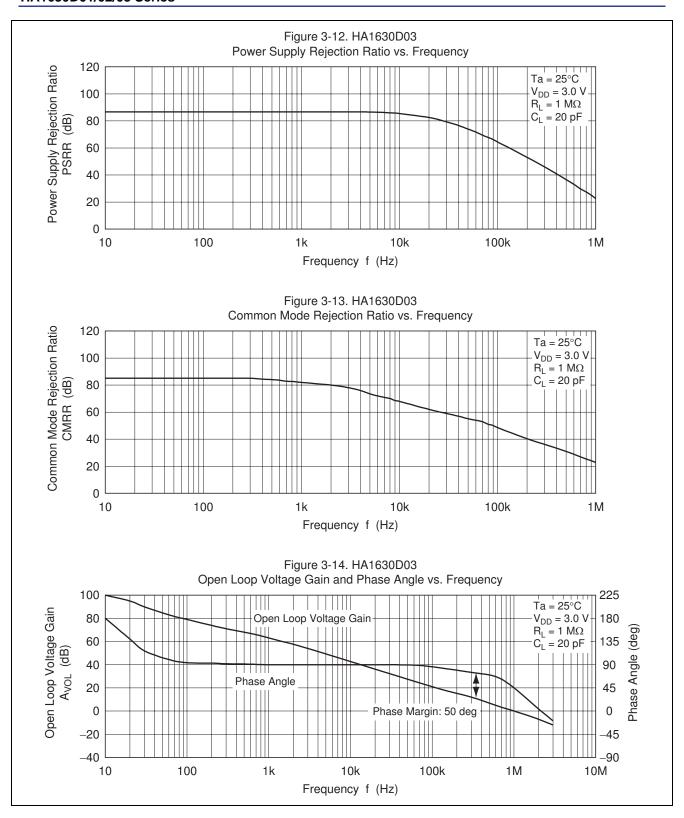


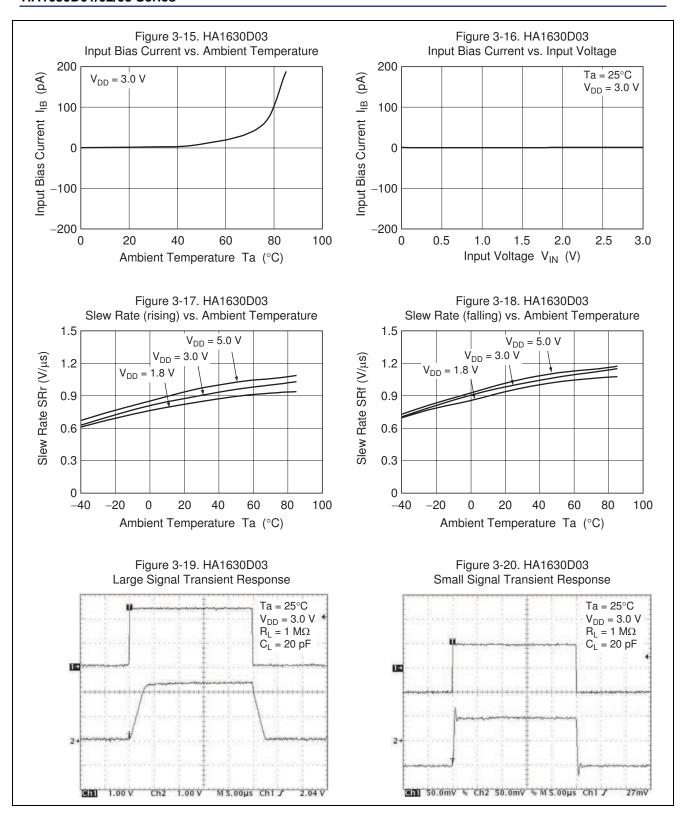


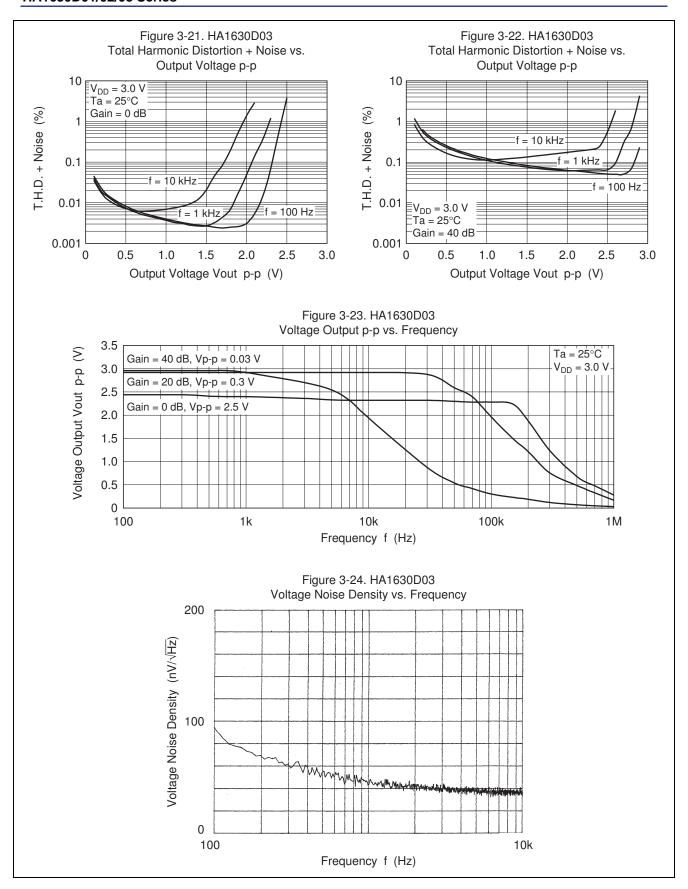
Main Characteristics (HA1630D03)

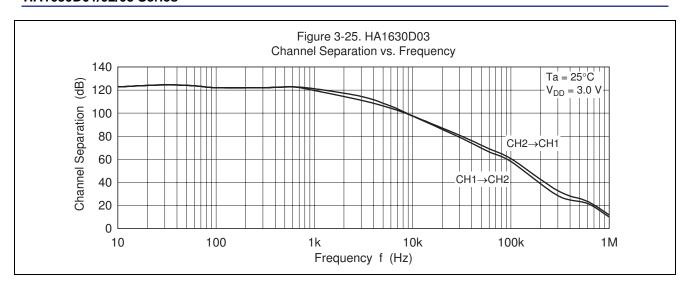






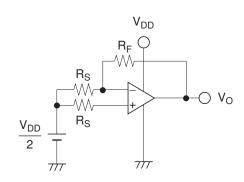






Test Circuits

1. Power Supply Rejection Ratio, PSRP & Voltage Offset, V_{IO}



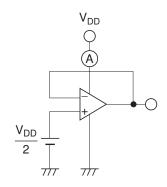
$$\frac{V_{IO}}{V_{IO}} = \left(V_O - \frac{V_{DD}}{2}\right) \times \frac{R_S}{R_S + R_F}$$

PSRR

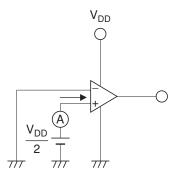
$$PSRR = -20log\left(\left|\frac{V_{O1} - V_{O2}}{V_{DD1} - V_{DD2}}\right| \times \frac{R_S}{R_S + R_F}\right)$$

Measure V_O corresponding to V_{DD1} = 1.8 V and V_{DD2} = 5.5 V

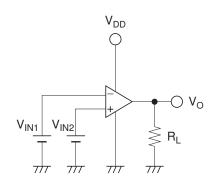
2. Supply Current, I_{DD}



3. Input Bias Current, I_{IB}



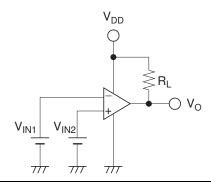
4. Output High Voltage, V_{OH}



$$\underline{V_{OH}}$$

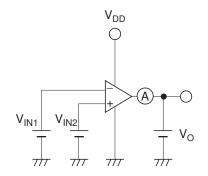
$$\begin{aligned} & \overline{R_L} = 1 \ M\Omega \\ & V_{IN1} = V_{DD} \, / \, 2 - 0.05 \ V \\ & V_{IN2} = V_{DD} \, / \, 2 + 0.05 \ V \end{aligned}$$

5. Output Low Voltage, V_{OL}



$$\begin{split} &\frac{V_{OL}}{R_L = 1 \ M\Omega} \\ &V_{IN1} = V_{DD} \, / \, 2 + 0.05 \ V \\ &V_{IN2} = V_{DD} \, / \, 2 - 0.05 \ V \end{split}$$

6. Output Source Current, IOSOURCE & Output Sink Current, IOSINK



$$V_{O} = V_{DD} - 0.5 \text{ V}$$

$$V_{IN1} = V_{DD} / 2 - 0.05 \text{ V}$$

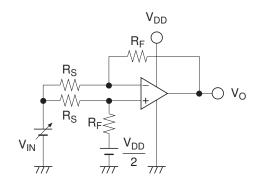
$$V_{IN2} = V_{DD} / 2 + 0.05 \text{ V}$$

I_{OSINK}

$$V_O = + 0.5 \text{ V}$$

 $V_{IN1} = V_{DD} / 2 + 0.05 \text{ V}$
 $V_{IN2} = V_{DD} / 2 - 0.05 \text{ V}$

7. Common Mode Input Voltage, V_{CM} & Common Mode Rejection Ratio, CMRR

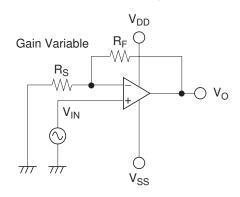


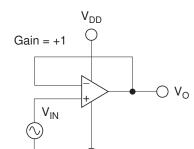
CMRR

$$CMRR = -20log\left(\left|\frac{V_{O1} - V_{O2}}{V_{IN1} - V_{IN2}}\right| \times \frac{R_S}{R_S + R_F}\right)$$

Measure V_O corresponding to V_{IN1} = 0 V and V_{IN2} = 2.1 V

8. Total Harmonic Distortion, THD

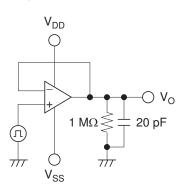




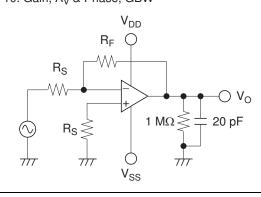
 V_{SS}

$$\label{eq:THD} \begin{split} \frac{\text{THD}}{\text{Gain Variable}} \\ &1 + R_F / R_S = 100 \\ &\text{freq} = 100 \text{ Hz}, 1 \text{ kHz}, 10 \text{ kHz} \end{split}$$

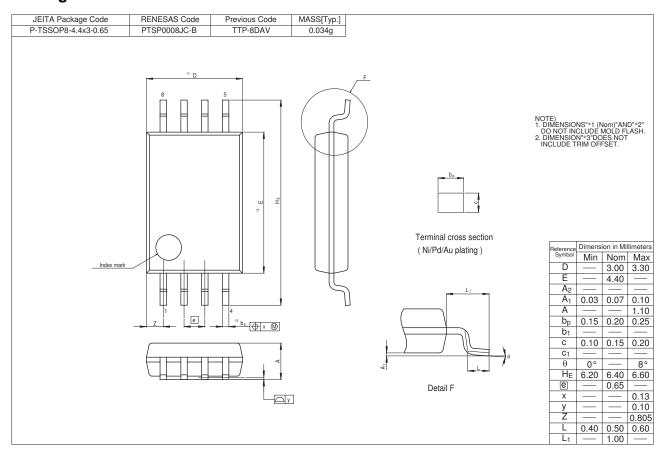
9. Slew Rate, SR

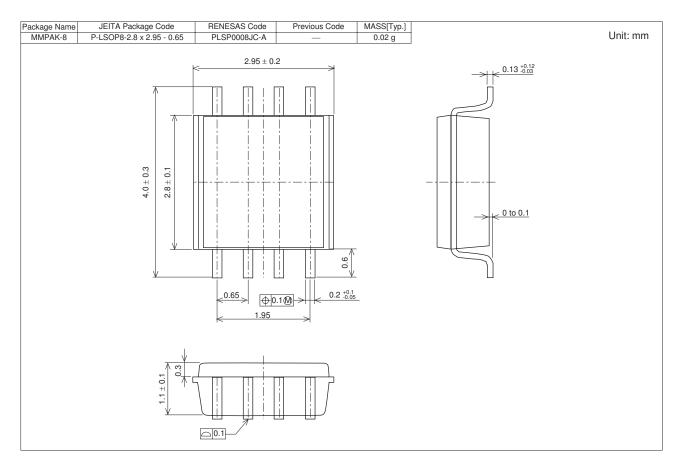


10. Gain, A_V & Phase, GBW

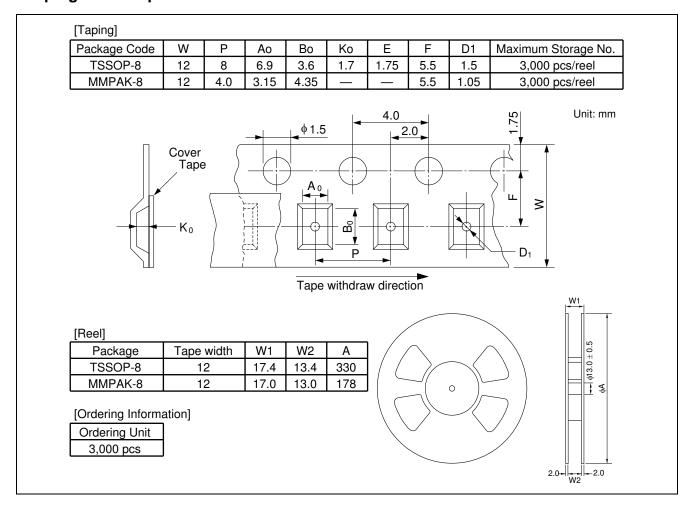


Package Dimensions

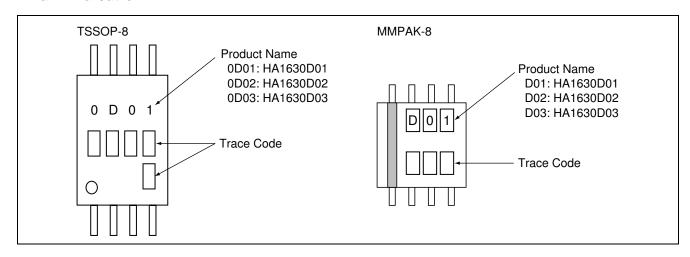




Taping & Reel Specification



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