

Rev.6.00

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(Previous: REJ03G1230-0500)

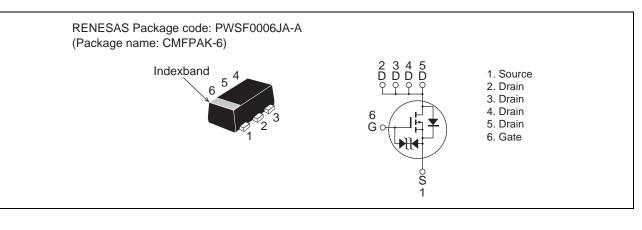
# HAT1093C

Silicon P Channel MOSFET Power Switching

#### Features

- Low on-resistance
- $R_{DS(on)} = 41 \text{ m}\Omega \text{ typ.} (at V_{GS} = -4.5 \text{ V})$
- Low drive current.
- 1.8 V gate drive devices.
- High density mounting

## Outline



## **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Ratings	Unit
Drain to Source voltage	V <sub>DSS</sub>	-12	V
Gate to Source voltage	V <sub>GSS</sub>	±8	V
Drain current	I <sub>D</sub>	-3	А
Drain peak current	I <sub>D</sub> (pulse) <sup>Note1</sup>	-12	А
Body - Drain diode reverse drain current	I <sub>DR</sub>	-3	А
Channel dissipation	Pch <sup>Note 2</sup>	900	mW
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1.  $PW \leq 10~\mu s,~duty~cycle \leq 1\%$ 

2. When using the glass epoxy board. (FR4 40  $\times$  40  $\times$  1.6mm), Ta = 25°C



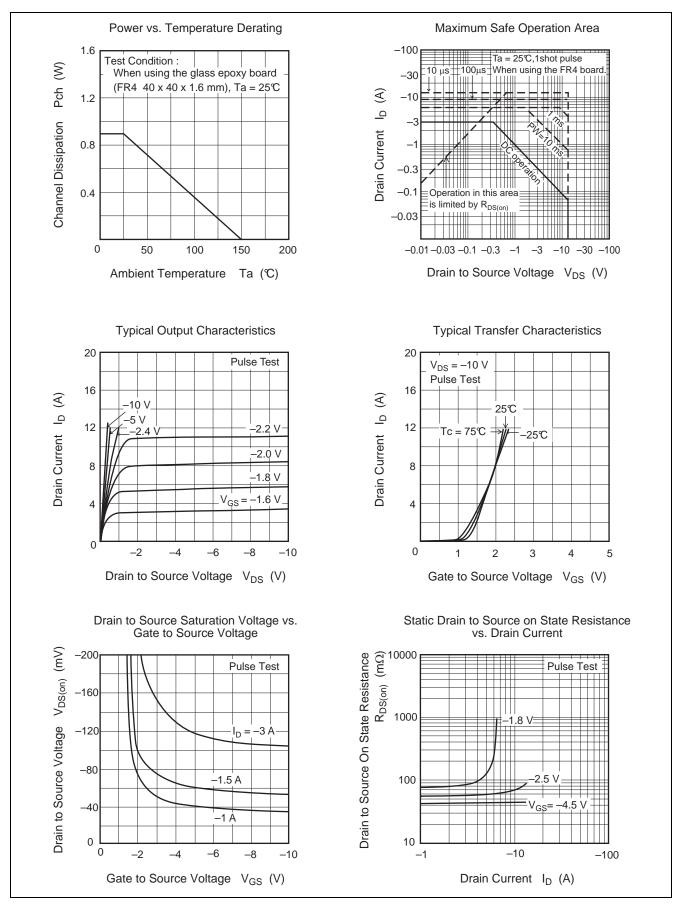
## **Electrical Characteristics**

						$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to Source breakdown voltage	V <sub>(BR)DSS</sub>	-12	—	_	V	$I_D = -10 \text{ mA}, V_{GS} = 0$
Gate to Source breakdown voltage	V <sub>(BR)GSS</sub>	±8	—	_	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to Source leakage current	I <sub>GSS</sub>	_	—	±10	μΑ	$V_{GS}=\pm6.4V,~V_{DS}=0$
Drain to Source leakage current	I <sub>DSS</sub>		—	-1	μA	$V_{DS} = -12 V, V_{GS} = 0$
Gate to Source cutoff voltage	V <sub>GS(th)</sub>	-0.3	—	-1.2	V	$I_D = -1 \text{ mA}, V_{DS} = -10 \text{ V}^{\text{Note3}}$
Drain to Source on state resistance	R <sub>DS(on)</sub>	_	41	54	mΩ	$I_D = -1.5 \text{ A}, V_{GS} = -4.5 \text{ V}^{Note3}$
		_	54	76	mΩ	$I_D = -1.5 \text{ A}, V_{GS} = -2.5 \text{ V}^{Note3}$
		_	79	119	mΩ	$I_D = -1.5 \text{ A}, V_{GS} = -1.8 \text{ V}^{Note3}$
Forward transfer admittance	y <sub>fs</sub>	4	6.5		S	$I_D = -1.5 \text{ A}, V_{DS} = -10 \text{ V}^{\text{Note3}}$
Input capacitance	Ciss	_	940		pF	$V_{DS} = -10 \text{ V}, \text{ V}_{GS} = 0,$ f = 1 MHz
Output capacitance	Coss	_	200	_	pF	
Reverse transfer capacitance	Crss	_	130	_	pF	
Total gate charge	Qg	_	11	_	nC	$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V},$
Gate to Source charge	Qgs	_	1.5	_	nC	$I_D = -3 A$
Gate to Drain charge	Qgd	_	3.5	_	nC	
Turn - on delay time	t <sub>d(on)</sub>	_	18	_	ns	$ \begin{array}{l} V_{DS} = -10 \ V, \ V_{GS} = -4.5 \ V, \\ I_D = -1.5 \ A, \ R_L = 6.7 \ \Omega, \\ R_g = 4.7 \ \Omega \end{array} $
Rise time	tr	_	23	_	ns	
Turn - off delay time	t <sub>d(off)</sub>		50	—	ns	
Fall time	t <sub>f</sub>	_	28	—	ns	
Body - Drain diode forward voltage	V <sub>DF</sub>	_	-0.8	-1.1	V	$I_F = -3 A, V_{GS} = 0$

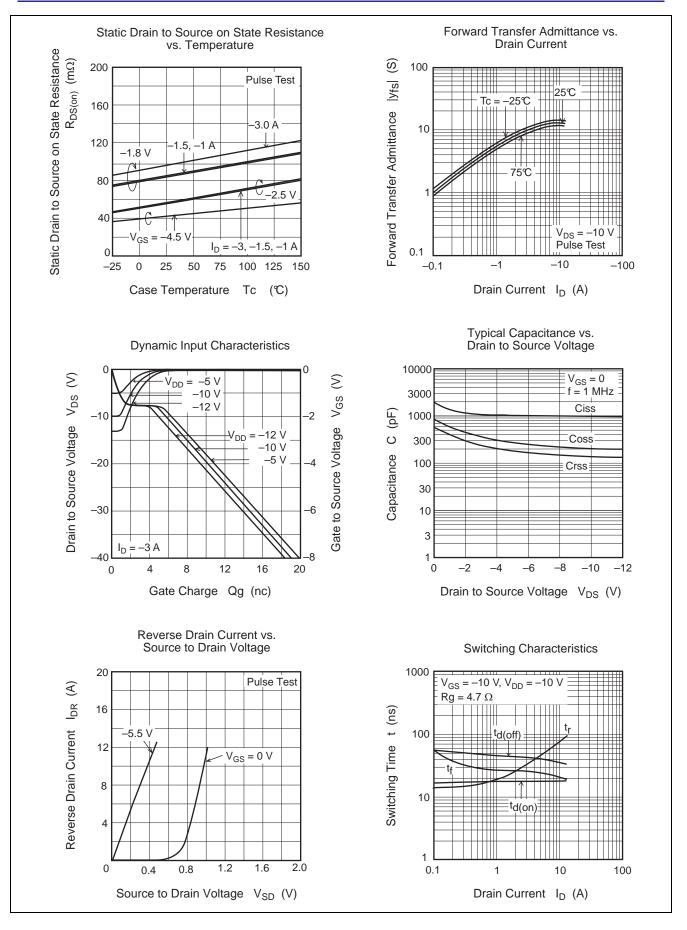
Note: 3. Pulse test

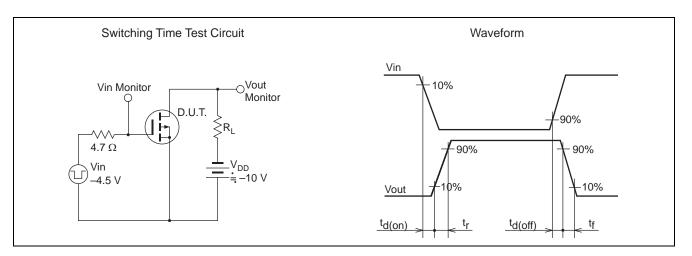


#### **Main Characteristics**



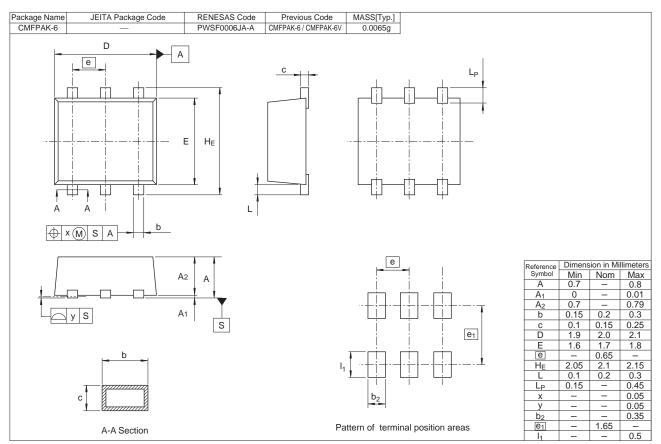








## **Package Dimensions**



## **Ordering Information**

Part Name	Quantity	Shipping Container
HAT1093C-EL#E	3000 pcs	Taping

Note: The symbol of a "#" are occasionally presented as a "-".



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