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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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HD74AC139/HD74ACT139

Dual 1-of-4 Decoder/Demultiplexer

REJ03D0249-0200Z (Previous ADE-205-369 (Z)) Rev.2.00 Jul.16.2004

Description

The HD74AC139/HD74ACT139 is a high-speed, dual 1-of-4 decoder/demultiplexer. The device has two independent decoders, each accepting two inputs and providing four mutually-exclusive active-Low outputs. Each decoder has an active-Low Enable input which can be used as a data input for a 4-output demultiplexer. Each half of the HD74AC139/HD74ACT139 can be used as a function generator providing all four minterms of two variables.

Features

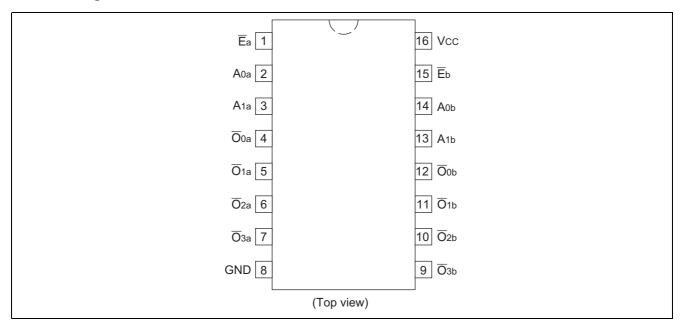
- Multifunction Capability
- Two Completely Independent 1-of-4 Decoders
- Active Low Mutually Exclusive Outputs
- Outputs Source/Sink 24 mA
- HD74ACT139 has TTL-Compatible Inputs
- Ordering Information: Ex. HD74AC139

Part Name	Package Type	Package Code	Package Abbreviation	Taping Abbreviation (Quantity)
HD74AC139AP	DIP-16 pin	DP-16E, -16FV	Р	_
HD74AC139AFPEL	SOP-16 pin (JEITA)	FP-16DAV	FP	EL (2,000 pcs/reel)
HD74AC139ARPEL	SOP-16 pin (JEDEC)	FP-16DNV	RP	EL (2,500 pcs/reel)
HD74AC139TELL	TSSOP-16 pin	TTP-16DAV	Т	ELL(2,000 pcs/reel)

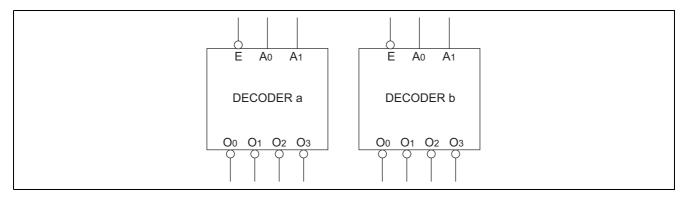
Notes: 1. Please consult the sales office for the above package availability.

2. The packages with lead-free pins are distinguished from the conventional products by adding V at the end of the package code.

Pin Arrangement



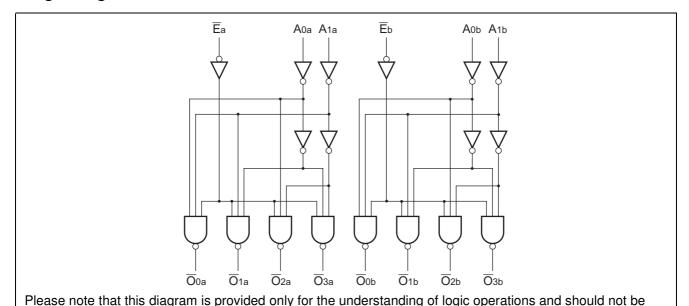
Logic Symbol



Pin Names

 $\begin{array}{ll} A_0,\,A_1 & \text{Address Inputs} \\ \overline{E} & \text{Enable Inputs} \\ \overline{O}_0 \text{ to } \overline{O}_3 & \text{Outputs} \end{array}$

Logic Diagram



Functional Description

used to estimate propagation delays.

The HD74AC139/HD74ACT139 is a high-speed dual 1-of-4 decoder/demultiplexer. The device has two independent decoders, each of which accepts two binary weighted inputs (A_0 to A_1) and provides four mutually exclusive active-Low outputs (\overline{O}_0 to \overline{O}_3). Each decoder has an active-Low enable (\overline{E}). When \overline{E} is High all outputs are forced High. The enable can be used as the data input for a 4-output demultiplexer application. Each half of the HD74AC139/HD74ACT139 generates all four minterms of two variables. These four minterms are useful in some applications, replacing multiple gate functions as shown in Figure a, and thereby reducing the number of packages required in a logic network.

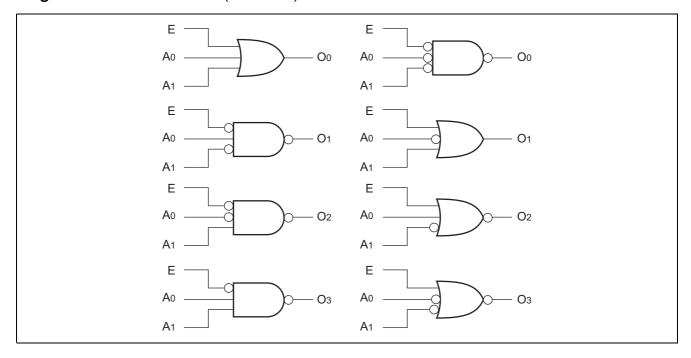
Truth Table

Inputs			Outputs			
Ē	\mathbf{A}_{0}	\mathbf{A}_{1}	$\overline{O}_{\!\scriptscriptstyle{0}}$	\overline{O}_1	$\overline{O}_{\!\scriptscriptstyle 2}$	$\overline{O}_{\scriptscriptstyle{3}}$
Н	Х	X	Н	Н	Н	Н
L	L	L	L	Н	Н	Н
L	Н	L	Н	L	Н	Н
L	L	Н	Н	Н	L	Н
L	Н	Н	Н	Н	Н	L

H: High Voltage Level
L: Low Voltage Level

X : Immaterial

Figure a: Gate Functions (each half)



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Condition
Supply voltage	V _{CC}	-0.5 to 7	V	
DC input diode current	I _{IK}	-20	mA	$V_1 = -0.5V$
		20	mA	$V_I = Vcc+0.5V$
DC input voltage	Vı	-0.5 to Vcc+0.5	V	
DC output diode current	I _{OK}	-50	mA	$V_{O} = -0.5V$
		50	mA	$V_O = Vcc+0.5V$
DC output voltage	V _o	-0.5 to Vcc+0.5	V	
DC output source or sink current	Io	±50	mA	
DC V _{CC} or ground current per output pin	I _{CC} , I _{GND}	±50	mA	
Storage temperature	Tstg	-65 to +150	°C	

Recommended Operating Conditions: HD74AC139

Item	Symbol	Ratings	Unit	Condition
Supply voltage	V _{CC}	2 to 6	٧	
Input and output voltage	V _I , V _O	0 to V _{CC}	٧	
Operating temperature	Та	-40 to +85	°C	
Input rise and fall time	tr, tf	8	ns/V	$V_{CC} = 3.0V$
(except Schmitt inputs)				$V_{CC} = 4.5 \text{ V}$
V_{IN} 30% to 70% V_{CC}				V _{CC} = 5.5 V

DC Characteristics: HD74AC139

Item	Sym- bol	Vcc (V)	-	Га = 25°(C	-	–40 to 5°C	Unit	Condition
			min.	typ.	max.	min.	max.		
Input Voltage	V _{IH}	3.0	2.1	1.5	—	2.1	_	٧	$V_{OUT} = 0.1 \text{ V or } V_{CC} - 0.1 \text{ V}$
		4.5	3.15	2.25	—	3.15	_		
		5.5	3.85	2.75		3.85			
	V_{IL}	3.0	_	1.50	0.9	_	0.9		$V_{OUT} = 0.1 \text{ V or } V_{CC} - 0.1 \text{ V}$
		4.5	_	2.25	1.35	_	1.35		
		5.5	_	2.75	1.65	_	1.65		
Output voltage	V _{OH}	3.0	2.9	2.99	_	2.9	_	٧	$V_{IN} = V_{IL}$ or V_{IH}
		4.5	4.4	4.49	_	4.4	_		$I_{OUT} = -50 \mu A$
		5.5	5.4	5.49	_	5.4	_		
		3.0	2.58	_	_	2.48	_		$V_{IN} = V_{IL}$ or V_{IH} $I_{OH} = -12$ mA
		4.5	3.94	_	_	3.80	_		$I_{OH} = -24 \text{ mA}$
		5.5	4.94	_	_	4.80	_		$I_{OH} = -24 \text{ mA}$
	V _{OL}	3.0	_	0.002	0.1	_	0.1		$V_{IN} = V_{IL}$ or V_{IH}
		4.5	_	0.001	0.1	_	0.1		$I_{OUT} = 50 \mu A$
		5.5	_	0.001	0.1	_	0.1		
		3.0	_	_	0.32	_	0.37		$V_{IN} = V_{IL} \text{ or } V_{IH}$ $I_{OL} = 12 \text{ mA}$
		4.5	_	_	0.32	_	0.37		$I_{OL} = 24 \text{ mA}$
		5.5	_	_	0.32	_	0.37		$I_{OL} = 24 \text{ mA}$
Input leakage	I _{IN}	5.5	_	_	±0.1	_	±1.0	μΑ	V _{IN} = V _{CC} or GND
current									
Dynamic output	I _{OLD}	5.5	_	_	_	86	_	mA	V _{OLD} = 1.1 V
current*	I _{OHD}	5.5	_	_		-75	_	mA	$V_{OHD} = 3.85 \text{ V}$
Quiescent supply current	I _{cc}	5.5	_	_	8.0	_	80	μА	$V_{IN} = V_{CC}$ or ground

^{*}Maximum test duration 2.0 ms, one output loaded at a time.

Recommended Operating Conditions: HD74ACT139

Item	Symbol	Ratings	Unit	Condition
Supply voltage	V _{cc}	2 to 6	V	
Input and output voltage	V_{I}, V_{O}	0 to V _{CC}	V	
Operating temperature	Та	-40 to +85	°C	
Input rise and fall time (except Schmitt inputs) V _{IN} 0.8 to 2.0 V	tr, tf	8	ns/V	$V_{CC} = 4.5V$ $V_{CC} = 5.5V$

DC Characteristics: HD74ACT139

Item	Sym- bol	V _{cc} (V)	1	Γa = 25°0	C	Ta = -40 to +85°C		Unit	Condition
			min.	typ.	max.	min.	max.		
Input voltage	V _{IH}	4.5	2.0	1.5	_	2.0	_	٧	V _{OUT} = 0.1 V or Vcc–0.1 V
		5.5	2.0	1.5	_	2.0	_		
	V _{IL}	4.5	_	1.5	0.8	_	0.8		V _{OUT} = 0.1 V or Vcc–0.1 V
		5.5	_	1.5	0.8	_	0.8		
Output voltage	V _{OH}	4.5	4.4	4.49	_	4.4	_	٧	$V_{IN} = V_{IL}$ or V_{IH}
		5.5	5.4	5.49	_	5.4	_		$I_{OUT} = -50 \mu A$
		4.5	3.94	_	_	3.80	_		$V_{IN} = V_{IL}$ $I_{OH} = -24 \text{ mA}$
		5.5	4.94	_	_	4.80	_		$I_{OH} = -24 \text{ mA}$
	V _{OL}	4.5	_	0.001	0.1	_	0.1		$V_{IN} = V_{IL} \text{ or } V_{IH}$
		5.5	_	0.001	0.1	_	0.1		$I_{OUT} = 50 \mu A$
		4.5	_	_	0.32	_	0.37		$V_{IN} = V_{IL}$ $I_{OL} = 24 \text{ mA}$
		5.5	_	_	0.32	_	0.37		$I_{OL} = 24 \text{ mA}$
Input current	I _{IN}	5.5	_	_	±0.1	_	±1.0	μΑ	$V_{IN} = V_{CC}$ or GND
I _{cc} /input current	I _{CCT}	5.5	_	0.6	_	_	1.5	mΑ	$V_{IN} = V_{CC} - 2.1 \text{ V}$
Dynamic output	I _{OLD}	5.5	_	_	_	86	_	mΑ	V _{OLD} = 1.1 V
current*	I _{OHD}	5.5	_	_		-75	_	mA	V _{OHD} = 3.85 V
Quiescent supply current	I _{cc}	5.5	_	_	8.0	_	80	μΑ	$V_{IN} = V_{CC}$ or ground

^{*}Maximum test duration 2.0 ms, one output loaded at a time.

AC Characteristics: HD74AC139

			Ta = +25℃			Ta = -40 °C to +85 °C		
			(C _L = 50 p	F	C _L =	50 pF	
Item	Symbol	V _{cc} (V)*1	Min	Тур	Max	Min	Max	Unit
Propagation delay	t _{PLH}	3.3	1.0	8.0	11.5	1.0	13.0	ns
A_n to \overline{O}_n		5.0	1.0	6.5	8.5	1.0	9.5	
Propagation delay	t _{PHL}	3.3	1.0	7.0	10.0	1.0	11.0	ns
A_n to \overline{O}_n		5.0	1.0	5.5	7.5	1.0	8.5	
Propagation delay	t _{PLH}	3.3	1.0	9.5	12.0	1.0	13.0	ns
\overline{E}_{n} to \overline{O}_{n}		5.0	1.0	7.0	8.5	1.0	10.0	
Propagation delay	t _{PHL}	3.3	1.0	8.0	10.0	1.0	11.0	ns
\overline{E}_{n} to \overline{O}_{n}		5.0	1.0	6.0	7.5	1.0	8.5	

Note: 1. Voltage Range 3.3 is 3.3 V \pm 0.3 V Voltage Range 5.0 is 5.0 V \pm 0.5 V

AC Characteristics: HD74ACT139

			Ta = +25 ℃ C _L = 50 pF				C to +85 ℃ 50 pF	
Item	Symbol	V _{cc} (V)*1	Min	Тур	Max	Min	Max	Unit
Propagation delay A_n to \overline{O}_n	t _{PLH}	5.0	1.0	6.0	8.5	1.0	9.5	ns
Propagation delay A_n to \overline{O}_n	t _{PHL}	5.0	1.0	6.0	9.5	1.0	10.5	ns
Propagation delay \overline{E}_n to \overline{O}_n	t _{PLH}	5.0	1.0	7.0	10.0	1.0	11.0	ns
Propagation delay \overline{E}_n to \overline{O}_n	t _{PHL}	5.0	1.0	7.0	9.5	1.0	10.5	ns

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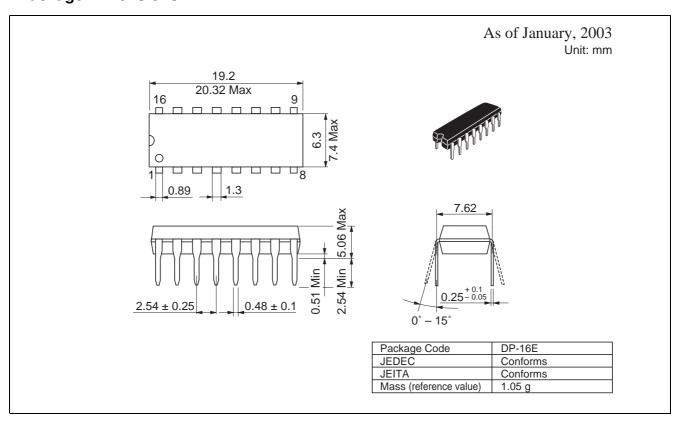
Note: 1. Voltage Range 5.0 is 5.0 V ± 0.5 V

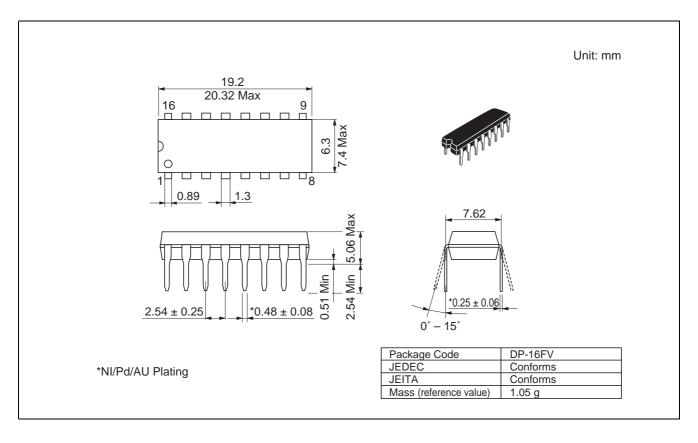
HD74AC139/HD74ACT139

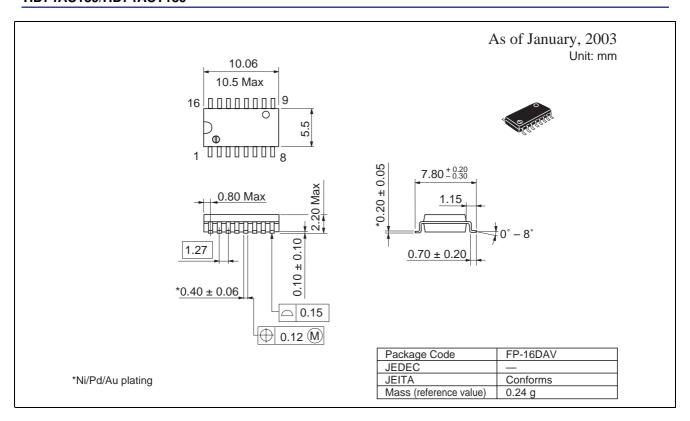
Capacitance

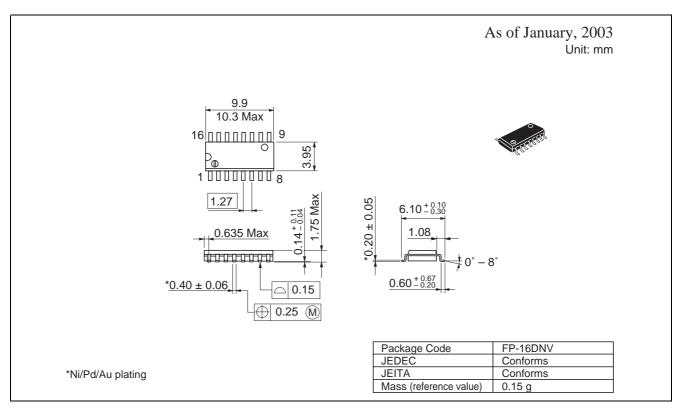
Item	Symbol	Тур	Unit	Condition
Input capacitance	C _{IN}	4.5	pF	V _{CC} = 5.5 V
Power dissipation capacitance	C_{PD}	40.0	pF	$V_{CC} = 5.0 \text{ V}$

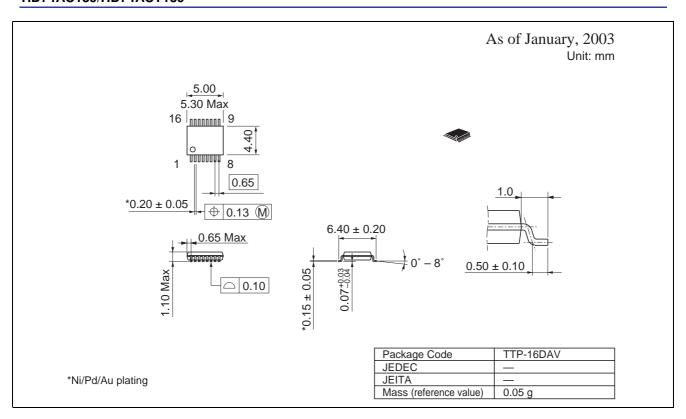
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