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April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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HD74LS02

Quadruple 2-Input Positive NOR Gates

REJ03D0389-0200 Rev.2.00 Feb.18.2005

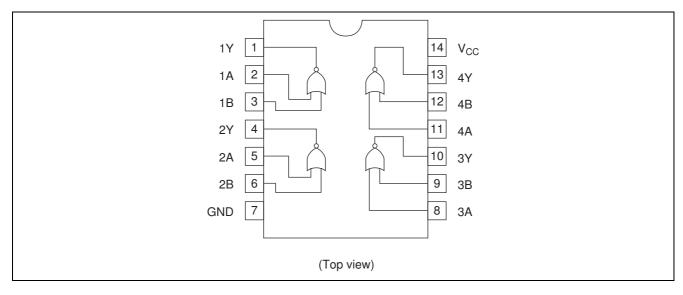
Features

• Ordering Information

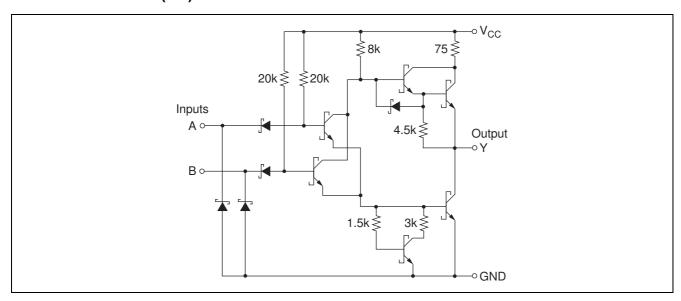
Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LS02P	DILP-14 pin	PRDP0014AB-B (DP-14AV)	Р	_
HD74LS02FPEL	SOP-14 pin (JEITA)	PRSP0014DF-B (FP-14DAV)	FP	EL (2,000 pcs/reel)
HD74LS02RPEL	SOP-14 pin (JEDEC)	PRSP0014DE-A (FP-14DNV)	RP	EL (2,500 pcs/reel)

Note: Please consult the sales office for the above package availability.

Pin Arrangement



Circuit Schematic (1/4)



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit
Supply voltage	V _{CC} Note	7	V
Input voltage	V _{IN}	7	V
Power dissipation	P _T	400	mW
Storage temperature	Tstg	-65 to +150	°C

Note: Voltage value, unless otherwise noted, are with respect to network ground terminal.

Recommended Operating Conditions

Item	Symbol	Min	Тур	Max	Unit
Supply voltage	V_{CC}	4.75	5.00	5.25	V
Output ourrant	I _{OH}	_	_	-400	μΑ
Output current	I _{OL}	_	_	8	mA
Operating temperature	Topr	-20	25	75	°C

Electrical Characteristics

 $(Ta = -20 \text{ to } +75 \, ^{\circ}\text{C})$

Item	Symbol	min.	typ.*	max.	Unit	Condition
Input voltage	V_{IH}	2.0	_	_	V	
input voitage	V_{IL}	_	_	0.8	V	
	V_{OH}	2.7	_	_	V	$V_{CC} = 4.75 \text{ V}, V_{IL} = 0.8 \text{ V}, I_{OH} = -400 \mu\text{A}$
Output voltage	V _{OL}		—	0.5	V	$I_{OL} = 8 \text{ mA}$ $V_{CC} = 4.75 \text{ V}, V_{IH} = 2 \text{ V}$
		_	—	0.4	V	$I_{OL} = 4 \text{ mA}$ $V_{CC} = 4.73 \text{ V}, \text{ V}_{H} = 2 \text{ V}$
	l _{iH}	_	—	20	μΑ	$V_{CC} = 5.25 \text{ V}, V_{I} = 2.7 \text{ V}$
Input current	I _{IL}	_	—	-0.4	mA	$V_{CC} = 5.25 \text{ V}, V_I = 0.4 \text{ V}$
	I _I	_	—	0.1	mA	$V_{CC} = 5.25 \text{ V}, V_I = 7 \text{ V}$
Short-circuit output current	los	-20	_	-100	mA	V _{CC} = 5.25 V
Supply current	Іссн	_	1.6	3.2	mA	V _{CC} = 5.25 V
	I _{CCL}	_	2.8	5.4	mA	V _{CC} = 5.25 V
Input clamp voltage	V_{IK}	_	_	-1.5	V	$V_{CC} = 4.75 \text{ V}, I_{IN} = -18 \text{ mA}$

Note: $^* V_{CC} = 5 V$, $Ta = 25 ^{\circ}C$

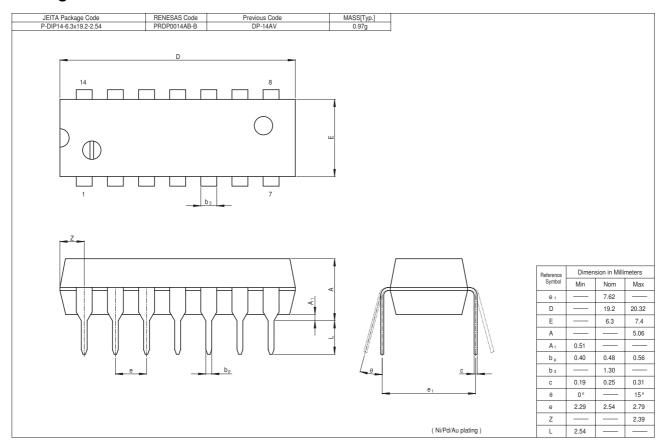
Switching Characteristics

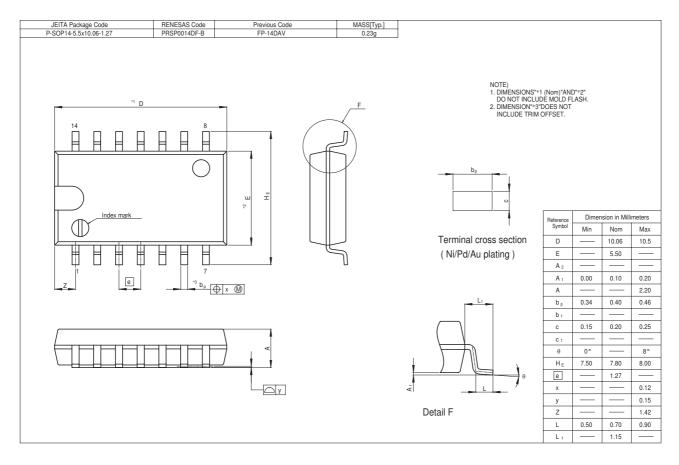
 $(V_{CC} = 5 \text{ V}, \text{Ta} = 25^{\circ}\text{C})$

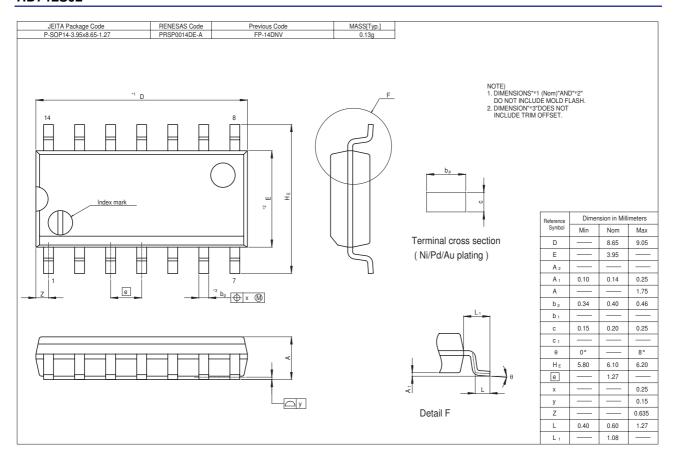
Item	Symbol	min.	typ.	max.	Unit	Condition
Propagation delay time	t _{PLH}	_	10	15	ns	$C_L = 15 \text{ pF}, R_L = 2 \text{ k}\Omega$
	t _{PHL}	_	10	15	ns	$C_L = 15 \text{ pr}, R_L = 2 \text{ K}\Omega$

Note: Refer to Test Circuit and Waveform of the Common Item "TTL Common Matter (Document No.: REJ27D0005-0100)".

Package Dimensions







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