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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<u>http://www.renesas.com</u>)

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HD74LS09

Quadruple 2-Input Positive AND Gates (with Open Collector Outputs)

REJ03D0395-0200 Rev.2.00 Feb.18.2005

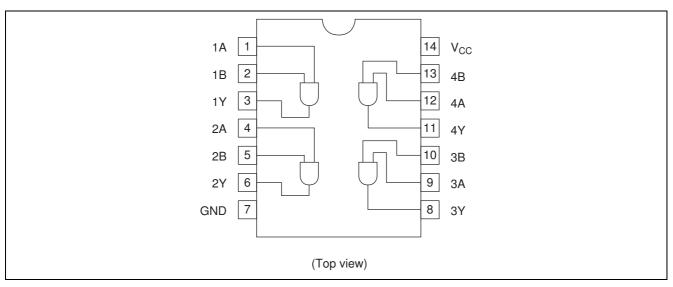
Features

• Ordering Information

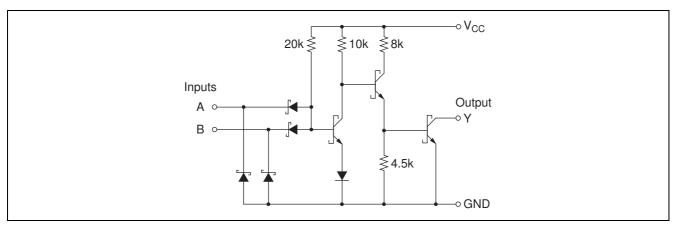
| Part Name | Package Type | Package Code (Previous Code) | Package Abbreviation | Taping Abbreviation (Quantity) |
|--------------|--------------------|---------------------------------|-------------------------|-----------------------------------|
| HD74LS09P | DILP-14 pin | PRDP0014AB-B (DP-14AV) | Ρ | — |
| HD74LS09FPEL | SOP-14 pin (JEITA) | PRSP0014DF-B (FP-14DAV) | FP | EL (2,000 pcs/reel) |

Note: Please consult the sales office for the above package availability.

Pin Arrangement



Circuit Schematic (1/4)





Absolute Maximum Ratings

| Item | Symbol | Ratings | Unit |
|---------------------|----------------------|-------------|------|
| Supply voltage | V _{CC} Note | 7 | V |
| Input voltage | V _{IN} | 7 | V |
| Power dissipation | PT | 400 | mW |
| Storage temperature | Tstg | -65 to +150 | °C |

Note: Voltage value, unless otherwise noted, are with respect to network ground terminal.

Recommended Operating Conditions

| Item | Symbol | Min | Тур | Max | Unit |
|-----------------------|-----------------|------|------|------|------|
| Supply voltage | V _{CC} | 4.75 | 5.00 | 5.25 | V |
| Output voltage | V _{OH} | — | — | 5.5 | V |
| Output current | I _{OL} | — | — | 8 | mA |
| Operating temperature | Topr | -20 | 25 | 75 | °C |



Electrical Characteristics

| | | | | | | $(Ta = -20 \text{ to } +75 ^{\circ}\text{C})$ |
|---------------------|------------------|------|-------|------|------|---|
| Item | Symbol | min. | typ.* | max. | Unit | Condition |
| Input voltage | VIH | 2.0 | — | — | V | |
| | VIL | — | _ | 0.8 | V | |
| Output valtage | V _{OL} | | — | 0.5 | V | $I_{OL} = 8 \text{ mA}$ $V_{CC} = 4.75 \text{ V}, \text{ V}_{IH} = 0.8 \text{ V}$ |
| Output voltage | VOL | _ | — | 0.4 | | $I_{OL} = 4 \text{ mA}$ $V_{CC} = 4.75 \text{ V}, \text{ V}_{H} = 0.8 \text{ V}$ |
| Output current | I _{OH} | _ | — | 100 | μA | $V_{CC} = 4.75 \ V, \ V_{IH} = 2 \ V, \ V_{OH} = 5.5 \ V$ |
| | I _{IH} | _ | — | 20 | μA | $V_{CC} = 5.25 \text{ V}, \text{ V}_{I} = 2.7 \text{ V}$ |
| Input current | IIL | _ | — | -0.4 | mA | $V_{CC} = 5.25 \text{ V}, \text{ V}_{I} = 0.4 \text{ V}$ |
| | l _l | _ | — | 0.1 | mA | $V_{CC} = 5.25 V, V_1 = 7 V$ |
| Supply current | I _{CCH} | — | 2.4 | 4.8 | mA | $V_{CC} = 5.25 V$ |
| | ICCL | _ | 4.4 | 8.8 | mA | $V_{CC} = 5.25 V$ |
| Input clamp voltage | VIK | _ | | -1.5 | V | $V_{CC} = 4.75 \text{ V}, I_{IN} = -18 \text{ mA}$ |

Note: $V_{CC} = 5 \text{ V}, \text{ Ta} = 25 ^{\circ}\text{C}$

Switching Characteristics

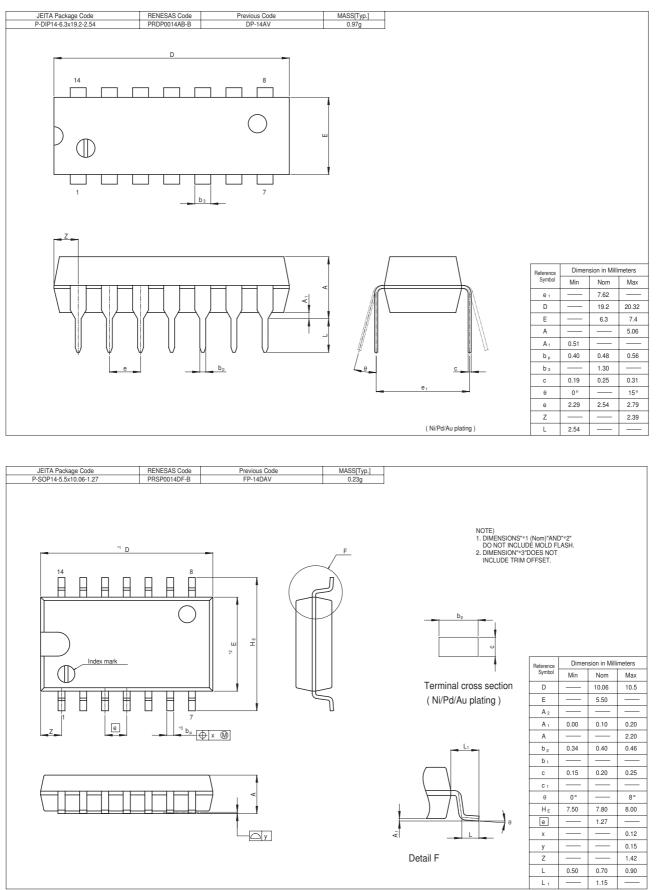
 $(V_{CC} = 5 V, Ta = 25^{\circ}C)$

| | | | | | | (|
|------------------------|------------------|------|------|------|------|--|
| Item | Symbol | min. | typ. | max. | Unit | Condition |
| Propagation delay time | t _{PLH} | — | 20 | 35 | ns | $C_{L} = 15 \text{ pF}, R_{L} = 2 \text{ k}\Omega$ |
| | t _{PHL} | _ | 17 | 35 | ns | $G_{L} = 15 \text{ pr}, \text{ R}_{L} = 2 \text{ K}\Omega$ |

Note: Refer to Test Circuit and Waveform of the Common Item "TTL Common Matter (Document No.: REJ27D0005-0100)".



Package Dimensions





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