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# **HD74LS123**

# Dual Retriggerable Monostable Multivibrators (with Clear)

REJ03D0429-0200 Rev.2.00 Feb.18.2005

This d-c triggered multivibrator features output pulse width control by three method. The basic pulse time is programmed by selection of external resistance and capacitance values. Once triggered, the basic pulse width may be extended by retriggering the gated low-level -active (A) or high-level active (B) inputs, or be reduced by use of the overriding clear. Figure 1 illustrates pulse control by retriggering and early clear. This device is provided enough Schmitt hysteresis to ensure jitter-free triggering from the B input with transition rates as slow as 0.1 mV/ns.

#### **Features**

• Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LS123P	DILP-16 pin	PRDP0016AE-B (DP-16FV)	Р	_
HD74LS123FPEL	SOP-16 pin (JEITA)	PRSP0016DH-B (FP-16DAV)	FP	EL (2,000 pcs/reel)
HD74LS123RPEL	SOP-16 pin (JEDEC)	PRSP0016DG-A (FP-16DNV)	RP	EL (2,500 pcs/reel)

Note: Please consult the sales office for the above package availability.

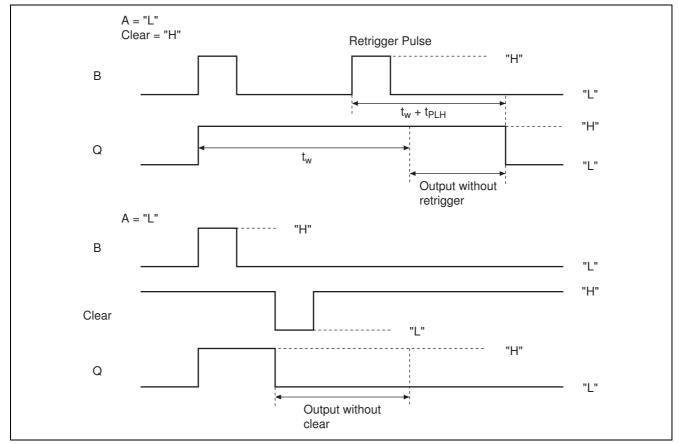
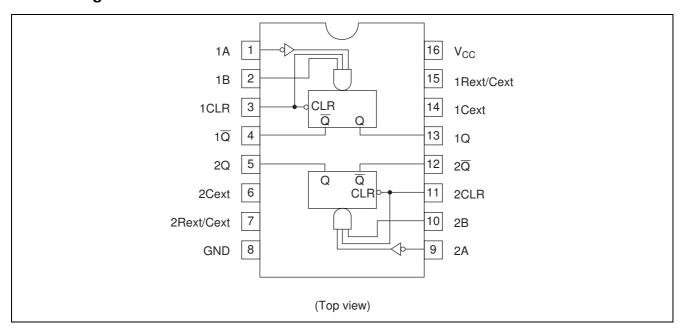


Figure 1 Typical Input / Output Pulse

#### **Pin Arrangement**



#### **Function Table**

	Inputs	Outputs			
Clear	<b>A</b> <sub>1</sub>	B <sub>2</sub>	Q	Q	
L	X	X	L	Н	
Х	Н	X	L	Н	
Х	X	L	L	Н	
Н	L	1	Л	ъ	
Н	<b>↓</b>	Н	Л	ъ	
1	L	Н	л	ъ	

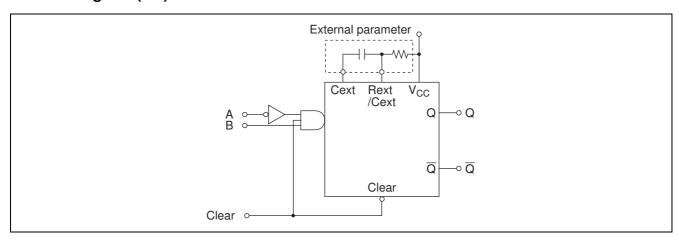
Notes: H; high level, L; low level, X; irrelevant

1; transition from low to high level

 $\downarrow$ ; transition from high to low level

」☐; one high-level pulse ☐; one low-level pulse

# Block Diagram (1/2)



## **Absolute Maximum Ratings**

Item	Symbol	Ratings	Unit
Supply voltage	V <sub>CC</sub>	7	V
Input voltage	$V_{IN}$	7	V
Power dissipation	P <sub>T</sub>	400	mW
Storage temperature	Tstg	-65 to +150	°C

Note: Voltage value, unless otherwise noted, are with respect to network ground terminal.

### **Recommended Operating Conditions**

Item			Symbol	Min	Тур	Max	Unit
Supply voltage			Vcc	4.75	5.00	5.25	V
Output current			I <sub>OH</sub>	_	_	-400	μΑ
			l <sub>OL</sub>	_	_	8	mA
Operating temperature	Operating temperature			-20	25	75	°C
	A, B	"H"	t <sub>w (in)</sub>	40	_	_	ns
Input pulse width	Α, Β	"L"		40	_	_	ns
	CLR	"L"		40	_	_	ns
External timing resistance R <sub>ext</sub>			5	_	260	kΩ	
External capacitance			C <sub>ext</sub>	Non restriction			
Wiring capacitance at I	/iring capacitance at Rext/Cext terminal — — 50				pF		

#### **Electrical Characteristics**

 $(Ta = -20 \text{ to } +75 \, ^{\circ}\text{C})$ 

Item	Symbol	min.	typ.*	max.	Unit	Condition
Input voltage	V <sub>IH</sub>	2.0	_	_	V	
Input voltage	V <sub>IL</sub>	_	_	0.8	V	
Output valtage	V <sub>OH</sub>	2.7			<b>V</b>	$\begin{aligned} V_{CC} &= 4.75 \text{ V}, \text{ V}_{IH} = 2 \text{ V}, \text{ V}_{IL} = 0.8 \text{ V}, \\ I_{OH} &= -400  \mu\text{A} \end{aligned}$
Output voltage	V <sub>OL</sub>	_		0.4	V	$I_{OL} = 4 \text{ mA}$ $V_{CC} = 4.75 \text{ V}, V_{IH} = 2 \text{ V},$
		_	_	0.5	7 v [	$I_{OL} = 8 \text{ mA}$ $V_{IL} = 0.8 \text{ V}$
	I <sub>IH</sub>	_	_	20	μΑ	$V_{CC} = 5.25 \text{ V}, V_{I} = 2.7 \text{ V}$
Input current	I <sub>IL</sub>	_	_	-0.4	mA	$V_{CC} = 5.25 \text{ V}, V_{I} = 0.4 \text{ V}$
	I <sub>I</sub>	_	_	0.1	mA	$V_{CC} = 5.25 \text{ V}, V_{I} = 7 \text{ V}$
Short-circuit output current	I <sub>OS</sub>	-20	_	-100	mA	V <sub>CC</sub> = 5.25 V
Supply current**	I <sub>CC</sub>	_	12	20	mA	V <sub>CC</sub> = 5.25 V
Input clamp voltage	$V_{IK}$	_		-1.5	٧	$V_{CC} = 4.75 \text{ V}, I_{IN} = -18 \text{ mA}$

<sup>\*</sup>  $V_{CC}$  = 5 V, Ta = 25  $^{\circ}$ C

Note: To measure  $V_{OH}$  at Q,  $V_{OL}$  at  $\overline{Q}$ , or  $I_{OS}$  at Q, ground  $R_{ext}$  /  $C_{ext}$ , apply 2 V to B and clear, and pulse A from 2 V to 0 V.

<sup>\*\*</sup> With all outputs open and 4.5 V applied to all data and clear inputs, I<sub>CC</sub> is measured after a momentary ground, then 4.5 V, is applied to clock.

#### **Switching Characteristics**

1	100	_	5	V	$T_{a}$	_	25°	C
( )	CC.	_	J	٧,	1 a	_	23	C)

Item	Symbol	Inputs	Outputs	min.	typ.	max.	Unit	Condition
	$t_{PLH}$	Α	Q		23	33	ns	$C_{ext} = 0, \ R_{ext} = 5 \ k\Omega,$ $C_L = 15 \ pF, \ R_L = 2 \ k\Omega$ $C_{ext} = 1000 \ pF,$
	$t_{PHL}$	А	Q		32	45		
Propagation delay	t <sub>PLH</sub>	В	Q	1	23	44		
time	t <sub>PHL</sub>		Q	1	34	56		
	t <sub>PLH</sub>	CLR	Q	1	20	27		
	t <sub>PHL</sub>		Q	1	28	45		
	t <sub>(out)min</sub>	A, B	Q	1	116	200	μs	
Output pulse width	t <sub>(out)</sub>		Q	4	4.5	5		$R_{ext} = 10 \text{ k}\Omega,$ $C_L = 15 \text{ pF}, R_L = 2 \text{ k}\Omega$

## **Typical Application Data for HD74LS123**

For pulse widths when  $C_{ext} \le 1000$  pF, See Figure 3.

The output pulse is primarily a function of the external capacitor and resistor. For  $C_{ext} > 1000$  pF, the output pulse width  $(t_w)$  is defined as:  $t_{w(out)} = K \bullet R_{ext} \bullet C_{ext}$ ; See Figure 4.

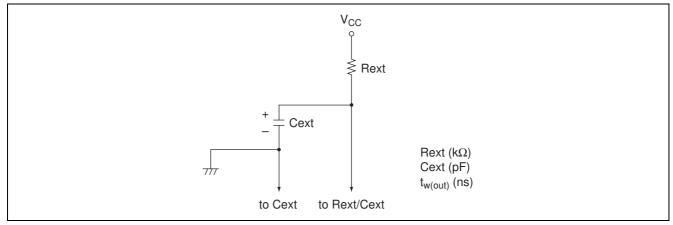


Figure 2 Timing Component Connections

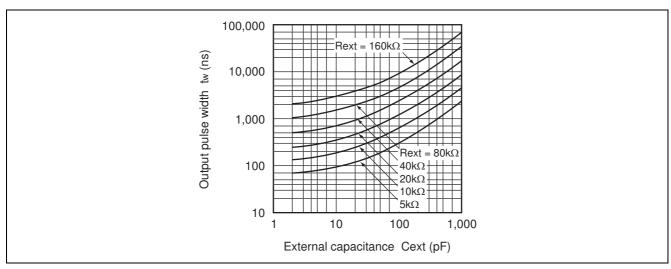


Figure 3 Typical Output Pulse Width (Cext ≤ 1000 pF)

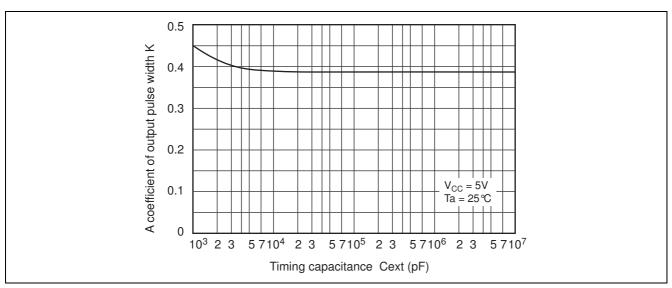
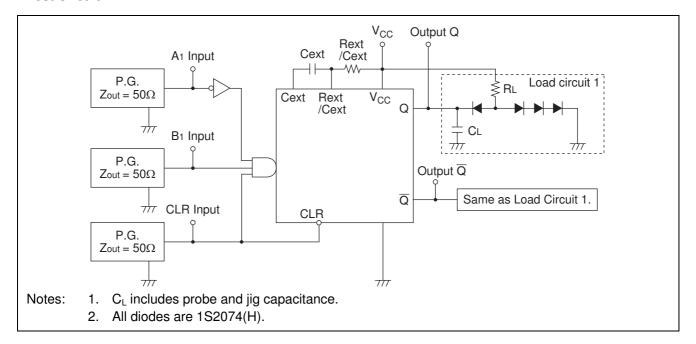


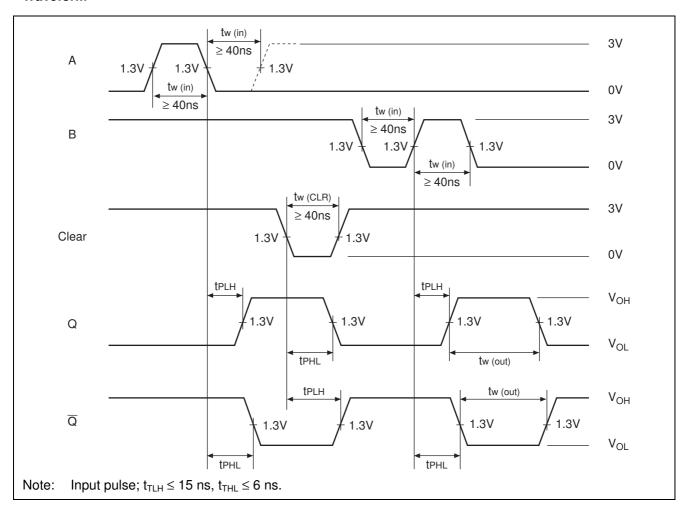
Figure 4  $C_{ext}$  vs. K ( $C_{ext} > 1000 pF$ )

#### **Testing Method**

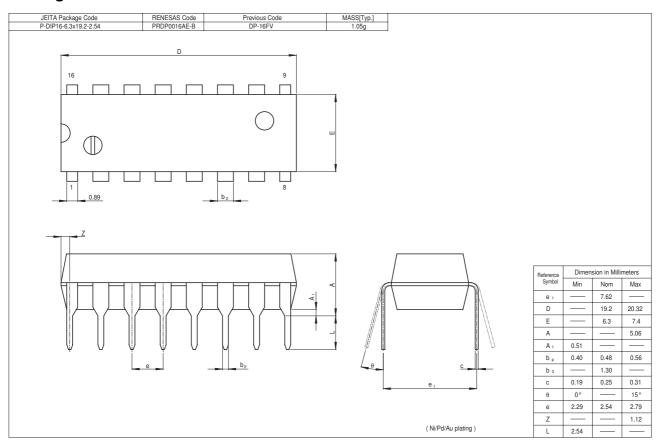
#### **Test Circuit**

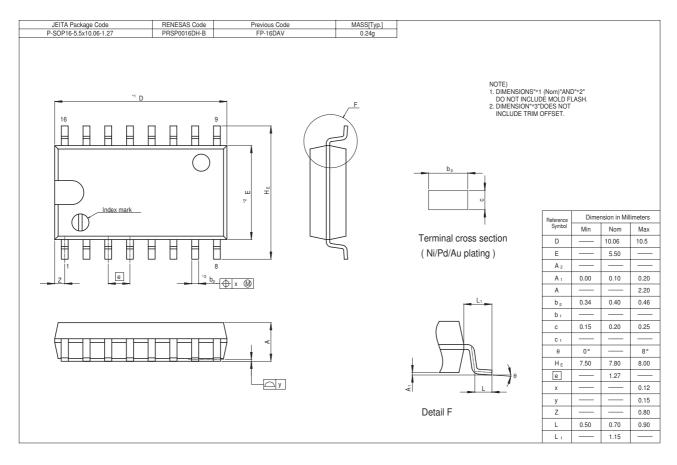


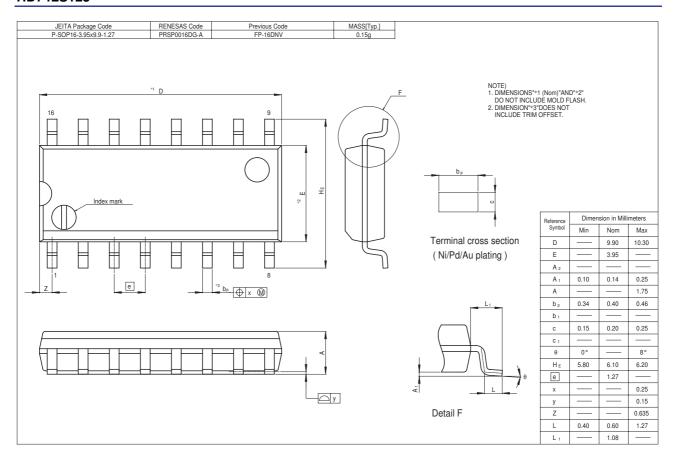
#### Waveform



### **Package Dimensions**







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