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April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

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# **HD74LS132**

## Quadruple 2-Input Positive NAND Schmitt-triggers

REJ03D0432-0300 Rev.3.00 Jul.13.2005

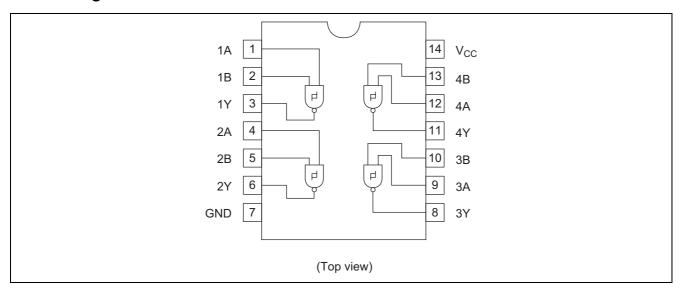
#### **Features**

• Ordering Information

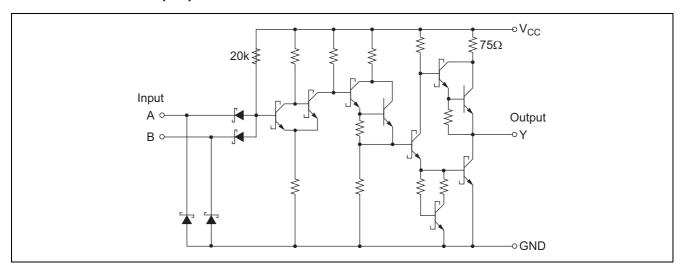
Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LS132P	DILP-14 pin	PRDP0014AB-B (DP-14AV)	Р	_
HD74LS132FPEL	SOP-14 pin (JEITA)	PRSP0014DF-B (FP-14DAV)	FP	EL (2,000 pcs/reel)
HD74LS132RPEL	SOP-14 pin (JEDEC)	PRSP0014DE-A (FP-14DNV)	RP	EL (2,500 pcs/reel)

Note: Please consult the sales office for the above package availability.

### **Pin Arrangement**



## Circuit Schematic (1/4)



## **Absolute Maximum Ratings**

Item	Symbol	Ratings	Unit	
Supply voltage	V <sub>CC</sub>	7	V	
Input voltage	V <sub>IN</sub>	7	V	
Power dissipation	P <sub>T</sub>	400	mW	
Storage temperature	Tstg	-65 to +150	∞	

## **Recommended Operating Conditions**

Item	Symbol	Min	Тур	Max	Unit
Supply voltage	$V_{CC}$	4.75	5.00	5.25	V
Output current	I <sub>OH</sub>			-400	μΑ
Output current	I <sub>OL</sub>		_	8	mA
Operating temperature	Topr	-20	25	75	°C

### **Electrical Characteristics**

 $(Ta = -20 \text{ to } +75 \text{ }^{\circ}\text{C})$ 

Item	Symbol	min.	typ.*	max.	Unit	Condition
Input threshold	$V_T^+$	1.4	1.6	1.9	V	V <sub>CC</sub> = 5 V
voltage	V <sub>T</sub> <sup>-</sup>	0.5	0.7	1.0	V	$V_{CC} = 5 \text{ V}$
Hysteresis	$V_T^+ - V_T^-$	0.4	0.9	_	V	$V_{CC} = 5 \text{ V}$
	V <sub>OH</sub>	2.7	_	_	V	$V_{CC} = 4.75 \text{ V}, V_{I} = 0.5 \text{ V}, I_{OH} = -400 \mu\text{A}$
Output voltage	V <sub>OL</sub>	_	_	0.5	V	l <sub>OL</sub> = 8 mA V <sub>CC</sub> = 4.75 V, V <sub>I</sub> = 1.9 V
	VOL	_	_	0.4	V	$I_{OL} = 4 \text{ mA}$ $V_{CC} = 4.75 \text{ V}, \text{ V} = 1.9 \text{ V}$
Input threshold	I <sub>T</sub> +	_	-0.14	_	mA	$V_{CC} = 5 \text{ V}, \text{ V}_{I} = \text{V}_{T}^{+}$
current	I <sub>T</sub> -	_	-0.18	_	mA	$V_{CC} = 5 \text{ V}, \text{ V}_{I} = \text{V}_{T}^{-}$
	I <sub>IH</sub>	_	_	20	μΑ	$V_{CC} = 5.25 \text{ V}, V_{I} = 2.7 \text{ V}$
Input current	I <sub>IL</sub>	_	_	-0.4	mA	$V_{CC} = 5.25 \text{ V}, V_I = 0.4 \text{ V}$
	I <sub>I</sub>	_	_	0.1	mA	$V_{CC} = 5.25 \text{ V}, V_{I} = 7 \text{ V}$
Short-circuit output current	los	-20	_	-100	mA	V <sub>CC</sub> = 5.25 V
Supply ourrent	I <sub>CCH</sub>	_	5.9	11	mA	V <sub>CC</sub> = 5.25 V
Supply current	I <sub>CCL</sub>	_	8.2	14	mA	V <sub>CC</sub> = 5.25 V
Input clamp voltage	V <sub>IK</sub>		_	-1.5	V	$V_{CC} = 4.75 \text{ V}, I_{IN} = -18 \text{ mA}$

Note:  $^*V_{CC} = 5 \text{ V}$ ,  $Ta = 25 ^{\circ}\text{C}$ 

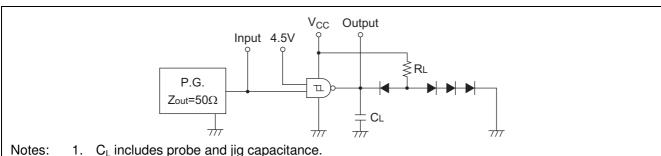
### **Switching Characteristics**

 $(V_{CC} = 5 \text{ V}, \text{Ta} = 25^{\circ}\text{C})$ 

Item	Symbol	min.	typ.	max.	Unit	Condition
Propagation delay time	t <sub>PLH</sub>	_	15	22	ns	$C_L = 15 \text{ pF}, R_L = 2 \text{ k}\Omega$
	t <sub>PHL</sub>	_	15	22	ns	$C_L = 15  \text{pr},  R_L = 2  \text{K}  \text{M}_L = 2  $

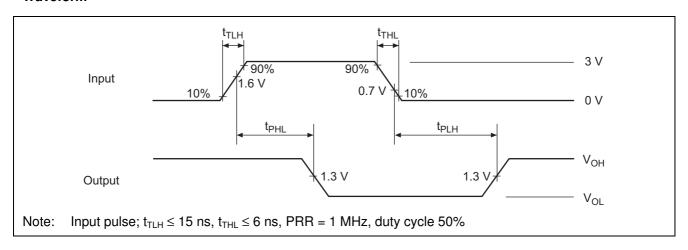
### **Testing Method**

#### **Test Circuit**

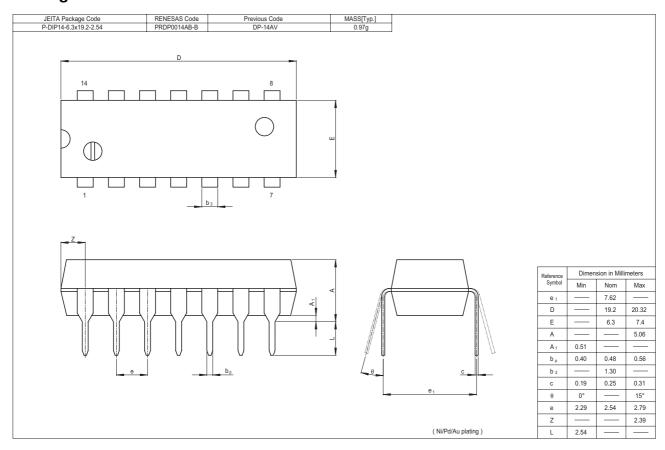


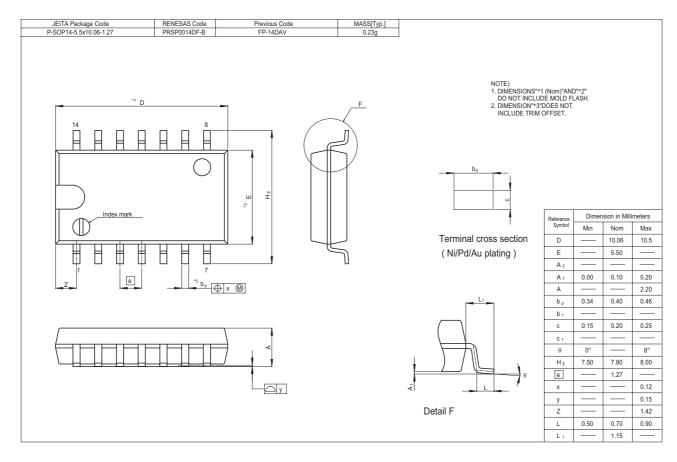
- 1.  $C_L$  includes probe and jig capacitance.
  - 2. All diodes are 1S2074(H).

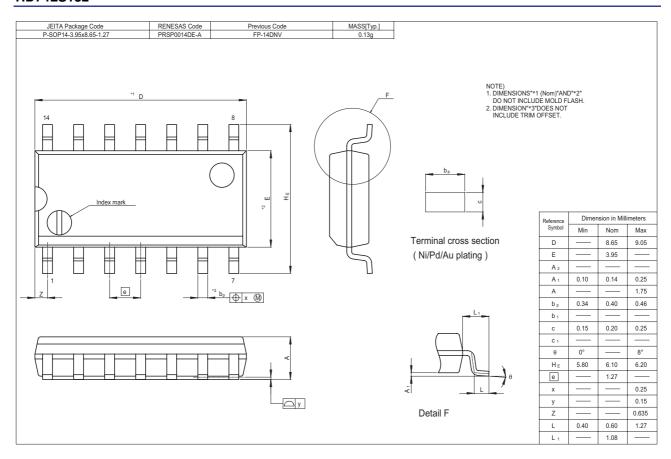
#### Waveform



### **Package Dimensions**







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