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April 1st, 2010 Renesas Electronics Corporation

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HD74LV123A

Dual Retriggerable Monostable Multivibrators

REJ03D0314-0600Z (Previous ADE-205-258D (Z)) Rev.6.00 Jun. 02, 2004

Description

The HD74LV123A features output pulse-duration control by three methods. In the first method, the A input is low and the B input goes high. In the second method, the B input is high and the \overline{A} input goes low. In the third method, the \overline{A} input is low, the B input is high, and the clear (\overline{CLR}) input goes high.

The basic pulse duration is programmed by selecting external resistance and capacitance values.

The external timing capacitor must be connected between Cext and Rext/Cext (positive) and an external resistor connected between Rext/Cext and Vcc

To obtain variable pulse durations, connect an external variable resistance between Rext/Cext and Vcc.

Once triggered, the basic pulse duration can be extended by retriggering the gated low-level-active (\overline{A}) or high-level-active (B) input. Pulse duration can be reduced by taking \overline{CLR} low.

Features

- $V_{CC} = 2.0 \text{ V}$ to 5.5 V operation
- All inputs V_{IH} (Max.) = 5.5 V (@ V_{CC} = 0 V to 5.5 V)
- All outputs V_0 (Max.) = 5.5 V (@V_{CC} = 0 V)
- Output current ± 6 mA (@V_{CC} = 3.0 V to 3.6 V), ± 12 mA (@V_{CC} = 4.5 V to 5.5 V)
- Ordering Information

Part Name	Package Type	Package Code	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LV123AFPEL	SOP-16 pin(JEITA)	FP-16DAV	FP	EL (2,000 pcs/reel)
HD74LV123ARPEL	SOP-16 pin(JEDEC)	FP-16DNV	RP	EL (2,500 pcs/reel)
HD74LV123ATELL	TSSOP-16 pin	TTP-16DAV	Т	ELL (2,000 pcs/reel)

Note: Please consult the sales office for the above package availability.

Function Table

Inputs			Outputs		
CLR	Ā	В	Q	Q	
L	Χ	X	L	Н	
Н	Н	X	L	Н	
Н	Χ	L	L	Н	
Н	L	↑	Λ	ν	
Н	\downarrow	Н	Λ	ν	
\uparrow	L	Н	V	ν	

Note: H: High level

L: Low level

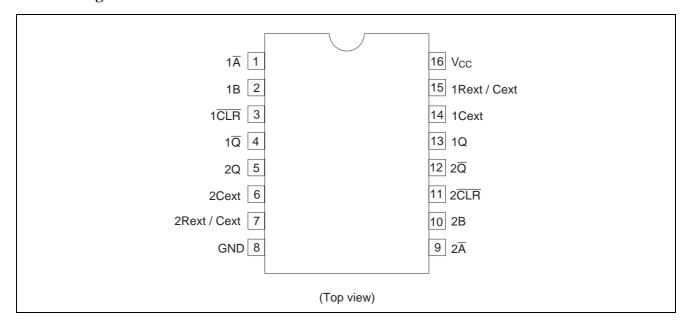
X: Immaterial

↑: Low to high transition

↓: High to low transition

T: Low level pulse

Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Conditions
Supply voltage range	Vcc	-0.5 to 7.0	V	
Input voltage range*1	VI	-0.5 to 7.0	V	
Output voltage range*1, 2	Vo	-0.5 to $V_{CC} + 0.5$	V	Output: H or L
		-0.5 to 7.0		V _{CC} : OFF
Input clamp current	I _{IK}	-20	mA	V _I < 0
Output clamp current	l _{ok}	±50	mA	$V_O < 0$ or $V_O > V_{CC}$
Continuous output current	I _O	±25	mA	$V_{O} = 0$ to V_{CC}
Continuous current through	I _{CC} or I _{GND}	±50	mA	
V _{CC} or GND				
Maximum power dissipation at	P _T	785	mW	SOP
Ta = 25 $^{\circ}$ C (in still air) *3		500		TSSOP
Storage temperature	Tstg	-65 to 150	℃	

Notes: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

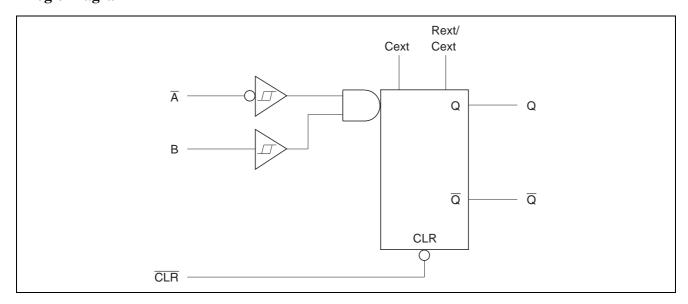
- 1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.
- 2. This value is limited to 5.5 V maximum.
- 3. The maximum package power dissipation was calculated using a junction temperature of 150 ℃.

Recommended Operating Conditions

Item	Symbol	Min	Тур	Max	Unit	Conditions
Supply voltage range	V _{CC}	2.0	_	5.5	V	
Input voltage range	Vı	0	_	5.5	V	
Output voltage range	Vo	0	_	V _{CC}	V	
Output current	I _{OH}	_	_	- 50	μΑ	V _{CC} = 2.0 V
		_	_	- 2	mA	$V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$
		_	_	- 6		V _{CC} = 3.0 to 3.6 V
		_	_	-12		V _{CC} = 4.5 to 5.5 V
	I _{OL}	_	_	50	μΑ	V _{CC} = 2.0 V
		_	_	2	mA	$V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$
		_	_	6		V _{CC} = 3.0 to 3.6 V
		_	_	12		V _{CC} = 4.5 to 5.5 V
Input transition rise or fall rate	Δt /Δν	0	_	200	ns/V	$V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$
		0	_	100		V _{CC} = 3.0 to 3.6 V
		0	_	20		V _{CC} = 4.5 to 5.5 V
External timing resistance	Rext	5	_	_	kΩ	V _{CC} = 2.0 V
		1	_	_		V _{CC} ≥ 2.3 V
External timing capacitance	Cext	_	Unlimited	_	F	
Power-up ramp rate	$\Delta t / \Delta V_{CC}$	1	_	_	ms/	
 					V	
Operating free-air temperature	Та	-40	_	85	℃	

Note: Unused or floating inputs must be held high or low.

Logic Diagram



DC Electrical Characteristics

 $Ta = -40 \text{ to } 85^{\circ}C$

Item	Symbol	V _{CC} (V)*	Min	Тур	Max	Unit	Test Conditions
Input voltage	V _{IH}	2.0	1.5	_	_	V	
		2.3 to 2.7	$V_{CC} \times 0.7$	_	_	_	
		3.0 to 3.6	$V_{CC} \times 0.7$	_	_	_	
		4.5 to 5.5	$V_{CC} \times 0.7$	_	_	_	
	V _{IL}	2.0	_	_	0.5		
		2.3 to 2.7	_	_	$V_{\text{CC}} \times 0.3$		
		3.0 to 3.6	_	_	$V_{\text{CC}} \times 0.3$		
		4.5 to 5.5	_	_	$V_{\text{CC}} \times 0.3$		
Output voltage	V _{OH}	Min to Max	V _{CC} - 0.1	_	_	V	$I_{OH} = -50 \mu\text{A}$
		2.3	2.0	_	_		$I_{OH} = -2 \text{ mA}$
		3.0	2.48	_	_		$I_{OH} = -6 \text{ mA}$
		4.5	3.8	_	_		$I_{OH} = -12 \text{ mA}$
	V _{OL}	Min to Max	_	_	0.1		$I_{OL} = 50 \mu\text{A}$
		2.3	_	_	0.4		I _{OL} = 2 mA
		3.0	_	_	0.44		I _{OL} = 6 mA
		4.5	_	_	0.55		I _{OL} = 12 mA
Input current	I _{IN}	0 to 5.5	_	_	±1	μΑ	V _{IN} = 5.5 V or GND
Input current	I _{IN}	5.5	_	_	±2.5	μΑ	$V_{IN} = V_{CC}$ or GND
Rext / Cext							
Quiescent supply current	I _{CC}	5.5	_	_	20	μΑ	$V_{IN} = V_{CC}$ or GND, $I_O = 0$
Active state supply	ΔI_{CC}	2.3	_	_	220	μΑ	$V_{IN} = V_{CC}$ or GND
current		3.0	_	_	280	_	Rext/Cext = 0.5 V _{CC}
(per circuit)		4.5	_	_	650	_	
		5.5	_	_	975	_	
Output leakage	I _{OFF}	0	_	_	5	μΑ	V_{I} or $V_{O} = 0 \text{ V to } 5.5 \text{ V}$
current							
Input capacitance	C_{IN}	3.3	_	4.0	_	pF	$V_I = V_{CC}$ or GND

Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.

Switching Characteristics

 $V_{\rm CC}=2.5\pm0.2~\rm V$

		Ta = :	25℃		Ta = -40) to 85℃		Test	FROM	то
Item	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	13.5	31.4	1.0	37.0	ns	C _L = 15 pF	A or B	Q or \overline{Q}
delay time	t_PHL	_	16.0	36.0	1.0	42.0		$C_L = 50 pF$		
		_	11.0	25.0	1.0	29.5		$C_L = 15 pF$	CLR	Q or \overline{Q}
		_	13.0	32.8	1.0	34.5		$C_L = 50 pF$		
		_	14.0	33.4	1.0	39.0		$C_L = 15 pF$	CLR	Q or \overline{Q}
		_	16.0	38.0	1.0	44.0	_	$C_L = 50 pF$	(Trigger)	
Output pulse	t_{wQ}	_	170	260	_	320	ns	$C_L = 50 \text{ pF, Ce}$	xt = 28 pF, l	Rext = $2 k\Omega$
width		90	100	110	90	110	μs	$C_L = 50 pF$,		
								Cext = 0.01 μF	, Rext = 10	kΩ
		0.9	1.0	1.1	0.9	1.1	ms	$C_L = 50 pF$,		
								Cext = $0.1 \mu F$,	Rext = 10 k	(Ω
	Δt_{wQ}	_	±1	_	_	_	%	$C_L = 50 pF$		
Pulse width	t _w	6.0	_	_	6.5	_	ns	\overline{A} , B or \overline{CLR}		
Retrigger time	t _{rr}	_	40	_	_	_	ns	\overline{A} , or B		
								(Rext = 1 kΩ, C	ext = 100 p	F)
		_	1.5	_	_	_	μs	\overline{A} , or B		
								(Rext = 1 kΩ, C	Cext = 0.01 µ	ιF)

 $V_{\rm CC}=3.3\pm0.3~\rm V$

		Ta =	25℃		Ta = -4	0 to 85℃		Test	FROM	то
Item	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	9.7	20.6	1.0	24.0	ns	C _L = 15 pF	A or B	Q or Q
delay time	t _{PHL}	_	11.5	24.1	1.0	27.5		$C_L = 50 pF$		
		_	8.0	15.8	1.0	18.5	<u> </u>	C _L = 15 pF	CLR	Q or Q
		_	9.5	19.3	1.0	22.0	<u> </u>	C _L = 50 pF	<u> </u>	
		_	9.9	22.4	1.0	26.0	<u> </u>	C _L = 15 pF	CLR	Q or Q
		_	11.5	25.9	1.0	29.5	<u> </u>	C _L = 50 pF	(Trigger)	
Output pulse	t _{wQ}	_	150	240	_	300	ns	$C_L = 50 \text{ pF}, \text{ Cext} = 28 \text{ pF}, \text{ Rext} = 2 \text{ k}\Omega$		
width		90	100	110	90	110	μs	$C_L = 50 pF$,		
								$Cext = 0.01 \mu F$, Rext = 10	kΩ
		0.9	1.0	1.1	0.9	1.1	ms	$C_L = 50 pF$,		
								Cext = 0.1 μ F,	Rext = 10 k	Ω
	Δt_{wQ}	_	±1	_	_	_	%	$C_L = 50 pF$		
Pulse width	t _w	5.0	_	_	5.0	_	ns	\overline{A} , B or \overline{CLR}		
Retrigger time	t _{rr}	_	30	_	_	_	ns	\overline{A} , or B		
								(Rext = 1 kΩ, C	ext = 100 p	oF)
		_	1.2	_	_	_	μs	\overline{A} , or B		
								(Rext = 1 kΩ, C	ext = 0.01	μF)

Switching Characteristics (cont)

 $V_{CC} = 5.0 \pm 0.5 \text{ V}$

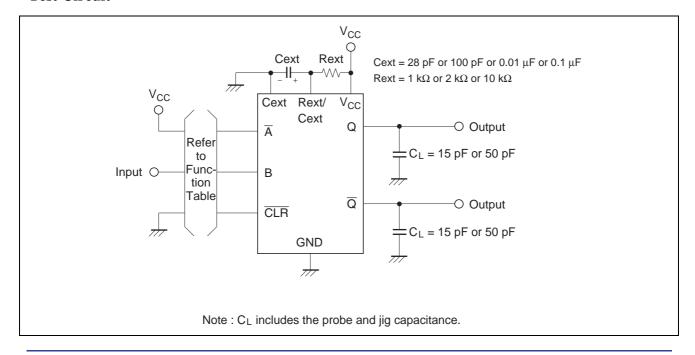
		Ta =	25℃		Ta = -4	0 to 85℃		Test	FROM	то
Item	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	7.3	12.0	1.0	14.0	ns	C _L = 15 pF	A or B	Q or Q
delay time	t _{PHL}	_	8.5	14.0	1.0	16.0	<u> </u>	C _L = 50 pF	<u> </u>	
		_	5.9	9.4	1.0	11.0		$C_L = 15 pF$	CLR	Q or Q
		_	7.5	11.4	1.0	13.0		$C_L = 50 pF$		
		_	7.3	12.9	1.0	15.0		$C_L = 15 pF$	CLR	Q or Q
		_	8.7	14.9	1.0	17.0		C _L = 50 pF	(Trigger)	
Output pulse	t_{wQ}	_	140	200	_	240	ns	$C_L = 50 \text{ pF}, \text{ Cext} = 28 \text{ pF}, \text{ Rext} = 2 \text{ k}\Omega$		
width		90	100	110	90	110	μs	$C_L = 50 pF$,		
								Cext = 0.01 μ F,	Rext = 10	kΩ
		0.9	1.0	1.1	0.9	1.1	ms	$C_L = 50 pF$,		
								Cext = 0.1 μ F, F	Rext = 10 k	Ω
	Δt_{wQ}	_	±1	_		_	%	$C_L = 50 pF$		
Pulse width	t _w	5.0	_	_	5.0	_	ns	\overline{A} , B or \overline{CLR}		
Retrigger time	t _{rr}	_	20	_	_	_	ns	\overline{A} , or B		
								(Rext = 1 kΩ, C	ext = 100 p	oF)
		_	0.95	_	_	_	μs	\overline{A} , or B		
								(Rext = 1 kΩ, C	ext = 0.01	μF)

Operating Characteristics

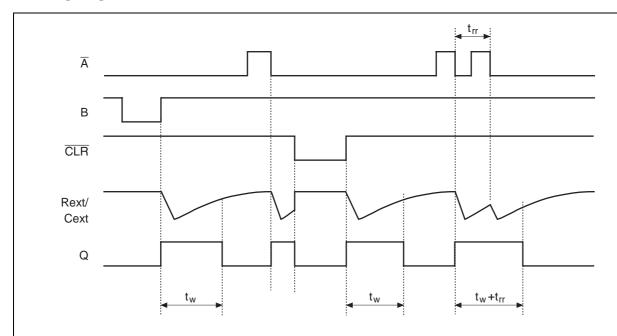
 $C_L = 50 pF$

			1a = 2	5°C			
Item	Symbol	V _{CC} (V)	Min	Тур	Max	Unit	Test Conditions
Power dissipation capacitance	C_{PD}	3.3	_	74.0	_	pF	f = 10 MHz
		5.0	_	86.0	_		

Test Circuit



Timing diagram



Caution in use

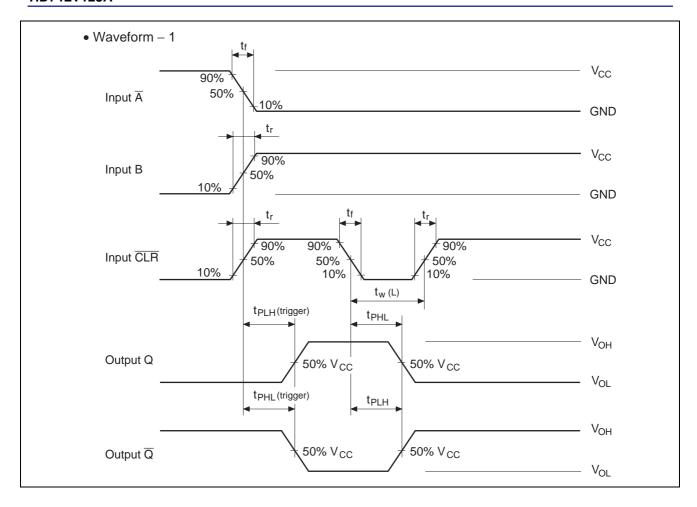
In order to prevent any malfunctions due to noise, connect a high frequency performance capacitor between Vcc and GND, and keep the wiring between the External components and Cext, Rext/Cext pins as short as possible.

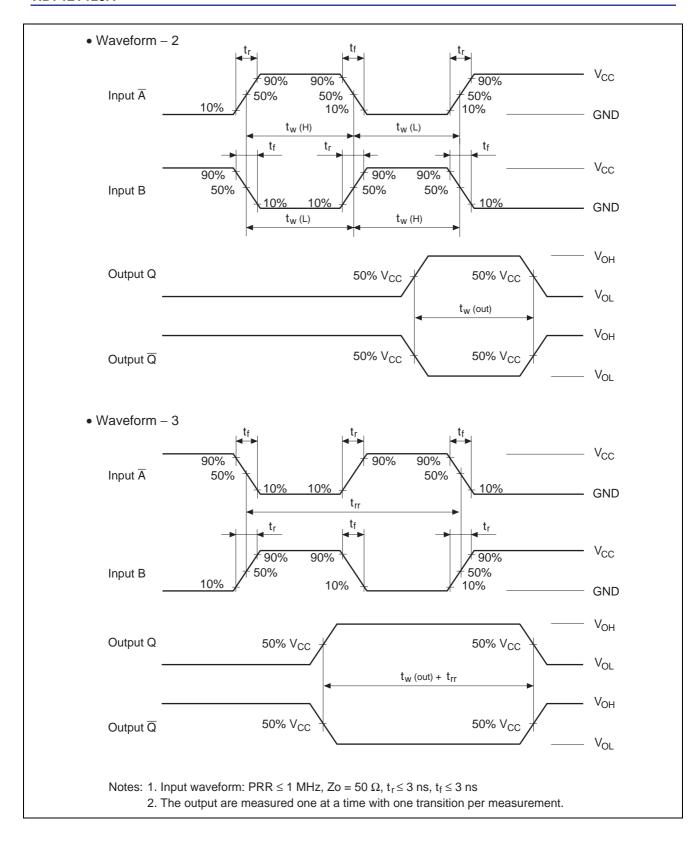
Large values of Cext may cause problems when powering down the HD74LV123A because of the amount of energy stored in the capacitor. When a system containing this device is powered down, the capacitor may discharge from Vcc through the protection diodes at pin 7 or pin 15.

Current through the input protection diodes must be limited to 20 mA; therefore, the turn-off time of the Vcc power supply must not be faster than $t = Vcc \bullet Cext/(20 \text{ mA})$. For example, if Vcc = 5 V and $Cext = 22 \,\mu\text{F}$, the Vcc supply must turn off no faster than $t = (5 \text{ V}) \bullet (22 \,\mu\text{F})/20 \,\text{mA} = 5.5 \,\text{ms}$. This is usually not a problem because power supplies are heavily filtered and cannot discharge at this rate.

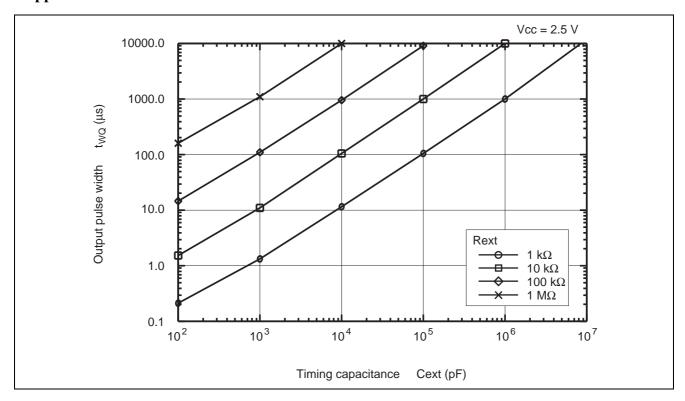
When a more rapid decrease of Vcc to zero volts occurs, the HD74LV123A may sustain damage. To avoid this possibility, use an external calmping diode.

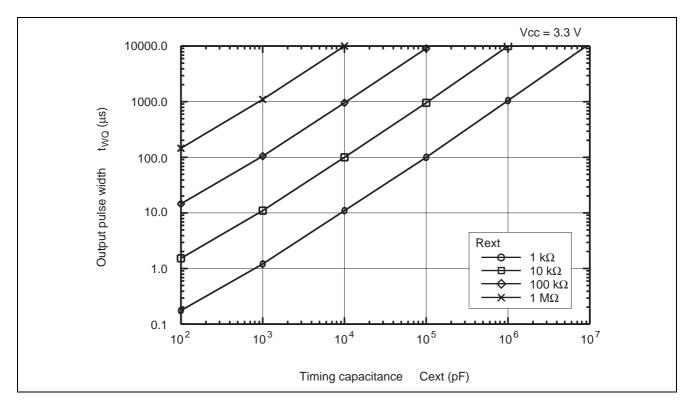
The input pins for unused circuit should be used under conditions to fix the outputs to avoid malfunction cased by noises. Also, it's recommended that Rext / Cext terminals are open and external parts are not connected to.

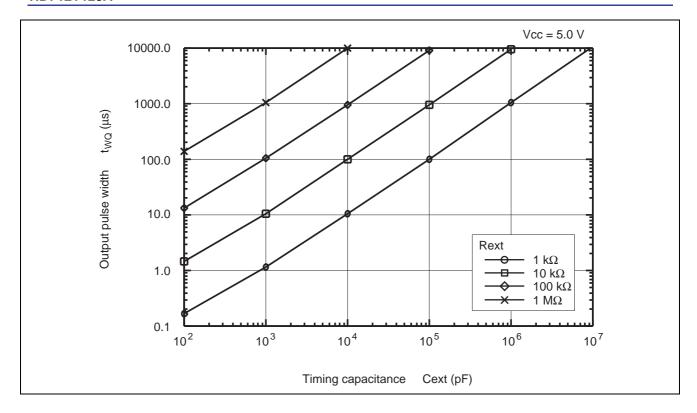


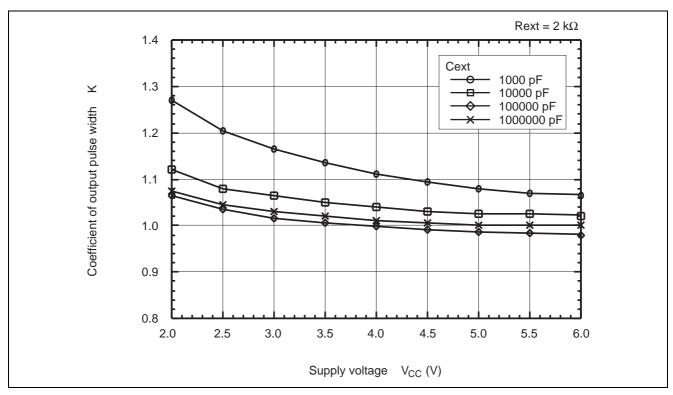


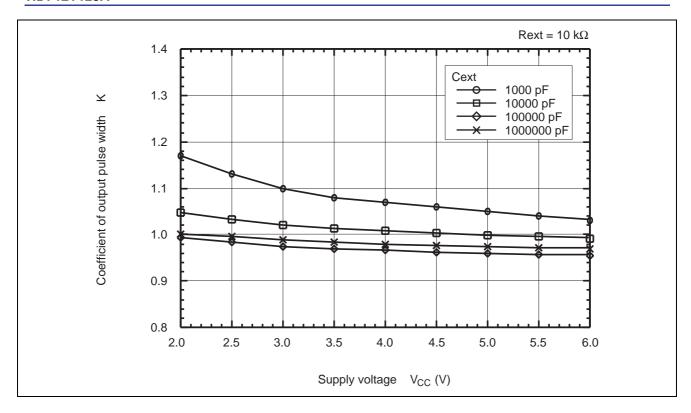
Application Data



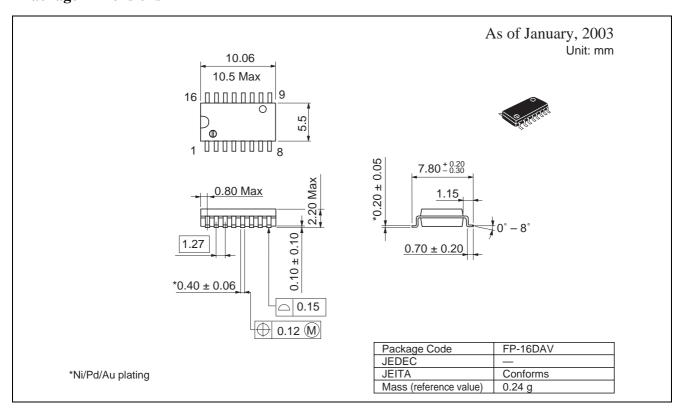


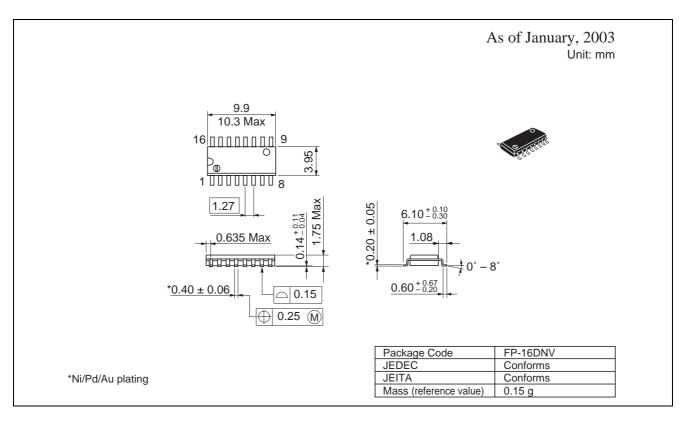


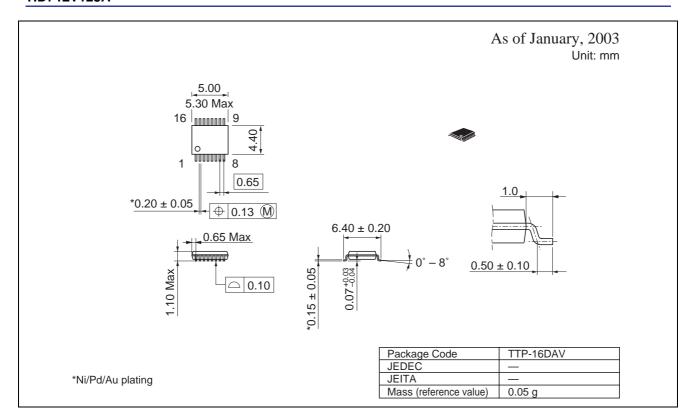




Package Dimensions







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