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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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HD74LV1G66A

Analog Switch

REJ03D0069-0700 Rev.7.00 Mar 21, 2008

Description

The HD74LV1G66A has an analog switch in a 5 pin package. Switch section has its enable input control (C). High-level voltage applied to C turns on the switch section. Applications include signal gating, chopping, modulation or demodulation (modem), and signal multiplexing for analog to digital and digital to analog conversion systems. Low voltage and high-speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

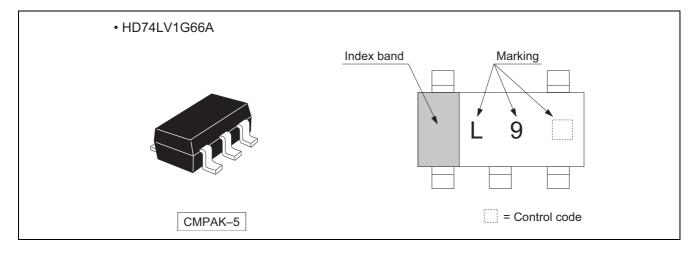
Features

- The basic gate function is lined up as Renesas uni logic series.
- Supplied on emboss taping for high-speed automatic mounting.
- Electrical characteristics equivalent to the HD74LV4066A
 Supply voltage range: 1.65 to 5.5 V
 Operating temperature range: -40 to +85°C
- Control inputs V_{IH} (Max.) = 5.5 V (@ V_{CC} = 0 V to 5.5 V)
- Control inputs has hysteresis voltage for the slow transition.
- Ordering Information

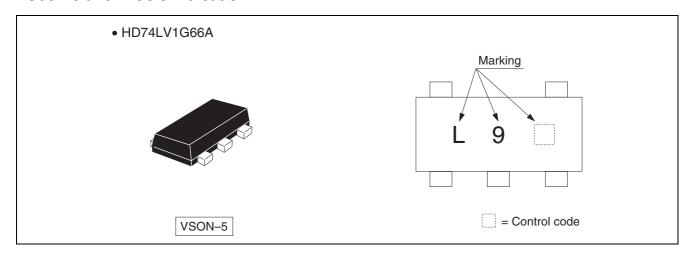
Part Name	Package Type Package Code (Previous Code)		Package Abbreviation	Taping Abbreviation (Quantity)	
HD74LV1G66ACME	CMPAK-5 pin	PTSP0005ZC-A (CMPAK-5V)	СМ	E (3000 pcs/reel)	
HD74LV1G66AVSE	74LV1G66AVSE VSON-5 pin		VS	E (3000 pcs/reel)	

Note: Please consult the sales office for the above package availability.

Outline and Article Indication



Outline and Article Indication

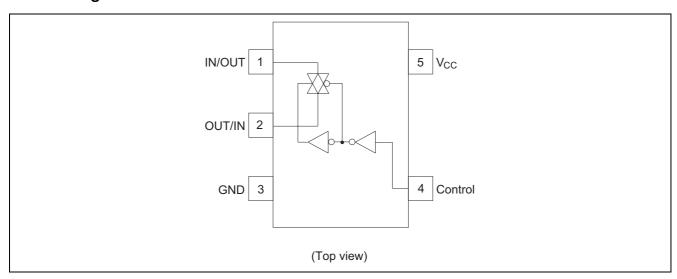


Function Table

Control	Switch
L	OFF
Н	ON

H : High level L : Low level

Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Test Conditions
Supply voltage range	V _{CC}	-0.5 to 7.0	V	
Input voltage range *1	Vı	-0.5 to 7.0	V	
Output voltage range *1, 2	Vo	-0.5 to V _{CC} + 0.5	V	Output: H or L
Input clamp current	I _{IK}	-20	mA	V ₁ < 0
Output clamp current	I _{OK}	±50	mA	$V_O < 0$ or $V_O > V_{CC}$
Continuous output current	I _O	±25	mA	$V_{\rm O} = 0$ to $V_{\rm CC}$
Continuous current through V _{CC} or GND	I _{CC} or I _{GND}	±50	mA	
Maximum power dissipation at $Ta = 25 ^{\circ}C$ (in still air) *3	P _T	200	mW	
Storage temperature	Tstg	-65 to 150	℃	

Notes: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore no two of which may be realized at the same time.

- 1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.
- 2. This value is limited to 5.5 V maximum.
- 3. The maximum package power dissipation was calculated using a junction temperature of 150 ℃.

Recommended Operating Conditions

Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	V_{CC}	1.65	5.5	V	
Input voltage range	Vı	0	5.5	V	
Input / output voltage range	V _{I/O}	0	V _{CC}	V	
	Δt / Δ v	0	300		$V_{CC} = 1.65 \text{ to } 1.95 \text{ V}$
Input transition rise or fall rate		0	200	ns / V	$V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$
Input transition rise or fall rate		0	100	115 / V	$V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$
		0	20		V _{CC} = 4.5 to 5.5 V
Operating free-air temperature	Ta	-40	85	∞	

Note: Unused or floating control inputs must be held high or low.

Electrical Characteristics

ltom	Cumbal	V (V)	Т	a = 25°	Č	T _a =	-40 to 8	5°C	Unit	Test			
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Min	Тур	Max	Unit	Conditions			
		1.65 to 1.95	_	_	_	V _{CC} ×0.75	_	_					
	V _{IH}	2.3 to 2.7		_	_	V _{CC} ×0.7	_	_					
	VIH	3.0 to 3.6		_	_	V _{CC} ×0.7	_	_					
Input voltage		4.5 to 5.5	_	_	_	V _{CC} ×0.7	_	_	V	V Control input only			
input voitage		1.65 to 1.95	_	_	_	_	_	V _{CC} ×0.25	V				
	V _{IL}	2.3 to 2.7	_	_	_	_	_	$V_{CC} \times 0.3$					
	V IL	3.0 to 3.6	_	_	_	_	_	$V_{CC} \times 0.3$					
		4.5 to 5.5	_	_	_	_	_	$V_{CC} \times 0.3$					
		1.8	_	_	_	_	0.25	_					
Hysteresis	V_{H}	2.5	_	_	_	_	0.30	_	V	$V_T^+ - V_T^-$			
voltage	VH	3.3	_	_	_	_	0.35	_	V	VT - VT			
		5.0	_	_	_	_	0.45	_					
		1.65	_	120	360	_	_	450		V V 0VD			
On-state switch	R _{ON}	2.3	_	60	180	_	_	225	Ω	$V_{IN} = V_{CC}$ or GND $V_{C} = V_{IH}$			
resistance	HON	3.0	1	50	150	_	_	190	22	$V_C = V_{IH}$ $I_T = 1 \text{ mA}$			
		4.5	_	40	75	_	_	100		11 - 1 111/1			
		1.65	_	700	1100	_	_	1400		V V - OND			
Peak on	Day (B)	2.3		250	500	_	_	600	Ω	$V_{IN} = V_{CC}$ to GND $V_{C} = V_{IH}$			
resistance	R _{ON (P)}	3.0	_	100	180	_	_	225	22	$I_T = 1 \text{ mA}$			
		4.5	_	50	100	_	_	125					
Off-state switch leakage current	I _{s (OFF)}	5.5		_	±0.1	-	_	±1.0	μΑ	$\begin{aligned} &V_{IN} = V_{CC}, \\ &V_{OUT} = GND \\ &\text{or } V_{IN} = GND, \\ &V_O = V_{CC}, \ V_C = V_{IL} \end{aligned}$			
On-state switch leakage current	I _{s (ON)}	5.5	-		±0.1	_	_	±1.0	μΑ	$V_{IN} = V_{CC}$ or GND $V_C = V_{IH}$			
Input current	I _{IN}	0 to 5.5	1	_	±0.1	_	_	±1.0	μΑ	$V_{IN} = 5.5 \text{ V or GND}$			
Quiescent supply current	I _{CC}	5.5	_	_		_		10	μΑ	V _{IN} = V _{CC} or GND			
Control input capacitance	C _{IC}	_	_	3.5	_	_	_	_	pF				
Switch terminal capacitance	C _{IN / OUT}	_	_	4.0	_	_	_	_	pF				
Feed through capacitance	C _{IN-OUT}	_	_	0.5	_	_		_	pF				

Switching Characteristics

• $V_{CC} = 1.8 \pm 0.15 \text{ V}$

Item	Symbol		Ta = 25℃	;	Ta = -40	to 85℃	Unit	Test	FROM	ТО
iteiii	Syllibol	Min	Тур	Max	Min	Max	Offic	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	4.0	13.0	_	19.0	ns	$C_L = 15 pF$	IN/OUT	OUT/IN
delay time	t _{PHL}	_	11.0	23.0	_	29.0	115	$C_L = 50 pF$	or OUT/IN	or IN/OUT
Enable time	t _{zH}	_	11.0	24.0	_	29.0	ns	$C_L = 15 pF$	С	IN/OUT
Litable time	t _{ZL}	_	18.0	44.0	_	51.0	115	$C_L = 50 pF$	C	or OUT/IN
Disable time	t _{HZ}	_	11.0	21.0	_	29.0	ne	$C_L = 15 pF$	С	IN/OUT
	t_{LZ}	_	18.0	46.0	_	53.0	ns	$C_L = 50 pF$		or OUT/IN

• $V_{CC} = 2.5 \pm 0.2 \text{ V}$

Item	Symbol	Ta = 25 ℃			Ta = -40	Ta = -40 to 85 ℃		Test	FROM	ТО
item	Syllibol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	2.0	10.0	_	16.0	ns	$C_L = 15 pF$	IN/OUT	OUT/IN
delay time	t _{PHL}	_	5.0	12.0	_	18.0	115	$C_L = 50 pF$	or OUT/IN	or IN/OUT
Enable time	t _{zH}	_	6.0	15.0	_	20.0	ns	$C_L = 15 pF$	C	IN/OUT
Litable time	t_{ZL}	_	8.0	25.0	_	32.0	115	$C_L = 50 pF$		or OUT/IN
Disable time	t _{HZ}	_	7.0	15.0	_	23.0	nc	$C_L = 15 pF$	C	IN/OUT
	t_{LZ}	_	11.0	25.0		32.0	ns	$C_L = 50 pF$		or OUT/IN

• $V_{CC} = 3.3 \pm 0.3 \text{ V}$

Item	Symbol	Ta = 25 ℃			Ta = -40 to 85 ℃		Unit	Test	FROM	ТО
item	Syllibol	Min	Тур	Max	Min	Max	Oilit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	1.5	6.0	_	10.0	ns	$C_L = 15 pF$	IN/OUT	OUT/IN
delay time	t _{PHL}	_	4.0	9.0	_	12.0	115	$C_L = 50 pF$	or OUT/IN	or IN/OUT
Enable time	t _{zH}	_	4.0	11.0	_	15.0	ns	$C_{L} = 15 pF$	C	IN/OUT
Litable time	t_{ZL}	_	6.0	18.0	_	22.0	115	$C_L = 50 pF$		or OUT/IN
Disable time	t _{HZ}	_	5.0	11.0	_	15.0	ns	$C_L = 15 pF$	C	IN/OUT
	t_{LZ}	_	8.0	18.0		22.0	115	$C_L = 50 pF$	C	or OUT/IN

Switching Characteristics (cont)

• $V_{CC} = 5.0 \pm 0.5 \text{ V}$

	Cumbal	Ta = 25 ℃			Ta = -40	Ta = -40 to 85 ℃		Test	FROM	то
Item	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	1.0	4.0	_	7.0	ns	$C_L = 15 pF$	IN/OUT	OUT/IN
delay time	t _{PHL}	_	3.0	6.0	_	8.0	115	$C_L = 50 pF$	or OUT/IN	or IN/OUT
Enable time	t _{ZH}	_	3.0	7.0	_	10.0	no	$C_L = 15 pF$	C	IN/OUT
Enable time	t_{ZL}	_	5.0	12.0	_	16.0	ns	C _L = 50 pF	C	or OUT/IN
Disable time	t _{HZ}	_	4.0	7.0	_	10.0	no	$C_L = 15 pF$	C	IN/OUT
	t_{LZ}	_	6.0	12.0	_	16.0	ns	C _L = 50 pF		or OUT/IN

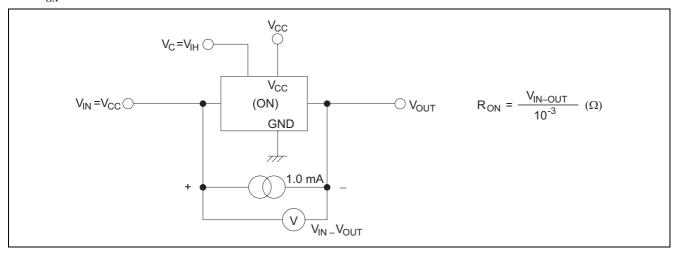
Operating Characteristics

• $C_L = 50 \text{ pF}$

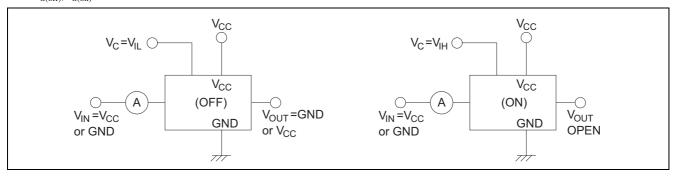
Itom	Symbol	V _{CC} (V)	Ta = 25 ℃			Unit	Test Conditions	
Item	Symbol		Min	Тур	Max	Ollit	rest Conditions	
Power dissipation	C	3.3	_	3.5	_	2	f = 10 MHz	
capacitance	C_{PD}	5.0	_	4.0	_	рF	= 10 MHZ	

Test Circuit

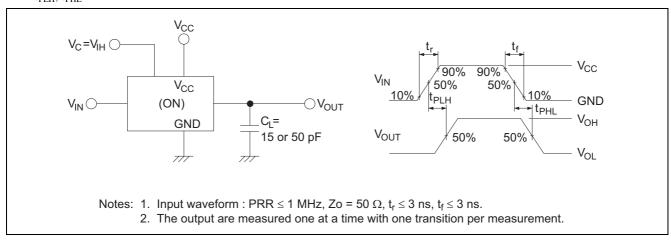
• R_{ON}



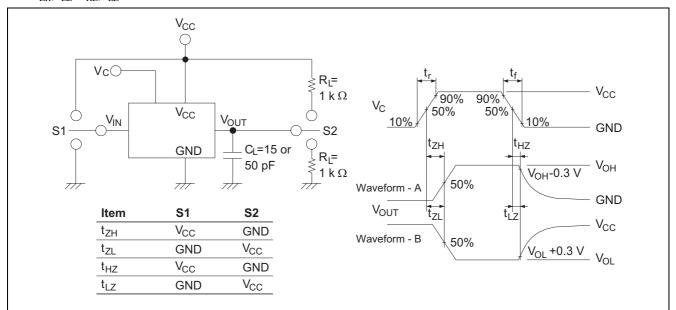
• $I_{S(off)}$, $I_{S(on)}$



• t_{PLH}, t_{PHL}

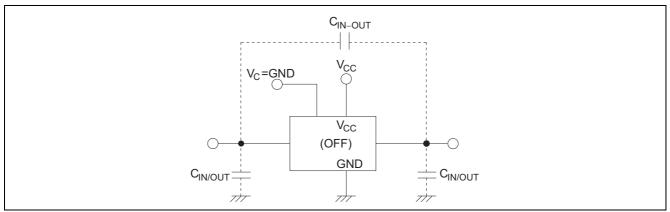


$\bullet \quad t_{ZH},\,t_{ZL}\,/\,t_{HZ},\,t_{LZ}$

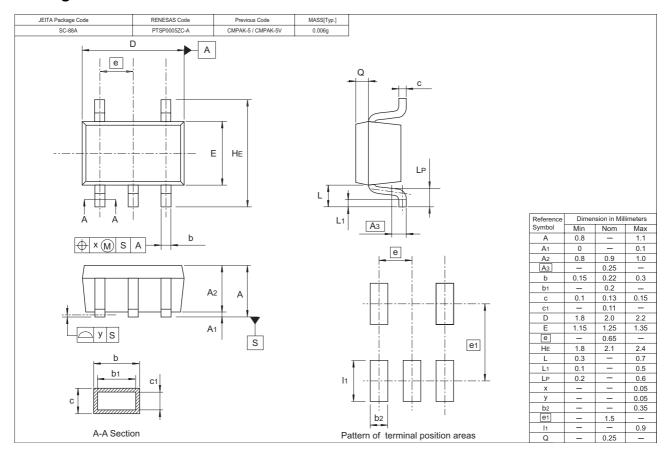


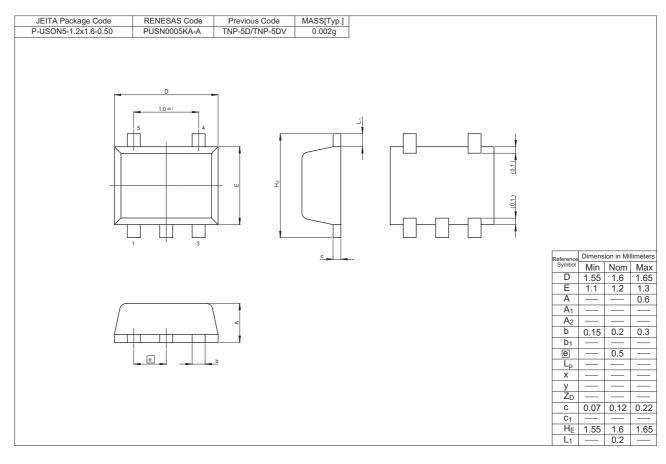
- Notes: 1. Input waveform : PRR \leq 1 MHz, Zo = 50 Ω , $t_r \leq$ 3 ns, $t_f \leq$ 3 ns.
 - 2. Waveform A is for an output with internal conditions such that the output is high except when disabled by the output control.
 - 3. Waveform B is for an output with internal conditions such that the output is low except when disabled by the output control.
 - 4. The output are measured one at a time with one transition per measurement.

• C_{IN/OUT}, C_{IN-OUT}



Package Dimensions





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