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# HD74LVU04A

## **Hex Inverters**

REJ03D0228-0200Z (Previous ADE-205-248 (Z)) Rev.2.00 May 21, 2004

#### **Description**

The HD74LVU04A has six inverters with unbuffered outputs in a 14-pin package.

Low-voltage and high-speed operation is suitable for the battery-powered products (e.g., notebook computers), and the low-power consumption extends the battery life.

#### **Features**

- $V_{CC} = 2.0 \text{ V}$  to 5.5 V operation
- All inputs  $V_{IH}$  (Max.) = 5.5 V (@ $V_{CC}$  = 0 V to 5.5 V)
- All outputs  $V_0$  (Max.) = 5.5 V (@ $V_{CC}$  = 0 V)
- Typical  $V_{OL}$  ground bounce < 0.8 V (@ $V_{CC}$  = 3.3 V, Ta = 25°C)
- Typical  $V_{OH}$  undershoot > 2.3 V (@ $V_{CC}$  = 3.3 V, Ta = 25°C)
- Output current  $\pm 6 \text{ mA}$  (@V<sub>CC</sub> = 3.0 V to 3.6 V),  $\pm 12 \text{ mA}$  (@V<sub>CC</sub> = 4.5 V to 5.5 V)
- Ordering Information

Part Name	Package Type	Package Code	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LVU04AFPEL	SOP-14 pin(JEITA)	FP-14DAV	FP	EL (2,000 pcs/reel)
HD74LVU04ARPEL	SOP-14 pin(JEDEC)	FP-14DNV	RP	EL (2,500 pcs/reel)
HD74LVU04ATELL	TSSOP-14 pin	TTP-14DV	Т	ELL (2,000 pcs/reel)

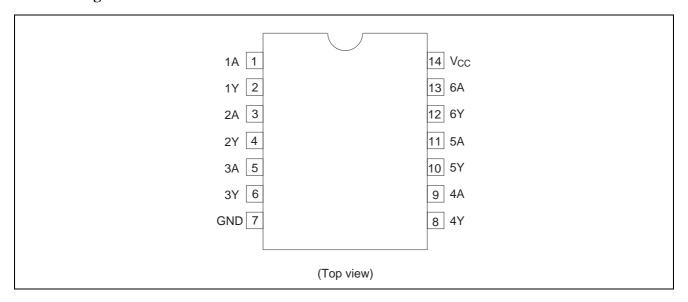
Note: Please consult the sales office for the above package availability.

#### **Function Table**

Input A	Output Y
Н	L
L	Н

Note: H: High level L: Low level

## **Pin Arrangement**



## **Absolute Maximum Ratings**

Item	Symbol	Ratings	Unit	Conditions
Supply voltage range	V <sub>CC</sub>	-0.5 to 7.0	V	
Input voltage range*1	Vı	-0.5 to 7.0	V	
Output voltage range*1,2	Vo	$-0.5$ to $V_{CC} + 0.5$	V	Output: H or L
Input clamp current	I <sub>IK</sub>	-20	mA	V <sub>I</sub> < 0
Output clamp current	lok	±50	mA	$V_O < 0$ or $V_O > V_{CC}$
Continuous output current	lo	±25	mA	$V_O = 0$ to $V_{CC}$
Continuous current through V <sub>CC</sub> or GND	I <sub>CC</sub> or I <sub>GND</sub>	±50	mA	
Maximum power dissipation at	P <sub>T</sub>	785	mW	SOP
Ta = 25℃ (in still air)* <sup>3</sup>		500		TSSOP
Storage temperature	Tstg	-65 to 150	∞	

Notes: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

- 1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.
- 2. This value is limited to 5.5 V maximum.
- 3. The maximum package power dissipation was calculated using a junction temperature of 150 ℃.

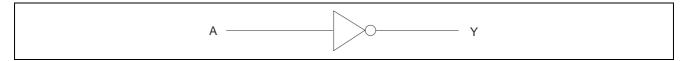
## HD74LVU04A

## **Recommended Operating Conditions**

Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	Vcc	2.0	5.5	V	
Input voltage range	Vı	0	5.5	V	
Output voltage range	Vo	0	V <sub>CC</sub>	V	
Output current	I <sub>OH</sub>	_	<del>-</del> 50	μΑ	V <sub>CC</sub> = 2.0 V
		_	-2	mA	V <sub>CC</sub> = 2.3 to 2.7 V
		_	-6		V <sub>CC</sub> = 3.0 to 3.6 V
		_	-12		V <sub>CC</sub> = 4.5 to 5.5 V
	I <sub>OL</sub>	_	50	μΑ	V <sub>CC</sub> = 2.0 V
		_	2	mA	V <sub>CC</sub> = 2.3 to 2.7 V
		_	6		V <sub>CC</sub> = 3.0 to 3.6 V
		_	12		V <sub>CC</sub> = 4.5 to 5.5 V
Operating free-air temperature	Ta	-40	85	℃	

Note: Unused or floating inputs must be held high or low.

## Logic Diagram



#### **DC** Electrical Characteristics

 $Ta = -40 \text{ to } 85^{\circ}\text{C}$ 

Item	Symbol	V <sub>CC</sub> (V)*	Min	Тур	Max	Unit	Test Conditions
Input voltage	$V_{IH}$	2.0	1.7	_	_	V	
		2.3 to 2.7	$V_{\text{CC}} \times 0.8$	_	_	_	
		3.0 to 3.6	$V_{\text{CC}} \times 0.8$	_	_	_	
		4.5 to 5.5	$V_{\text{CC}} \times 0.8$	_	_	_	
	V <sub>IL</sub>	2.0	_	_	0.3	_	
		2.3 to 2.7	_	_	$V_{\text{CC}} \times 0.2$	_	
		3.0 to 3.6	_	_	$V_{\text{CC}} \times 0.2$	_	
		4.5 to 5.5	_	_	$V_{\text{CC}} \times 0.2$	_	
Output voltage	$V_{OH}$	Min to Max	V <sub>CC</sub> - 0.1	_	_	V	$I_{OL} = -50 \mu A$
		2.3	2.0	_	_	_	$I_{OL} = -2 \text{ mA}$
		3.0	2.48	_	_	_	$I_{OL} = -6 \text{ mA}$
		4.5	3.8	_	_	_	$I_{OL} = -12 \text{ mA}$
	V <sub>OL</sub>	Min to Max	_	_	0.1	V	$I_{OL} = 50 \mu A$
		2.3	_	_	0.4	_	I <sub>OL</sub> = 2 mA
		3.0	_	_	0.44	_	I <sub>OL</sub> = 6 mA
		4.5	_	_	0.55	_	I <sub>OL</sub> = 12 mA
Input current	I <sub>IN</sub>	0 to 5.5	_	_	±1	μΑ	$V_{IN} = 5.5 \text{ V or GND}$
Quiescent supply current	I <sub>CC</sub>	5.5	_	_	20	μА	$V_{IN} = V_{CC}$ or GND, $I_O = 0$
Input capacitance	C <sub>IN</sub>	3.3	_	4.0	_	pF	$V_I = V_{CC}$ or GND

Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.

#### **Switching Characteristics**

 $V_{CC} = 2.5 \pm 0.2 \text{ V}$ **FROM** Ta = -40 to 85 ℃ Test Ta = 25 ℃ TO Unit **Conditions** (Input) (Output) Item **Symbol** Min Min Max Typ Max Propagation 3.2 14.0  $C_L = 15 pF$ Α  $t_{\text{PLH}}$ 10.9 1.0 ns delay time  $t_{\text{PHL}}$ 6.6 13.4 1.0 16.0  $C_L = 50 pF$ 

 $V_{CC} = 3.3 \pm 0.3 \text{ V}$ Ta = 25℃ Ta = -40 to 85 ℃ **Test FROM** TO Unit **Conditions** (Input) (Output) Item **Symbol** Min Тур Max Min Max  $C_L = 15 pF$ Propagation Α 2.5 8.9 1.0 10.5  $t_{\text{PLH}}$ delay time 4.7  $t_{PHL}$ 11.4 1.0 13.0  $C_L = 50 pF$ 

 $V_{CC} = 5.0 \pm 0.5 \text{ V}$ Ta = 25℃ Ta = -40 to 85 ℃ Test **FROM** TO Item **Symbol** Unit **Conditions** (Input) (Output) Min Max Min Max Typ Propagation 2.2 5.5 1.0 6.5  $C_L = 15 pF$ Α  $t_{\text{PLH}}$ delay time  $t_{\text{PHL}}$ 3.9 7.0 1.0 8.0  $C_L = 50 pF$ 

## **Operating Characteristics**

 $C_L = 50 \text{ pF}$ 

Ta	1 =	25	°C

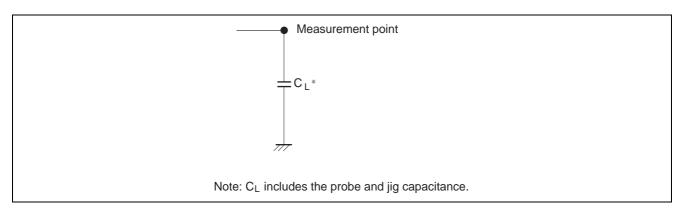
Item	Symbol	V <sub>CC</sub> (V)	Min	Тур	Max	Unit	Test Conditions
Power dissipation capacitance	C <sub>PD</sub>	3.3	_	5.6	_	pF	f = 10 MHz
		5.0	_	6.7	_		

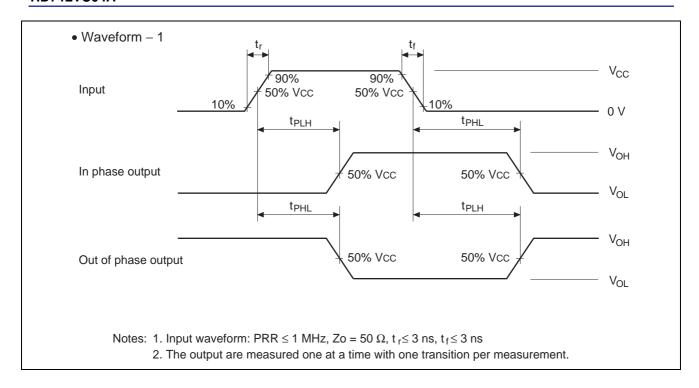
## **Noise Characteristics**

 $C_L = 50 \text{ pF}$ 

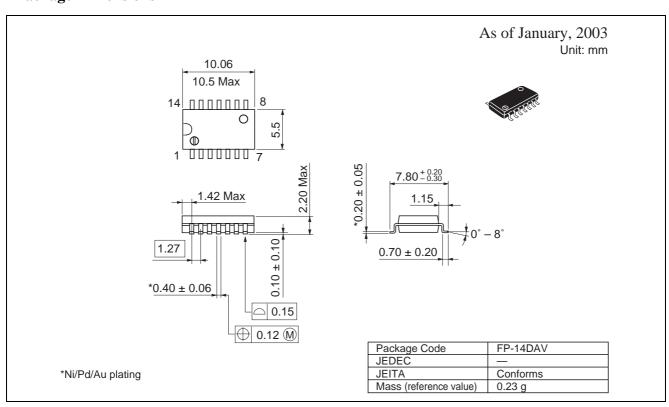
			Ta = 25	5℃			
Item	Symbol	V <sub>CC</sub> (V)	Min	Тур	Max	Unit	<b>Test Conditions</b>
Quiet output, maximum dynamic V <sub>OL</sub>	$V_{OL\ (P)}$	3.3	_	0.5	8.0	V	
Quiet output, minimum dynamic V <sub>OL</sub>	$V_{OL\ (V)}$	3.3	_	-0.1	-0.8	V	
Quiet output, minimum dynamic V <sub>OH</sub>	$V_{OH\ (V)}$	3.3	_	3.0	_	V	
High-level dynamic put voltage	V <sub>IH (D)</sub>	3.3	2.31	_	_	V	
Low-level dynamic put voltage	V <sub>IL (D)</sub>	3.3	_	_	0.99	V	

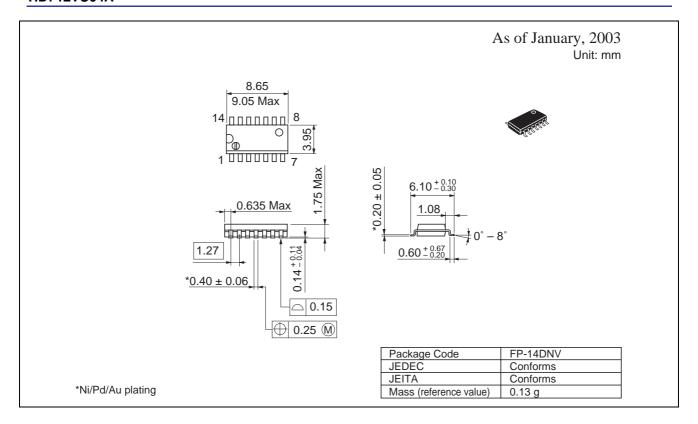
## **Test Circuit**

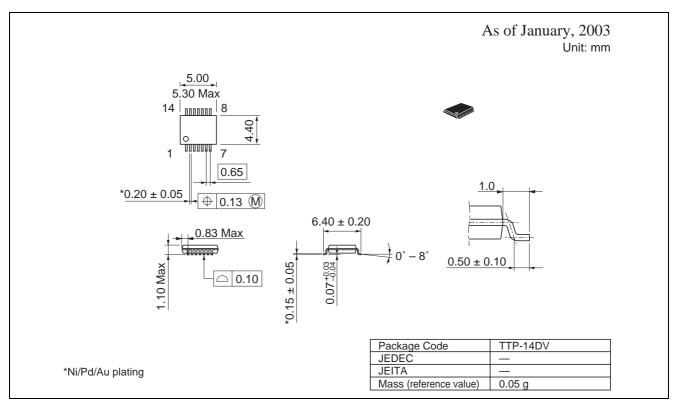




## **Package Dimensions**







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