# RENESAS

# **HSU119**

Silicon Epitaxial Planar Diode for High Speed Switching

REJ03G0190-0200 Rev.2.00 Feb 04, 2009

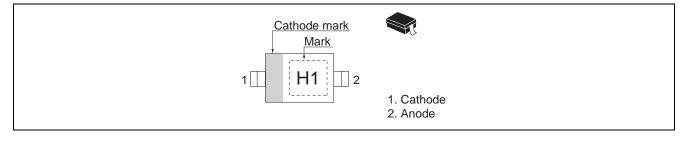
# Features

- Low capacitance. (C = 2.0 pF max)
- Short reverse recovery time. ( $t_{rr} = 3.0 \text{ ns max}$ )
- Lineup of environmental friendly Halogen free type (HSU119-N)
- Ultra small Resin Package (URP) is suitable for compact and high-density surface mount design.

# **Ordering Information**

Part No.	Laser Mark	Package Name	Package Code	Taping Abbreviation (Quantity)
HSU119TRF				
HSU119-NTRF	H1	URP	PTSP0002ZA-A	TRF (3,000 pcs / reel)
(Halogen-free type)				

# Pin Arrangement



# **Absolute Maximum Ratings**

			$(Ta = 25^{\circ}C)$
Item	Symbol	Value	Unit
Peak reverse voltage	V <sub>RM</sub>	85	V
Reverse voltage	V <sub>R</sub>	80	V
Peak forward current	I <sub>FM</sub>	300	mA
Non-Repetitive peak forward surge current	I <sub>FSM</sub> * <sup>1</sup>	4	А
Average rectified current	lo	100	mA
Junction temperature	Тј	125	°C
Storage temperature	Tstg	-55 to +125	°C

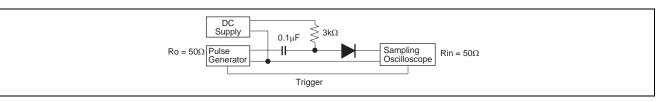
Note: 1. Within  $1\mu s$  forward surge current.

# **Electrical Characteristics**

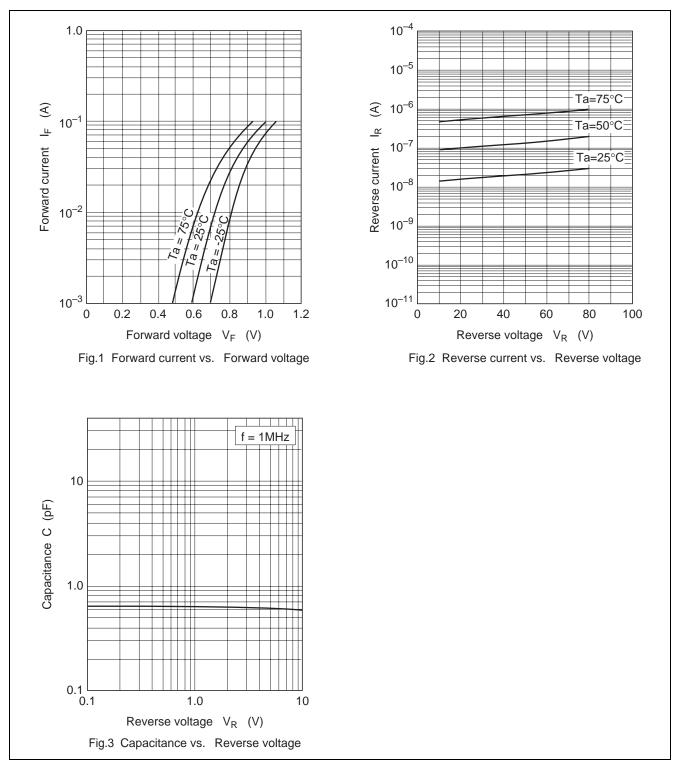
 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Forward voltage	V <sub>F1</sub>	—	—	0.8	V	I <sub>F</sub> = 10 mA
	V <sub>F2</sub>	—	—	1.2	V	I <sub>F</sub> = 100 mA
Reverse current	I <sub>R</sub>	—	—	0.1	μΑ	V <sub>R</sub> = 80 V
Capacitance	С	—	—	2.0	pF	$V_{R} = 0 V, f = 1 MHz$
Reverse recovery time*1	t <sub>rr</sub>	_	_	3.0	ns	$I_F$ = 10 mA, $V_R$ = 6 V, $R_L$ = 50 $\Omega$

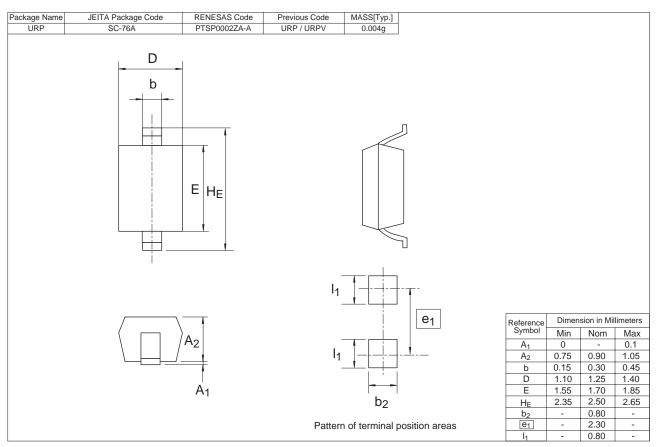
Note: 1. Reverse recovery time test circuit



# **Main Characteristics**



# **Package Dimensions**



## RenesasTechnology Corp. sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

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450 Holger Way, San Jose, CA 95134-1368, U.S.A Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

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1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632 Tel: <65> 6213-0200, Fax: <65> 6278-8001

Renesas Technology Korea Co., Ltd. Kukje Center Bldg. 18th Fl., 191, 2-ka, Hangang-ro, Yongsan-ku, Seoul 140-702, Korea Tel: <82> (2) 796-3115, Fax: <82> (2) 796-2145

Renesas Technology Malaysia Sdn. Bhd Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No.18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia Tel: <603> 7955-9390, Fax: <603> 7955-9510

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April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

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