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April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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HVL138A

Silicon Epitaxial Trench Pin Diode for Antenna Switching

REJ03G0431-0200 Rev.2.00 Jan 12, 2006

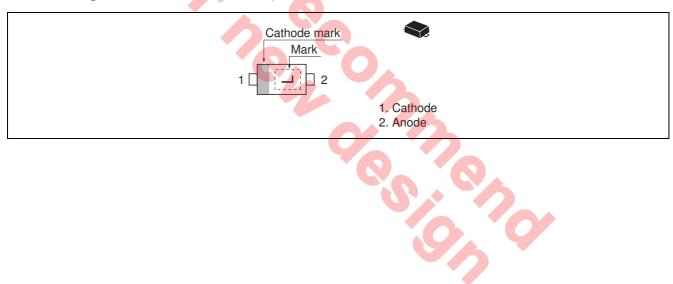
Features

- Adopting the trench structure improves low capacitance.(C = 0.85 pF max)
- Low forward resistance. (rf = $1.1 \Omega \text{ max}$)
- Low operation current.
- Extremely small Flat Lead Package (EFP) is suitable for surface mount design.

Ordering Information

Type No.	Laser Mark	Package Name	Package Code	
HVL138A	L	EFP	PXSF0002ZA-A	

Pin Arrangement



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Reverse voltage	V _R	30	V
Forward current	I _F	100	mA
Power dissipation	Pd	100	mW
Junction temperature	Tj	125	°C
Storage temperature	Tstg	-55 to +125	°C

Electrical Characteristics

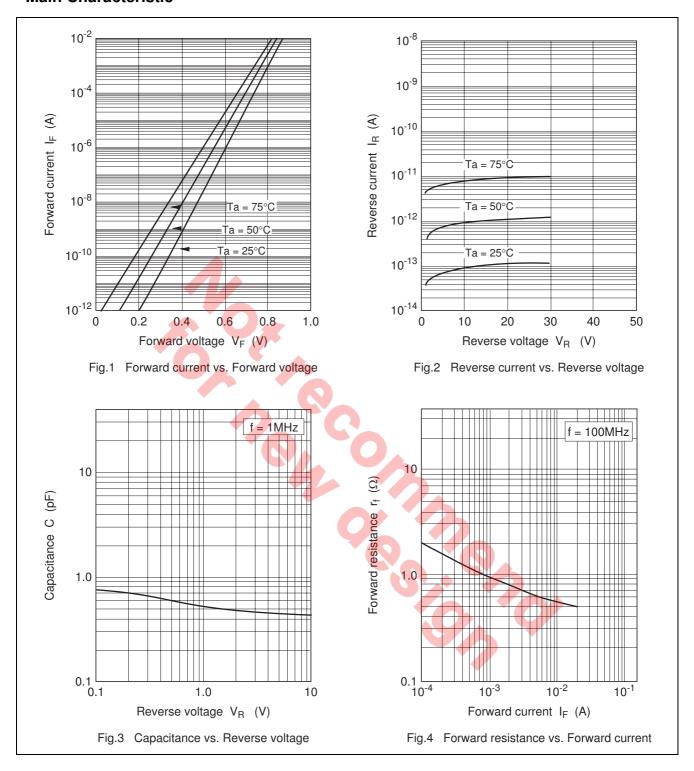
 $(Ta = 25^{\circ}C)$

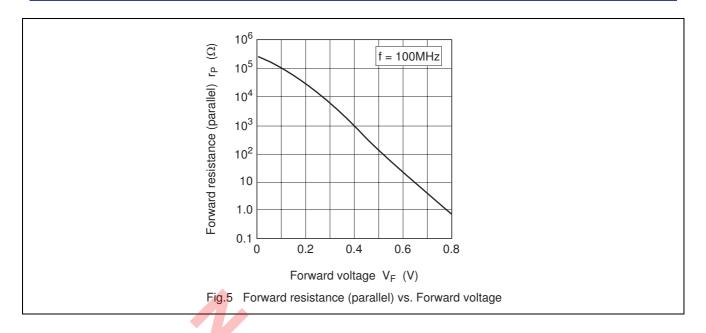
Item	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse current	I _R	_	_	10	nA	V _R = 25 V
Forward voltage	V_{F}	7	_	0.9	V	I _F = 2 mA
Capacitance	С	/_	_	0.85	pF	V _R = 1 V, f = 1 MHz
Forward resistance	r _f		_	1.1	Ω	I _F = 2 mA, f = 100 MHz

Note: For EFP package, the material of lead is exposed for cutting plane. There for, soldering nature of lead tip part is considered as unquestioned. Please kindly consider soldering nature.



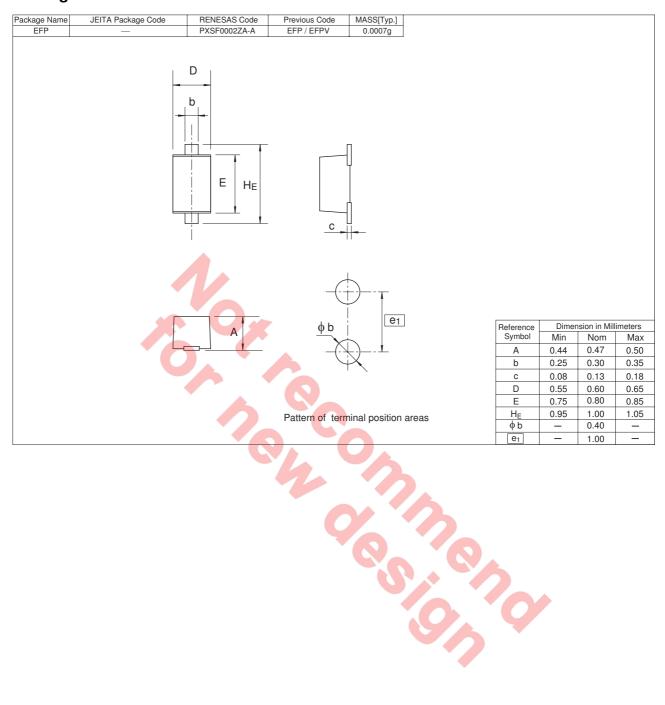
Main Characteristic







Package Dimensions



Renesas Technology Corp. Sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

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