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April 1<sup>st</sup>, 2010 Rene<mark>sas E</mark>lectronics Corporation

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# **HVL142AM**

# Silicon Epitaxial Planar Pin Diode for Antenna Switching

REJ03G0198-0200 Rev.2.00 Jan 19, 2006

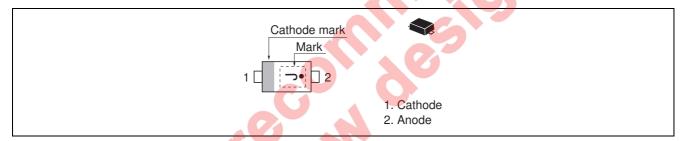
### **Features**

- An optimal solution for antenna switching in mobile phones.
- Low capacitance. (C = 0.35 pF max)
- Low forward resistance. (rf =  $1.3 \Omega \text{ max}$ )
- Thin Extremely small Flat Lead Package (TEFP) is suitable for surface mount design.

## **Ordering Information**

Type No.	Laser Mark	Package Name	Package Code	
HVL142AM	J	TEFP	PUSF0002ZA-A	

# **Pin Arrangement**



## **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Value	Unit
Reverse voltage	$V_R$	30	V
Forward current	l <sub>F</sub>	100	mA
Power dissipation	Pd	100	mW
Junction temperature	Tj	125	°C
Storage temperature	Tstg	-55 to +125	°C

### **Electrical Characteristics**

 $(Ta = 25^{\circ}C)$ 

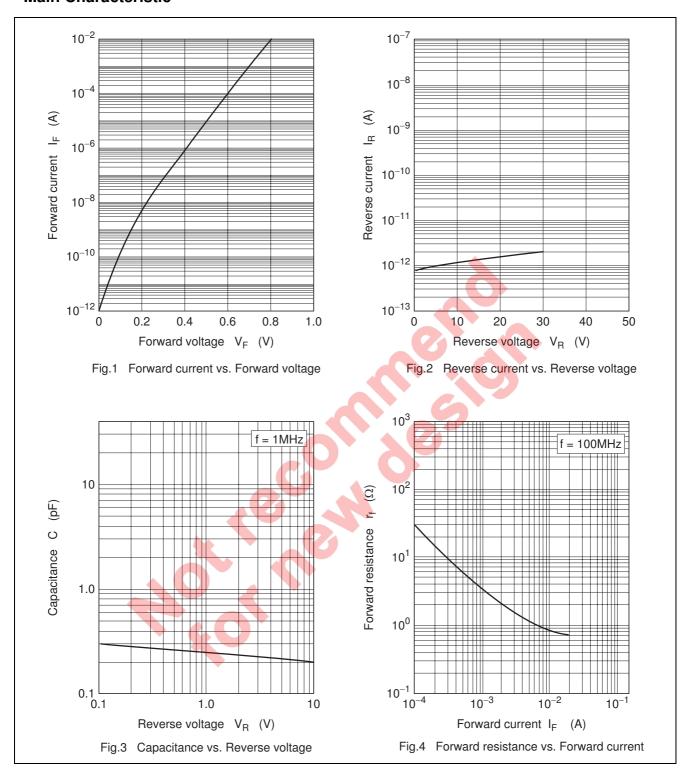
Item	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse current	I <sub>R</sub>	_	_	100	nA	V <sub>R</sub> = 30 V
Forward voltage	V <sub>F</sub>	_	_	1.0	V	I <sub>F</sub> = 10 mA
Capacitance	С	_	_	0.35	pF	$V_R = 1 \text{ V, f} = 1 \text{ MHz}$
Forward resistance	r <sub>f</sub>	_	_	1.3	Ω	I <sub>F</sub> = 10 mA, f = 100 MHz
ESD-Capability *1	_	100	_	_	V	$C = 200 \text{ pF}, R = 0 \Omega$ , Both forward
						and reverse direction 1 pulse.

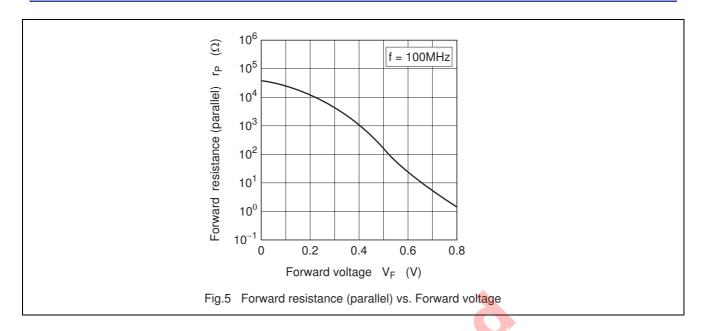
Notes: 1. Failure criterion;  $I_R > 100 \text{ nA}$  at  $V_R = 30 \text{ V}$ 

2. For TEFP package, the material of lead is exposed for cutting plane. There for, soldering nature of lead tip part is considered as unquestioned. Please kindly consider soldering nature.

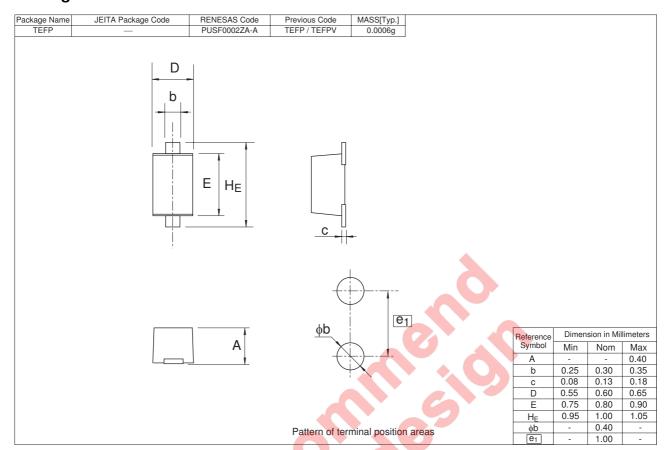


### **Main Characteristic**





## **Package Dimensions**





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