

High Volume Sensor for Low Pressure Applications

Freescale Semiconductor has developed a low cost, high volume, miniature pressure sensor package which is ideal as a sub-module component or a disposable unit. The unique concept of the Chip Pak allows great flexibility in system design while allowing an economic solution for the designer. This new chip carrier package uses Freescale Semiconductor's unique sensor die with its piezoresistive technology, along with the added feature of on-chip, thin-film temperature compensation and calibration.

NOTE: Freescale Semiconductor is also offering the Chip Pak package in application-specific configurations, which will have an "SPX" prefix, followed by a four-digit number, unique to the specific customer

Features

- Low Cost
- Integrated Temperature Compensation and Calibration
- Ratiometric to Supply Voltage
- Polysulfone Case Material (ISO 10993)
- Provided in Easy-to-Use Tape and Reel

MPXC2011DT1 MPXC2012DT1

Pressure Sensors
0 to 75 mmHg
(0 to 10 kPa)

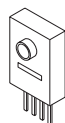
Application Examples

- Respiratory Diagnostics
- Air Movement Control
- Controllers
- Pressure Switching

ORDERING INFORMATION

Device Name	Package Options	Case No.	Pressure Type			Device Marking
			Gauge	Differential	Absolute	
MPXC2011DT1	Tape and Reel	423A		•		Date Code, Lot ID
MPXC2012DT1	Tape and Reel	423A		•		Date Code, Lot ID

CHIP PAK PACKAGE



MPXC2011DT1/MPXC2012DT1
CASE 423A-03

NOTE: The die and wire bonds are exposed on the front side of the Chip Pak (pressure is applied to the backside of the device). Front side die and wire protection must be provided in the customer's housing. Use caution when handling the devices during all processes.

Freescale Semiconductor's MPXC2011DT1/MPXC2012DT1 Pressure Sensor has been designed for medical usage by combining the performance of Freescale Semiconductor's shear stress pressure sensor design and the use of biomedically approved materials. Materials with a proven history in medical situations have been chosen to provide a sensor that can be used with confidence in applications, such as invasive blood pressure monitoring. It can be sterilized using ethylene oxide. The portions of the pressure sensor that are required to be biomedically approved are the rigid housing and the gel coating.

The rigid housing is molded from a white, medical grade polysulfone that has passed extensive biological testing including: 10993-5:1999, 10993-10:2002, and 10993-11:1993.

The MPXC2011DT1 contains a silicone dielectric gel which covers the silicon piezoresistive sensing element. The gel is a nontoxic, nonallergenic elastomer system which meets all USP XX Biological Testing Class V requirements. The properties of the gel allow it to transmit pressure uniformly to the diaphragm surface, while isolating the internal electrical connections from the corrosive effects of fluids, such as saline solution. The gel provides electrical isolation sufficient to withstand defibrillation testing, as specified in the proposed Association for the Advancement of Medical Instrumentation (AAMI) Standard for blood pressure transducers. A biomedically approved opaque filler in the gel prevents bright operating room lights from affecting the performance of the sensor.

The MPXC2012DT1 is a no-gel option.

MAXIMUM RATINGS

Table 1. Maximum Ratings⁽¹⁾

Rating	Symbol	Value	Unit
Maximum Pressure (Backside)	P_{max}	75	kPa
Storage Temperature	T_{stg}	-25 to +85	°C
Operating Temperature	T_A	+15 to +40	°C

1. Exposure beyond the specified limits may cause permanent damage or degradation to the device.

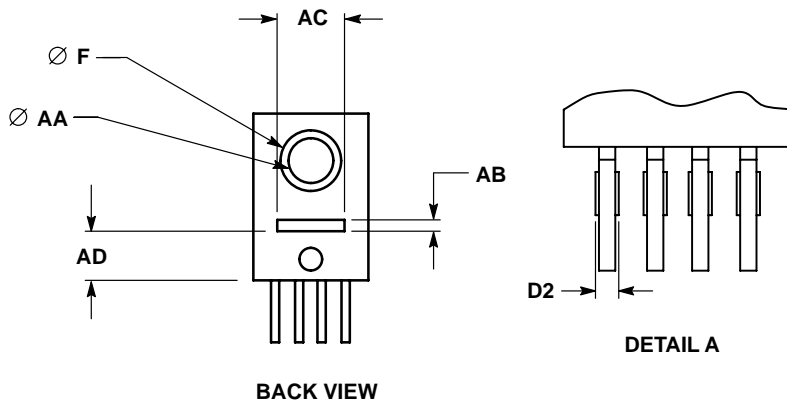
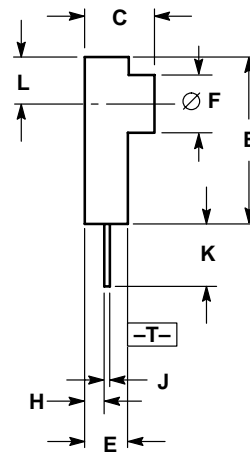
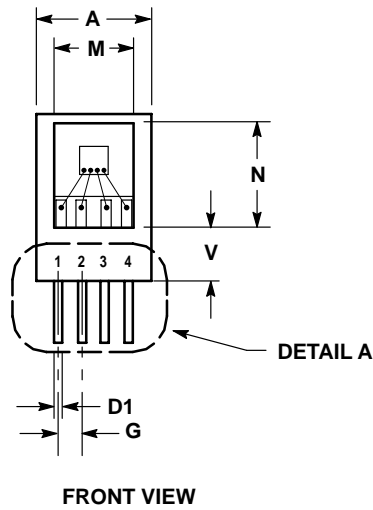
OPERATING CHARACTERISTICS

Table 2. Operating Characteristics ($V_S = 10$ Vdc, $T_A = 25^\circ\text{C}$ unless otherwise noted, $P_1 > P_2$)

Characteristic	Symbol	Min	Typ	Max	Unit
Pressure Range ⁽¹⁾	P_{OP}	0	—	10	kPa
Supply Voltage ⁽²⁾	V_S	—	3	10	Vdc
Supply Current	I_o	—	6.0	—	mAdc
Full Scale Span ⁽³⁾	V_{FSS}	24	25	26	mV
Offset ⁽⁴⁾	V_{off}	-1.0	—	1.0	mV
Sensitivity	$\Delta V/\Delta P$	—	2.5	—	mV/kPa
Linearity ⁽⁵⁾	—	-1.0	—	1.0	% V_{FSS}
Pressure Hysteresis ⁽⁵⁾ (0 to 10 kPa)	—	—	± 0.1	—	% V_{FSS}
Temperature Hysteresis ⁽⁵⁾ (+15°C to +40°C)	—	—	± 0.1	—	% V_{FSS}
Temperature Effect on Full Scale Span ⁽⁵⁾	TCV_{FSS}	-1.0	—	1.0	% V_{FSS}
Temperature Effect on Offset ⁽⁵⁾	TCV_{off}	-1.0	—	1.0	mV
Input Impedance	Z_{in}	1300	—	2550	Ω
Output Impedance	Z_{out}	1400	—	3000	Ω
Response Time ⁽⁶⁾ (10% to 90%)	t_R	—	1.0	—	ms
Warm-Up	—	—	20	v	ms
Offset Stability ⁽⁷⁾	—	—	± 0.5	—	% V_{FSS}

- 1.0 kPa (kiloPascal) equals 0.145 psi.
- Device is ratiometric within this specified excitation range. Operating the device above the specified excitation range may induce additional error due to device self-heating.
- Full Scale Span (V_{FSS}) is defined as the algebraic difference between the output voltage at full rated pressure and the output voltage at the minimum rated pressure.
- Offset (V_{off}) is defined as the output voltage at the minimum rated pressure.
- Accuracy (error budget) consists of the following:
 - Linearity: Output deviation from a straight line relationship with pressure, using end point method, over the specified pressure range.
 - Temperature Hysteresis: Output deviation at any temperature within the operating temperature range, after the temperature is cycled to and from the minimum or maximum operating temperature points, with zero differential pressure applied.
 - Pressure Hysteresis: Output deviation at any pressure within the specified range, when this pressure is cycled to and from the minimum or maximum rated pressure, at 25°C.
 - TcSpan: Output deviation at full rated pressure over the temperature range of 0 to 85°C, relative to 25°C.
 - TcOffset: Output deviation with minimum rated pressure applied, over the temperature range of 0 to 85°C, relative to 25°C.
- Response Time is defined as the time for the incremental change in the output to go from 10% to 90% of its final value when subjected to a specified step change in pressure.
- Offset stability is the product's output deviation when subjected to 1000 hours of Pulsed Pressure, Temperature Cycling with Bias Test.

PACKAGE DIMENSIONS



NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.240	0.260	6.10	6.60
B	0.350	0.370	8.89	9.40
C	0.140	0.150	3.56	3.81
D1	0.012	0.020	0.30	0.51
D2	0.014	0.022	0.36	0.56
E	0.088	0.102	2.24	2.59
F	0.123	0.128	3.12	3.25
G	0.045	0.055	1.14	1.40
H	0.037	0.047	0.94	1.19
J	0.007	0.011	0.18	0.28
K	0.120	0.140	3.05	3.56
L	0.095	0.105	2.41	2.67
M	0.165	0.175	4.19	4.45
N	0.223	0.239	5.66	6.07
V	0.105	0.115	2.67	2.92
AA	0.095	0.107	2.41	2.72
AB	0.015	0.035	0.38	0.89
AC	0.120	0.175	3.05	4.45
AD	0.100	0.115	2.54	2.92

STYLE 2:
 PIN 1. GND
 2. S+
 3. Vs
 4. S-

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 ISSUE C
 CHIP PAK PACKAGE

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