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SGA3486Z

DC to 5000MHz, CASCADABLE SiGe HBT MMIC AMPLIFIER

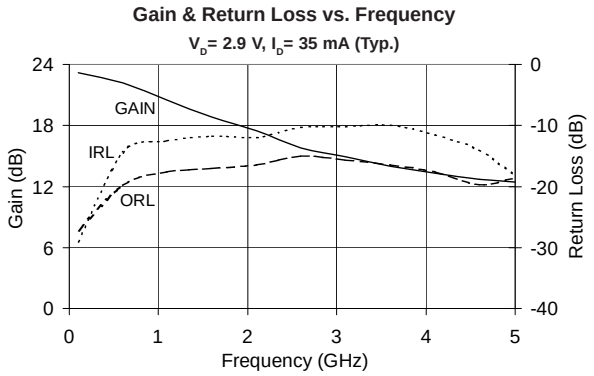
Package: SOT-86

Product Description

The SGA3486Z is a high performance SiGe HBT MMIC Amplifier. A Darlington configuration featuring one micron emitters provides high F_T and excellent thermal performance. The heterojunction increases breakdown voltage and minimizes leakage current between junctions. Cancellation of emitter junction non-linearities results in higher suppression of intermodulation products. Only two DC-blocking capacitors, a bias resistor, and an optional RF choke are required for operation.

Optimum Technology Matching® Applied

- ☐ GaAs HBT
- ☐ GaAs MESFET
- ☐ InGaP HBT
- ☐ SiGe BiCMOS
- ☐ Si BiCMOS
- ☑ SiGe HBT
- ☐ GaAs pHEMT
- ☐ Si CMOS
- ☐ Si BJT
- ☐ GaN HEMT
- ☐ RF MEMS



Features

- High Gain: 18.0dB at 1950MHz
- Cascadable 50Ω
- Operates from Single Supply
- Low Thermal Resistance Package

Applications

- PA Driver Amplifier
- Cellular, PCS, GSM, UMTS
- IF Amplifier
- Wireless Data, Satellite

Parameter	Specification			Unit	Condition
	Min.	Typ.	Max.		
Small Signal Gain	19.0	21.0	23.0	dB	850MHz
		18.0		dB	1950MHz
		16.5		dB	2400MHz
Output Power at 1dB Compression		12.7		dBm	850MHz
		12.5		dBm	1950MHz
Output Third Intercept Point		24.6		dBm	850MHz
		26.9		dBm	1950MHz
Bandwidth Determined by Return Loss		5000		MHz	>8dB
Input Return Loss		12.2		dB	1950MHz
Output Return Loss		16.3		dB	1950MHz
Noise Figure		3.2		dB	1950MHz
Device Operating Voltage	2.6	2.9	3.2	V	
Device Operating Current	31	35	39	mA	
Thermal Resistance (Junction - Lead)		97		°C/W	

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Absolute Maximum Ratings

Parameter	Rating	Unit
Max Device Current (I_D)	70	mA
Max Device Voltage (V_D)	4	V
Max RF Input Power	+18	dBm
Max Junction Temp (T_J)	+150	°C
Operating Temp Range (T_L)	-40 to +85	°C
Max Storage Temp	+150	°C

Operation of this device beyond any one of these limits may cause permanent damage. For reliable continuous operation, the device voltage and current must not exceed the maximum operating values specified in the table on page one.

Bias Conditions should also satisfy the following expression:

$I_D V_D < (T_J - T_L)/R_{TH}$, j-l

Caution! ESD sensitive device.

Exceeding any one or a combination of the Absolute Maximum Rating conditions may cause permanent damage to the device. Extended application of Absolute Maximum Rating conditions to the device may reduce device reliability. Specified typical performance or functional operation of the device under Absolute Maximum Rating conditions is not implied.

RoHS status based on EUDirective2002/95/EC (at time of this document revision).

Typical Performance at Key Operating Frequencies

Parameter	Unit	100 MHz	500 MHz	850 MHz	1950 MHz	2400 MHz	3500 MHz
Small Signal Gain	dB	23.2	22.4	21.0	18.0	16.5	14.2
Output Third Order Intercept Point	dBm		25.4	24.6	26.9	28.1	
Output Power at 1dB Compression	dBm		12.3	12.7	12.5	12.4	
Input Return Loss	dB	29.2	15.7	12.9	12.2	10.8	9.4
Output Return Loss	dB	27.2	20.4	18.1	16.3	16.1	16.1
Reverse Isolation	dB	24.9	24.8	24.4	22.9	22.2	19.9
Noise Figure	dB		2.8	2.8	3.2	3.4	

Test Conditions: V_S=5V, I_D=35mA Typ., OIP_3 Tone Spacing=1MHz, P_{OUT} per tone=-5dBm, R_{BIAS}=62Ω, T_L=25°C, Z_S=Z_L=50Ω

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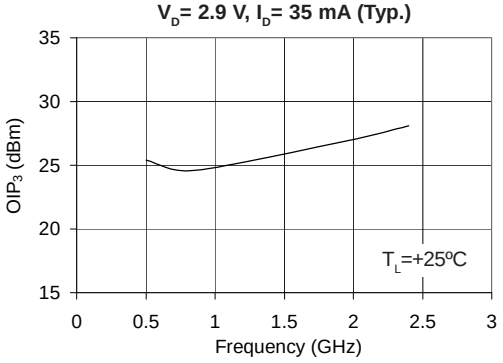
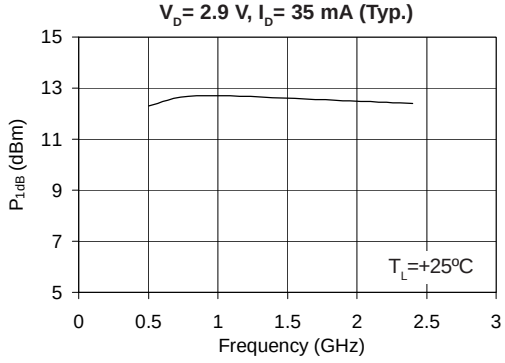
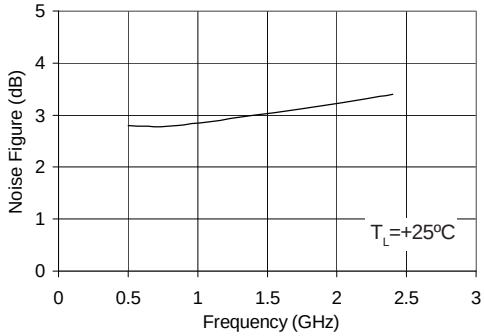
OIP_3 vs. Frequency
V_D= 2.9 V, I_D= 35 mA (Typ.)

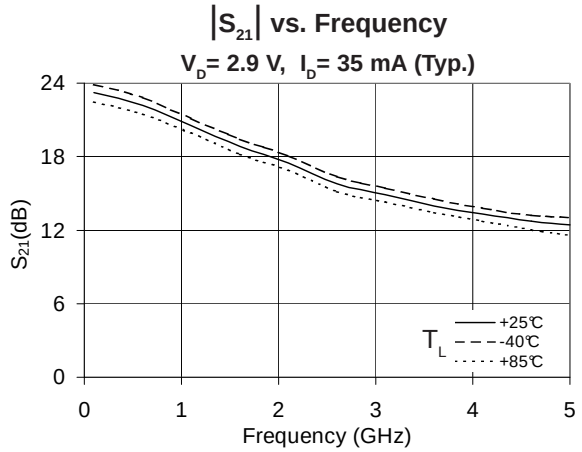


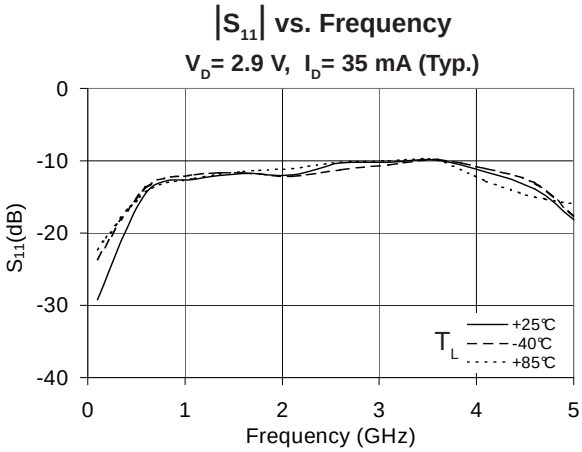
P_{1dB} vs. Frequency
V_D= 2.9 V, I_D= 35 mA (Typ.)

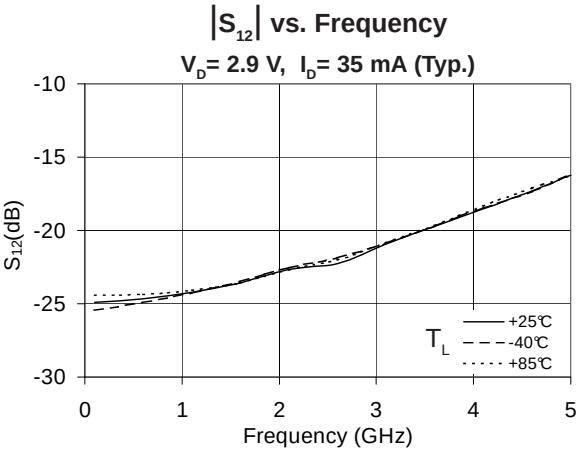


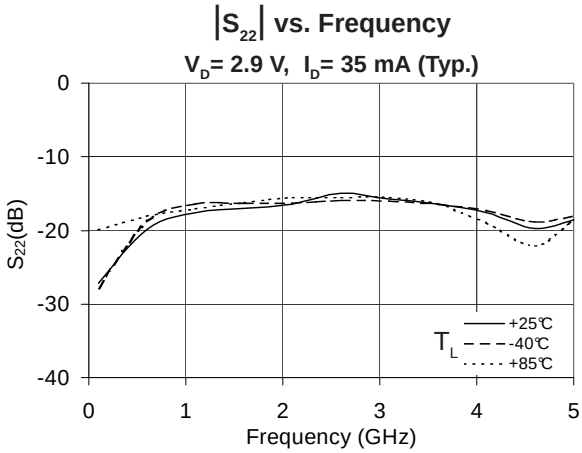
Noise Figure vs. Frequency
V_D= 2.9 V, I_D= 35 mA (Typ.)











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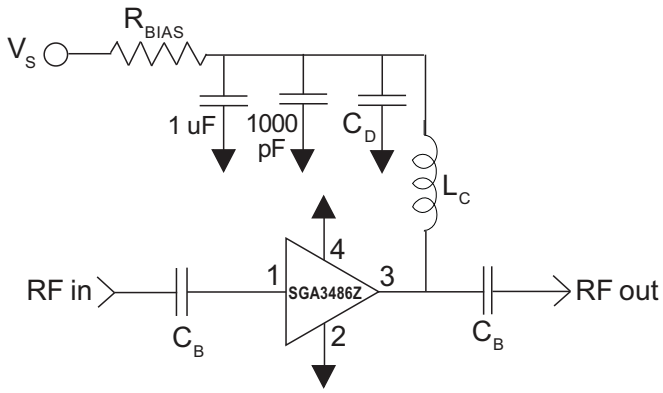
Pin	Function	Description
1	RF IN	RF input pin. This pin requires the use of an external DC-blocking capacitor chosen for the frequency of operation.
2, 4	GND	Connection to ground. For optimum RF performance, use via holes as close to ground leads as possible to reduce lead inductance.
3	RF OUT/BIAS	RF output and bias pin. DC voltage is present on this pin, therefor a DC-blocking capacitor is necessary for proper operation.

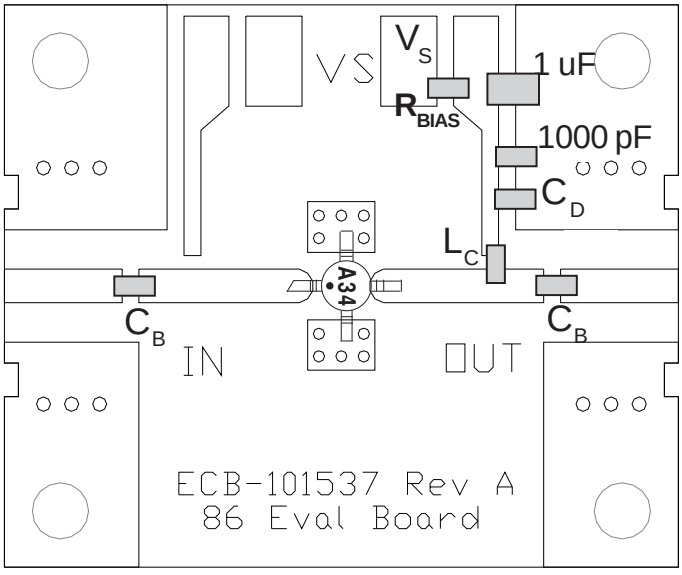
Application Schematic



Reference Designator	Frequency (Mhz)				
	500	850	1950	2400	3500
C_B	220 pF	100 pF	68 pF	56 pF	39 pF
C_D	100 pF	68 pF	22 pF	22 pF	15 pF
L_C	68 nH	33 nH	22 nH	18 nH	15 nH

Recommended Bias Resistor Values for I_D=35mA $R_{BIAS}=(V_S-V_D)/I_D$				
Supply Voltage(V_S)	5 V	8 V	10 V	12 V
R_{BIAS}	68 Ω	150 Ω	200 Ω	270 Ω
Note: R_{BIAS} provides DC bias stability over temperature.				

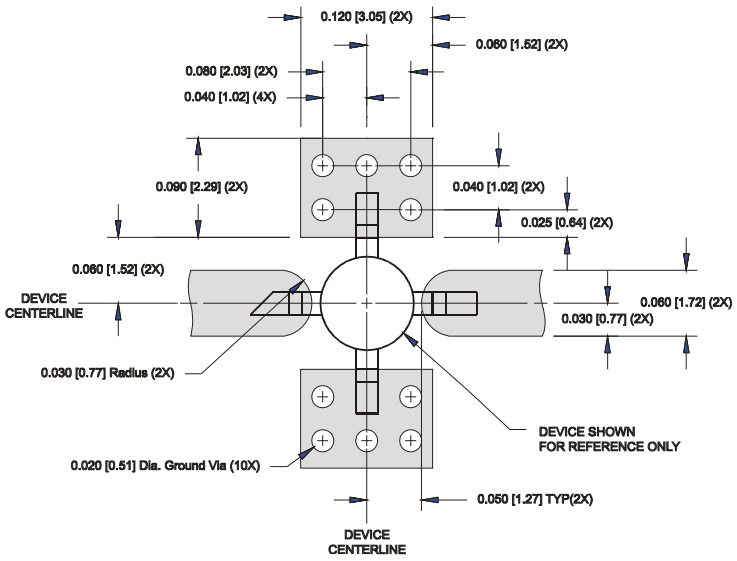
Evaluation Board Layout



Mounting Instructions

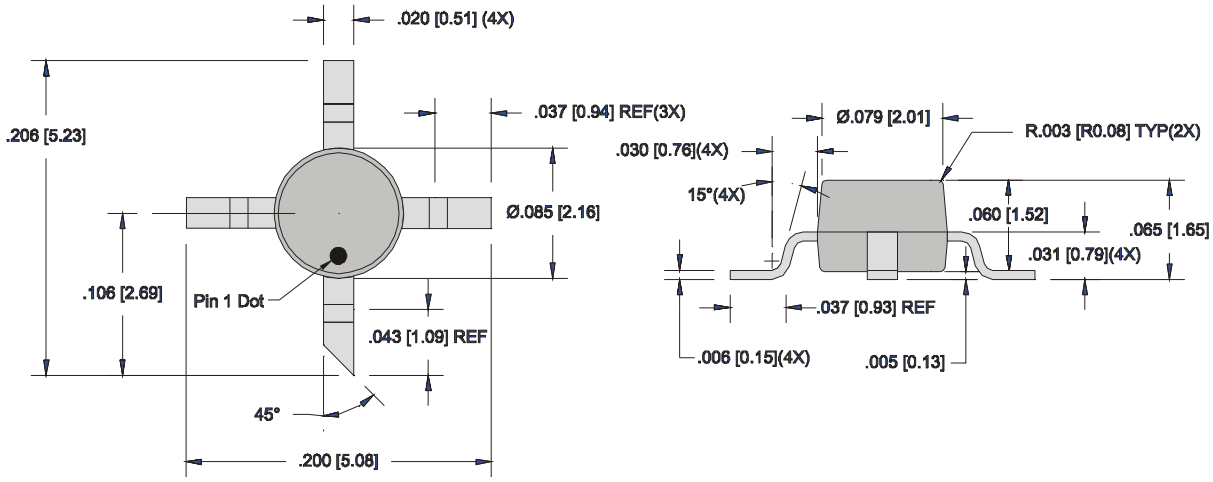
1. Use a large ground pad area under device pins 2 and 4 with many plated through-holes as shown.
2. We recommend 1 or 2 ounce copper. Measurements for this data sheet were made on a 31 mil thick FR-4 board with 1 ounce copper on both sides.

Suggested Pad Layout



Package Drawing

Dimensions in inches (millimeters)
Refer to drawing posted at www.rfmd.com for tolerances.



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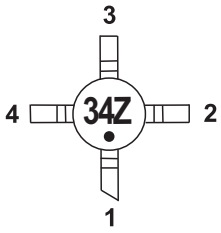
Part Identification



Ordering Information

Ordering Code	Description
SGA3486Z	13" Reel with 3000 pieces
SGA3486ZSQ	Sample bag with 25 pieces
SGA3486ZSR	7” Reel with 100 pieces
SGA3486ZPCK1	850MHz, 5V Operation PCBA with 5-piece sample bag

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