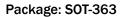




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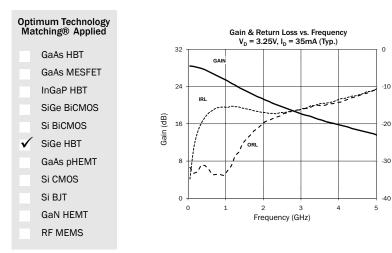
DC to 5000 MHz, CASCADABLE SiGe HBT MMIC AMPLIFIER





Product Description

The SGA3563Z is a high performance SiGe HBT MMIC Amplifier. A Darlington configuration featuring one-micron emitters provides high F_T and excellent thermal performance. The heterojunction increases breakdown voltage and minimizes leakage current between junctions. Cancellation of emitter junction non-linearities results in higher suppression of intermodulation products. Only two DC-blocking capacitors, a bias resistor, and an optional RF choke are required for operation.



Features

- High Gain: 25.5 dB at 850 MHz
- Cascadable 50Ω Gain Block
- High Output IP₃: 24.5dBm typ. at 1950MHz
- Low Noise Figure: 2.7 dB typ. at 1950 MHz
- Low Current Draw: 35 mA typ.
- Single Voltage Supply Operation

Applications

- PA Driver Amplifier
- Cellular, PCS, GSM, UMTS
- IF Amplifier
- Wireless Data, Satellite

Parameter	Specification			Unit	Condition	
	Min.	Тур.	Max.	Unit	Condition	
Small Signal Gain	23.5	25.5	27.5	dB	850MHz	
	19.5	21.5	23.5	dB	1950MHz	
		20.0		dB	2400MHz	
Output Power at 1dB Compression		13.0		dBm	850MHz	
	11.0	12.5		dBm	1950MHz	
Output Third Intercept Point		24.0		dBm	850MHz	
	22.5	24.5		dBm	1950MHz	
Bandwidth Determined by Return Loss		5000		MHz	>10dB	
Input Return Loss	11.2	15.5		dB	1950MHz	
Output Return Loss	11.2	20.0		dB	1950MHz	
Noise Figure		2.7	3.7	dB	1950MHz	
Device Operating Voltage	3.0	3.25	3.5	V		
Device Operating Current	31	35	39	mA		
Thermal Resistance (Junction - Lead)		255		°C/W		

(dB)

-oss

Return

Test Conditions: I_D =35mA Typ., T_L =25°C, Z_S = Z_L =50 Ω , P_{OUT} per tone=-5dBm, OIP₃ Tone Spacing=1MHz

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Absolute Maximum Ratings

Parameter	Rating	Unit				
Max Device Current (I _D)	70	mA				
Max Device Voltage (V _D)	6	V				
Max RF Input Power	+18	dBm				
Max Junction Temp (T _J)	+150	°C				
Operating Temp Range (T _L)	-40 to +85	°C				
Max Storage Temp	+150	°C				
ESD Rating - Human Body Model (HBM)	Class 1A					
Moisture Sensitivity Level (MSL)	MSL 1					

Operation of this device beyond any one of these limits may cause permanent damage. For reliable continuous operation, the device voltage and current must not exceed the maximum operating values specified in the table on page one. Bias Conditions should also satisfy the following expression: $I_D V_D < (T_J - T_L) / R_{TH}$, j-l

Parameter	Unit	100	500	850	1950	2400	3500
		MHz	MHz	MHz	MHz	MHz	MHz
Small Signal Gain	dB	28.5	27.5	25.5	21.5	20.0	17.0
Output Third Order Intercept Point	dBm	24.0	23.6	24.0	24.5	24.0	22.0
Output Power at 1dB Compression	dBm	13.0	13.0	13.0	12.5	12.0	10.0
Input Return Loss	dB	29.7	17.6	15.6	15.5	17.2	14.7
Output Return Loss	dB	31.8	31.1	33.6	20.0	17.9	15.0
Reverse Isolation	dB	29.4	29.3	28.6	25.5	23.9	21.3
Noise Figure	dB	2.3	2.3	2.3	2.7		

Test Conditions: $I_D = 35 \text{ mA Typ.}$, OIP₃ Tone Spacing=1MHz, P_{OUT} per tone=-5dBm, $R_{BIAS} = 100\Omega$, $T_L = 25^{\circ}$ C, $Z_S = Z_L = 50\Omega$



Caution! ESD sensitive device.

Exceeding any one or a combination of the Absolute Maximum Rating conditions may cause permanent damage to the device. Extended application of Absolute Maximum Rating conditions to the device may reduce device reliability. Specified typical perfor-mance or functional operation of the device under Absolute Maximum Rating condi-tions is not implied.

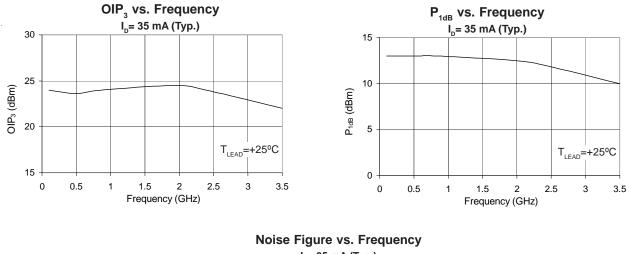
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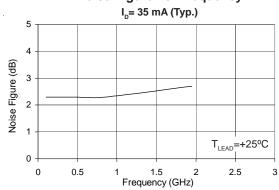


RFMD Green: RoHS compliant per EU Directive 2002/95/EC, halogen free per IEC 61249-2-21, < 1000 ppm each of antimony trioxide in polymeric materials and red phosphorus as a flame retardant, and <2% antimony in solder.



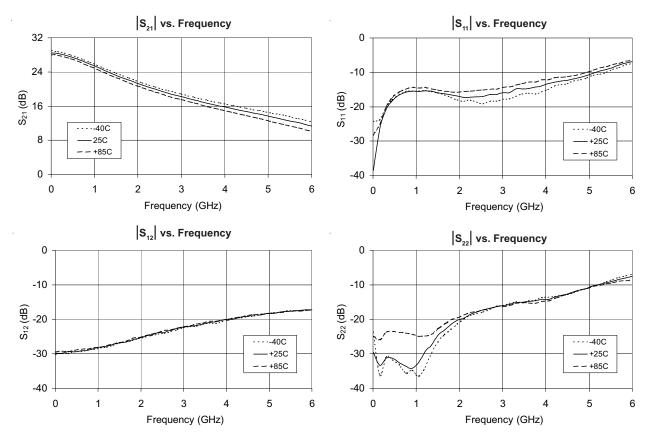








Typical RF Performance Over Lead Temperature – Bias: ID= 35 mA (Typ.) at TLEAD = +25°C

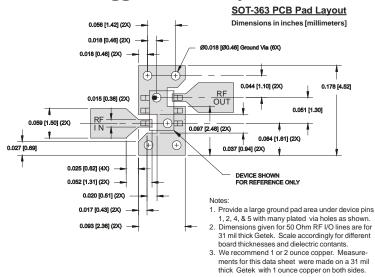




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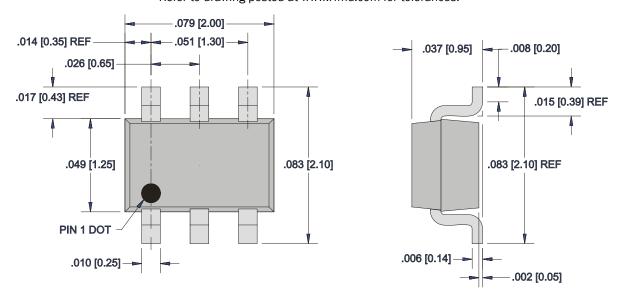
Pin	Function	Description
3	RF IN	RF input pin. This pin requires the use of an external DC-blocking capacitor chosen for the frequency of operation.
1, 2, 4, 5	GND	Connection to ground. For optimum RF performance, use via holes as close to ground leads as possible to reduce lead inductance.
6	RF OUT/BIAS	RF output and bias pin. DC voltage is present on this pin, therefor a DC-blocking capacitor is necessary for proper opera- tion.

Suggested Pad Layout

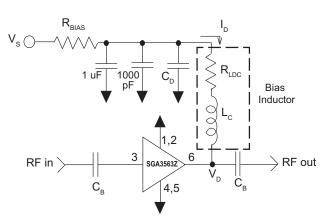


Package Drawing

Dimensions in inches (millimeters) Refer to drawing posted at www.rfmd.com for tolerances.







Application Schematic

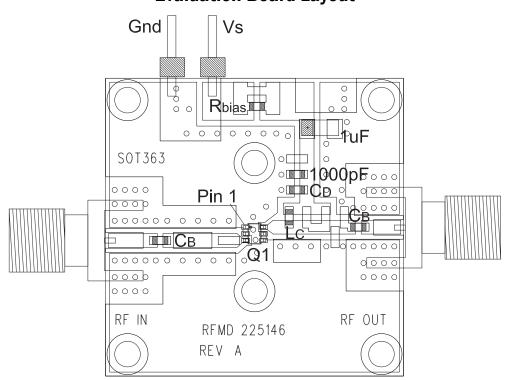
Application Circuit Element Values							
Reference	Reference Frequency (Mhz)						
Designator	100	500	850	1950	2400	3500	
C _B	1000 pF	220 pF	100 pF	68 pF	56 pF	39 pF	
C _D	100 pF	100 pF	68 pF	22 pF	22 pF	15 pF	
L _c	470 nH	68 nH	33 nH	22 nH	18 nH	15 nH	

Recommended Bias Resistance for I_{D} = 35 mA							
Supply Voltage (V _s) (Volts)	< 5	5	6	7	8	9	10
Bias Resistance* (Ohms)	N/R	50	79	107	136	164	193

* Bias Resistance = $R_{BIAS} + R_{LDC} = (V_{s} - V_{D}) / I_{D}$

Select $R_{_{BIAS}}$ so that $R_{_{BIAS}} + R_{_{LDC}} \sim$ the recommended bias resistance. Use 1% or 5% tolerance resisistors or parallel combinations to attain the recommended bias resistance +/- 3%. $R_{_{BIAS}}$ provides current stability over temperature.

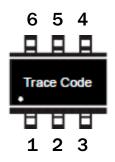
* N/R=Not Recommended. Contact Sirenza technical support for guidance when available supply voltage is less than 5 Volts.



Evaluation Board Layout



Part Identification



Ordering Information

Ordering Code	Description
SGA3563Z	7" Reel with 3000 pieces
SGA3563ZSQ	Sample Bag with 25 pieces
SGA3563ZSR	7" Reel with 100 pieces
SGA3563ZPCK1	850MHz, 5V Operation PCBA with 5-piece sample bag