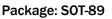
SGA5289Z



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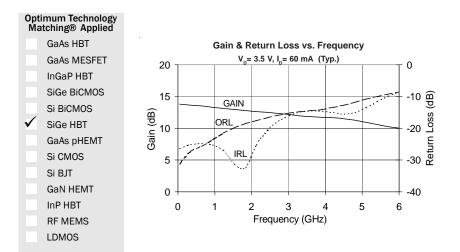
DC to 5000 MHz, CASCADABLE SiGe HBT MMIC AMPLIFIER





Product Description

The SGA5289Z is a high performance SiGe HBT MMIC Amplifier. A Darlington configuration featuring one-micron emitters provides high $F_{\rm T}$ and excellent thermal performance. The heterojunction increases breakdown voltage and minimizes leakage current between junctions. Cancellation of emitter junction non-linearities results in higher suppression of intermodulation products. Only two DC-blocking capacitors, a bias resistor, and an optional RF choke are required for operation.



Features

- High Gain: 12.7 dB at 1950MHz
- Cascadable 50Ω
- Operates from Single Supply
- Low Thermal Resistance Package

Applications

- PA Driver Amplifier
- Cellular, PCS, GSM, UMTS
- IF Amplifier
- Wireless Data, Satellite

Parameter	Specification			Unit	Condition	
	Min.	Тур.	Max.	Unit	Condition	
Small Signal Gain	12.1	13.4	14.7	dB	850MHz	
		12.7		dB	1950MHz	
		12.5		dB	2400MHz	
Output Power at 1dB Compression		15.8		dBm	850MHz	
		14.4		dBm	1950MHz	
Output Third Intercept Point		31.8		dBm	850MHz	
		28.1		dBm	1950MHz	
Bandwidth Determined by Return Loss		5000		MHz	>10dB	
Input Return Loss		29.2		dB	1950MHz	
Output Return Loss		18.1		dB	1950MHz	
Noise Figure		3.8		dB	1950MHz	
Device Operating Voltage	3.1	3.5	3.9	V		
Device Operating Current	54	60	66	mA		
Thermal Resistance (Junction - Lead)		97		°C/W		

Test Conditions: $V_S = 8V$, $I_D = 60$ mA Typ., OIP₃ Tone Spacing=1MHz, P_{OUT} per tone=-5dBm, $R_{BIAS} = 75\Omega$, $T_L = 25$ °C, $Z_S = Z_L = 50\Omega$

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SGA5289Z



Absolute Maximum Ratings

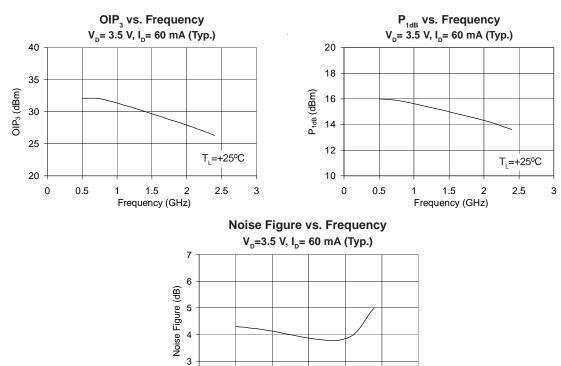
	-	
Parameter	Rating	Unit
Max Device Current (I_D)	120	mA
Max Device Voltage (V _D)	5	V
Max RF Input Power	+16	dBm
Max Junction Temp (T _J)	+150	°C
Operating Temp Range (T_L)	-40 to +85	°C
Max Storage Temp	+150	°C
Moisture Sensitivity Level	MSL 2	

Operation of this device beyond any one of these limits may cause permanent damage. For reliable continuous operation, the device voltage and current must not exceed the maximum operating values specified in the table on page one. Bias Conditions should also satisfy the following expression: $I_D V_D < (T_J - T_L) / R_{TH}$, j-l

Typical Performance at Key Operating Frequencies

Parameter	Unit	100	500	850	1950	2400	3500
		MHz	MHz	MHz	MHz	MHz	MHz
Small Signal Gain	dB	13.8	13.6	13.4	12.7	12.5	11.9
Output Third Order Intercept Point	dBm		32.1	31.8	28.1	26.3	
Output Power at 1dB Compression	dBm		16.0	15.8	14.4	13.6	
Input Return Loss	dB	26.8	24.8	25.1	29.2	21.1	14.6
Output Return Loss	dB	29.5	26.4	24.1	18.1	16.7	14.5
Reverse Isolation	dB	18.3	18.3	18.5	19.5	19.7	20.0
Noise Figure	dB		4.3	4.2	3.8	5.0	

Test Conditions: $V_S=8V$, $I_D=60$ mA Typ., OIP₃ Tone Spacing=1MHz, P_{OUT} per tone=-5dBm, $R_{BIAS}=75\Omega$, $T_L=25$ °C, $Z_S=Z_L=50\Omega$



2 0

0.5

1

Caution! ESD sensitive device.

Exceeding any one or a combination of the Absolute Maximum Rating conditions may cause permanent damage to the device. Extended application of Absolute Maximum Rating conditions to the device may reduce device reliability. Specified typical perfor-mance or functional operation of the device under Absolute Maximum Rating condi-tions is not implied.

RoHS status based on EUDirective2002/95/EC (at time of this document revision).

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1.5

Frequency (GHz)

2

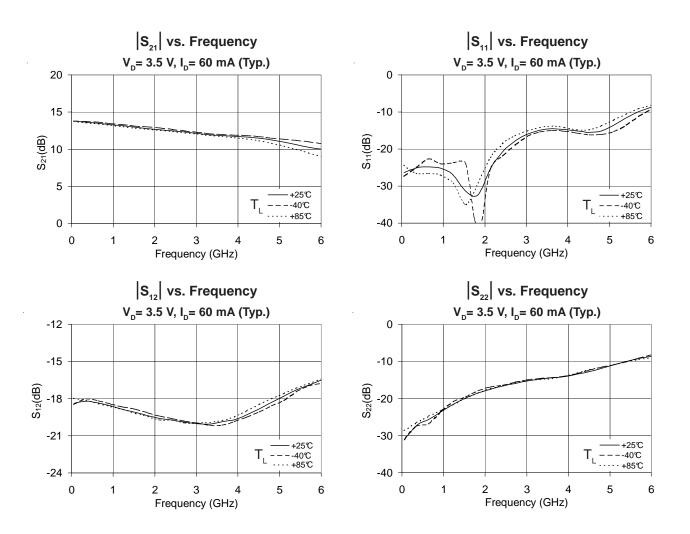
T_=+25°C

2.5

3







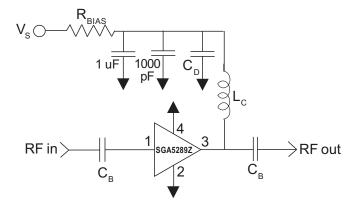
SGA5289Z

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Pin	Function	Description
1	RF IN	RF input pin. This pin requires the use of an external DC-blocking capacitor chosen for the frequency of operation.
2, 4	GND	Connection to ground. For optimum RF performance, use via holes as close to ground leads as possible to reduce lead inductance.
3	RF OUT/BIAS	RF output and bias pin. DC voltage is present on this pin, therefor a DC-blocking capacitor is necessary for proper opera- tion.

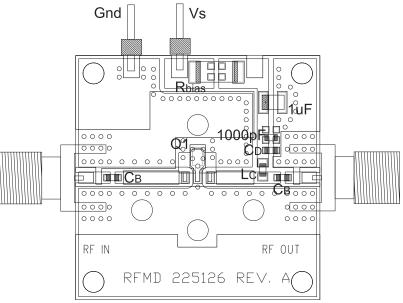
Application Schematic



Reference	Frequency (Mhz)							
Designator	500	850	1950	2400	3500			
C _B	220 pF	100 pF	68 pF	56 pF	39 pF			
C _D	100 pF	68 pF	22 pF	22 pF	15 pF			
L _c	68 nH	33 nH	22 nH	18 nH	15 nH			

Recommended Bias Resistor Values for I_{D} =60mA R_{BIAS} =(V_{S} - V_{D}) / I_{D}				
Supply Voltage(V _s)	6 V	8 V	10 V	12 V
R _{BIAS} 43 Ω 75 Ω 110 Ω 150 Ω				
Note: R_{BIAS} provides DC bias stability over temperature.				

Evaluation Board Layout



Mounting Instructions:

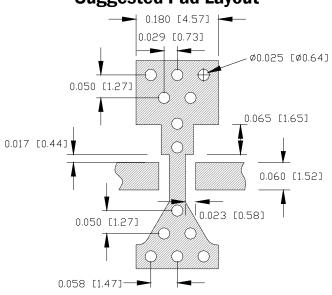
1. Solder the copper pad on the backside of the device package to the ground plane.

2. Use a large ground pad area with many plated through-holes as shown.

3. We recommend 1 or 2 ounce copper. Measurements for this data sheet were made on a 31mil thick FR-4 board with 1 ounce copper on both sides.



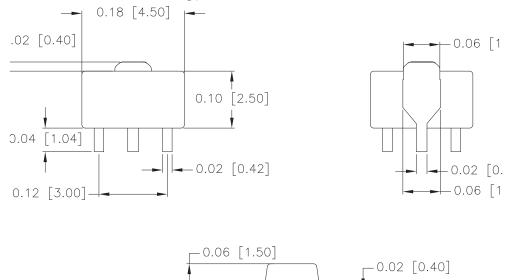




Package Drawing

Dimensions in inches (millimeters)

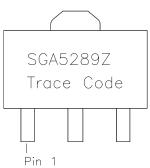
Refer to drawing posted at www.rfmd.com for tolerances.



Suggested Pad Layout



Part Identification



Ordering Information

Ordering Code	Description
SGA5289Z	13" Reel with 3000 pieces
SGA5289ZSQ	Sample bag with 25 pieces
SGA5289ZSR	7" Reel with 100 pieces
SGA5289ZPCK1	850MHz, 8V Operation PCBA with 5-piece sample bag