

RF3857 DUAL CHANNEL LNA WITH BYPASS MODE

Package Style: QFN, 16-Pin, 3mmx3mmx0.45mm



Features

- Low Noise and High Intercept Point
- Adjustable Bias Current
- Power Down Control
- Low Insertion Loss Bypass Feature
- 1.8V to 4V Operation (See "Bias Note" on Page 3)
- 0.9GHz to 4.0GHz Operation

Applications

- WiFi LNA with Bypass Feature
- WiMAX LNA with Bypass Feature
- CDMA PCS LNA with Bypass Feature
- Suitable for 1x2 or 2x1 MIMO Applications
- **Commercial and Consumer** . Systems



Functional Block Diagram

Product Description

The RF3857 is a dual channel switchable low noise amplifier with a very high dynamic range designed for digital cellular, WiMAX, and WiFi applications. The device functions as an outstanding front end low noise amplifier. The bias current may be set externally. The RF3857 combines two receive paths, which is ideal in an application that requires two receive paths, such as 1x2 and 2x2 MIMO for both WiFi and WiMax aplications. The IC is featured in a standard QFN, 16-pin, 3mmx3mm plastic package.

Optimum Technology Matching® Applied					
🗹 GaAs HBT	□ SiGe BiCMOS	🗌 GaAs pHEMT	GaN HEMT		
GaAs MESFET	Si BiCMOS	Si CMOS	RF MEMS		

SiGe HBT

Si BJT

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Absolute Maximum Ratings

Parameter	Rating	Unit
Supply Voltage	-0.5 to +6.0	V _{DC}
Input RF Level	+5 (see note)	dBm
Current Drain, I _{CC} per Channel	32	mA
Operating Ambient Temperature	-40 to +85	°C
Storage Temperature	-40 to +150	°C

NOTE: Exceeding any one or a combination of the above maximum rating limits may cause permanent damage. Input RF transients to +15dBm will not harm the device. For sustained operation at inputs \geq +5dBm, a small dropping resistor is recommended in series with the V_{CC} in order to limit the current due to self-biasing to <32mA per channel.



Exceeding any one or a combination of the Absolute Maximum Rating conditions may cause permanent damage to the device. Extended application of Absolute Maximum Rating conditions to the device may reduce device reliability. Specified typical performance or functional operation of the device under Absolute Maximum Rating conditions is not implied.

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Parameter	Specification		Unit	Condition	
Falanielei	Min.	Тур.	Max.	Onic	Condition
Typical Operating Conditions					Temp=25°C, VCC=3.3V, VREF=3.3V, Frequency=2450 MHz for WiBro/WiFi Tune and 3500 MHz for WiMax Tune, Gain Select=Low or High depending on the test unless otherwise noted in the condition column)
Frequency Range	900		4000	MHz	
WiBRO/WiFi Low Noise Amplifier					
Frequency	2300	2450	2900	MHz	
HIGH GAIN MODE					Gain Select<0.8V, V _{REF} =3.3V, T=+25°C
Gain	12.0	14.0	17.0	dB	RFIN1 to RFOUT1 or RFIN2 to RFOUT2 receive paths over full temp range, frequency, and process.
Noise Figure	1.0	1.3	1.55	dB	Over full temp range, frequency, and process.
Input IP3	+7.0	+9.0		dBm	IIP3 will improve if ICC is raised above 7 mA.
Output VSWR		1.7:1	2:1		
Total Current Drain (per channel)		8.5		mA	Total current includes I _{CC} +I _{REF}
Channel Isolation		-40		dBc	Difference between the P _{OUT} at RFOUT1 and RFOUT2 when signal is applied at RFIN1 or RFIN2
BYPASS MODE					Gain Select>1.8V, V _{REF} =0V, V _{CC} =3.3V
Gain	-3.0	-2.5	-2.0	dB	Both RX paths, over full temp range and process. Note: Bypass mode insertion loss will degrade gradually as V_{CC} goes below 2.7V.
Input IP3	+19.5	+21.0	+23.0	dBm	For each RX path, over full tmep range, and process.
Output VSWR		1.6:1			
Total Current Drain		2.0	3.0	mA	Total current includes I _{CC} +I _{REF}
WiMAX Low Noise Amplifier					
HIGH GAIN MODE					Gain Select<0.8V, V _{REF} =3.3V, V _{CC} =3.3V
Frequency	3100	3500		MHz	
Gain	10	12	14.5	dB	RFIN1 to RFOUT1 or RFIN2 to RFOUT2 receive paths over full temp range, frequency, and process.





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Parameter	Min.	Тур.	Max.	Unit	Condition
WiMAX Low Noise Amplifier, cont.					
Noise Figure	1.2	1.7	2.0	dB	Over full temp range, frequency range, and process.
IIP3		9		dBm	
Total Current Drain (per channel)		8.5	10	mA	Total current drain includes I _{CC} +I _{REF.} VCC=3.3V, VREF=3.3V, Gain select<1.8V.
BYPASS MODE					Gain Select>1.8V, V _{REF} =0V
Gain		-3		dB	Note: Bypass mode insertion loss will degrade gradually as $\rm V_{\rm CC}$ goes below 2.7 V.
Input IP3		20		dBm	
Total Current Drain (per channel)		2		mA	Total current drain includes I _{CC} +I _{REF}
Power Supply					
Voltage (V _{CC})	1.8	3.0	5.0	V	See bias note
V _{SELECT} Low			0.8	V	High Gain mode. Gain Select<0.8V, V _{REF} =3.3V
V _{SELECT} High	1.8			V	Low Gain mode. Gain Select>1.8V, V _{REF} =0V
Power Down per RX Path	0		10	μΑ	Gain Select<0.8V, V _{REF} =0V, V _{CC} =3.3V (Over full temp range, frequency, and beta)
VREF1 or VREF 2 Turn On/Off		100	<150	nSec	For faster turn on and off time C1 and C2 should be changed from 22nF to a value between 10pF to 100pF
Gain Select 1 or 2 Turn On/Off		100	<150	nSec	For faster turn on and off time C1 and C2 should be changed from 22nF to a value between 3.0pF to 100pF

Bias note: Due to the presence of ESD protection circuitry on the RF3857, the maximum allowable collector bias voltage (pin 4) is 4.0V. Higher supply voltages such as 5V are permissible if a series resistor is used to drop V_{CC} to ≤4.0V for a given I_{CC}.





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Pin	Function	Description	Interface Schematic
1	GAIN SEL1	This pin selects high gain and bypass modes for Amplifier 1. Gain Sel1≤0.8V, Amp1 high gain. Gain Sel1≥1.8V, Amp1 bypass.	
2	GND	Amplifier 1 ground connection. For best performance, keep traces physi- cally short and connect immediately to ground plane.	
3	RFOUT1	Amplifier 1 output pin. This pin is an open-collector output. It must be biased to VCC through a choke or matching inductor. This pin is matched to 50Ω with the network shown in the evaluation board schematic.	
4	N/C	No internal connection.	
5	RFIN1	RF input pin for amplifier 1. This part is designed such that 50Ω is the optimal source impedance for best noise figure. Best noise figure is achieved with only a series capacitor on the input.	To Bias Circuit RF IN O
6	GND	Isolation ground connection. Can be grounded or not connected.	
7	GND	See pin 7.	
8	RFIN2	RF input pin for amplifier 2. See pin 5.	
9	N/C	No internal connection.	
10	RFOUT2	Amplifier 2 output pin. See pin 3.	
11	GND	Amplifier 2 ground connection. See pin 2.	
12	GAIN SEL2	Selects high gain and bypass modes for Amplifier 2. See pin 1.	
13	VREF2	Bias control for amplifier 2. An external resistor can be used to set the bias current for any V_{REF} voltage.	V BIAS
14	N/C	No internal connection.	
15	N/C	No internal connection.	
16	VREF1	Bias control for amplifier 1. See pin 13.	



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Package Drawing



NOTES: Shaded Area is Pin 1 Indicator 1



Evaluation Board Schematic

WiBRO/WiFi (2.3GHz to 2.7GHz)



Note: for best turn on and OFF time for both RX paths C1 and C2 should be changed to a value between 10 pF to 100 pF.





Evaluation Board Schematic

WiMAX (3.1GHz to 3.8GHz)







WiBRO/WiFi DATA



2200.0 2300.0 2400.0 2500.0 2600.0 2700.0 2800.0 2900.0 Frequency (MHz)

ICC + IREF @ 85°C

14.0

12.0

2200.0 2300.0 2400.0 2500.0 2600.0 2700.0 2800.0 2900.0

Frequency (MHz)

5.0



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WIMAX DATA









Frequency (MHz)

IIP3 @ WiMAX Band in High Gain Mode





Icc + IREF @ WIMAX Band in High Gain Mode





Evaluation Board Layout Board Size 1.0" x 1.5"

Board Thickness 0.032", Board Material FR-4









RF3857

Ordering Information

Ordering Code	Description
RF3857	Standard 25 piece bag
RF3857SR	Standard 100 piece reel
RF3857TR7	Standard 2500 piece reel
RF3857PCK-410	Fully assembled evaluation board tuned for 2.0GHz to 3.0GHz and 5 loose sample pieces
RF3857PCK-411	Fully assembled evaluation board tuned for 3.1GHz to 4.0GHz and 5 loose sample pieces