

RF2370 3V LOW NOISE AMPLIFIER

Package Style: SOT 6-Lead

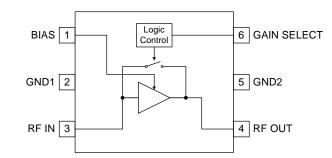


Features

- Low Noise and High Intercept Point
- Adjustable Bias Current
- Power Down Control
- Low Insertion Loss Bypass Feature
- 1.8V to 5V Operation (See Note: Page 2)
- 1.5 GHz to 3.8 GHz Operation

Applications

- WiFi LNA with Bypass Feature
- CDMA PCS LNA with Bypass Feature
- MMDS LNA with Bypass Feature
- General Purpose Amplification
- Commercial and Consumer Systems



Functional Block Diagram

Product Description

The RF2370 is a switchable low noise amplifier with a very high dynamic range designed for digital cellular and WiFi applications. The device functions as an outstanding front end low noise amplifier. The bias current may be set externally. The IC is featured in a standard SOT 6-lead plastic package.

Ordering Information

| 0 | |
|---------------|---|
| RF2370 | Standard 25 piece bag |
| RF2370SR | Standard 100 piece reel |
| RF2370TR7 | Standard 2500 piece reel |
| RF2370PCK-410 | Fully assembled evaluation board tuned for 1900 to 4000 |
| | MHz and 5 loose sample pieces |

Optimum Technology Matching® Applied

| 🗹 GaAs HBT | □ SiGe BiCMOS | GaAs pHEMT | 🗌 GaN HEMT |
|-------------|---------------|------------|------------|
| GaAs MESFET | 🗌 Si BiCMOS | 🗌 Si CMOS | |
| 🗌 InGaP HBT | SiGe HBT | 🗌 Si BJT | |

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Absolute Maximum Ratings

| Parameter | Rating | Unit | | |
|--------------------------------|---------------|-----------------|--|--|
| Supply Voltage | -0.5 to +6.0 | V _{DC} | | |
| Input RF Level | +5 (see note) | dBm | | |
| Current Drain, I _{CC} | 32 | mA | | |
| Operating Ambient Temperature | -40 to +85 | °C | | |
| Storage Temperature | -40 to +150 | °C | | |

NOTE: Exceeding any one or a combination of the above maximum rating limits may cause permanent damage. Input RF transients to +15dBm will not harm the device. For sustained operation at inputs \geq +5dBm, a small dropping resistor is recommended in series with the V_{CC} in order to limit the current due to self-biasing to <32mA.

Caution! ESD sensitive device.

Exceeding any one or a combination of the Absolute Maximum Rating conditions may cause permanent damage to the device. Extended application of Absolute Maximum Rating conditions to the device may reduce device reliability. Specified typical performance or functional operation of the device under Absolute Maximum Rating conditions is not implied.

RoHS status based on EUDirective2002/95/EC (at time of this document revision).

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| Parameter | Specification | | | Unit | Condition | |
|---------------------------|---------------|-------|------|------|--|--|
| Farameter | Min. | Тур. | Max. | Unit | Condition | |
| Operating Range | | | | | T _{AMB} =+25°C, V _{CC} =3.0V | |
| Frequency Range | 900 | | 4000 | MHz | | |
| WiBRO/WiFi/WiMAX Low | | | | | | |
| Noise Amplifier | | | | | | |
| Frequency | 2300 | | 2700 | MHz | | |
| HIGH GAIN MODE | | | | | Gain Select<0.8V, V _{BIAS} =3V, T=+25°C | |
| Gain | 12.0 | 14.0 | | dB | | |
| Noise Figure | | 1.3 | 1.5 | dB | | |
| Input IP3 | | +8 | | dBm | IIP3 will improve if ICC is raised above 7 mA. | |
| Output VSWR | | 1.7:1 | 2:1 | | | |
| Current Drain | | 7 | | mA | Current Drain=I _{CC} +I _{REF} | |
| BYPASS MODE | | | | | Gain Select>1.8V, V _{BIAS} =0V | |
| Gain | -4.0 | -3.0 | -2.0 | dB | Note: Bypass mode insertion loss will degrade gradually as V_{CC} goes below 2.7V. | |
| Input IP3 | +18.0 | +20.0 | | dBm | | |
| Output VSWR | | 1.6:1 | | | | |
| Current Drain | | 2.9 | 3.0 | mA | Current Drain=I _{CC} +I _{REF} | |
| WiMAX Low Noise Amplifier | | | | | | |
| Frequency | 3100 | 3500 | 3800 | MHz | | |
| Gain | | 11 | | dB | Gain Select<0.8V, V _{BIAS} =3V | |
| Noise Figure | | 1.6 | | dB | | |
| Input IP3 | +14 | | | dBm | | |
| BYPASS MODE (Low Gain) | | | | | Gain Select > 1.8 V, V _{BIAS} =0 V | |
| Gain | | -4 | | dBm | 1 | |
| Input IP3 | +18 | | | dBm | | |
| GPS Low Noise Amplifier | | | | | | |
| Frequency | 1500 | 1575 | 1600 | MHz | | |
| Gain | | 17 | | dB | Gain Select<0.8V, V _{BIAS} =3V | |
| Noise Figure | | 1.2 | | dB | | |
| Input IP3 | | +6 | | dBm | | |





| Parameter | | Specification | | | Condition | |
|----------------------------|------|---------------|------|------|--|--|
| | Min. | Тур. | Max. | Unit | Condition | |
| BYPASS MODE (Low Gain) | | | | | Gain Select>1.8V, V _{BIAS} =0V | |
| Gain | -4 | -3 | | dBm | | |
| Input IP3 | +20 | | | dBm | | |
| Power Supply | | | | | | |
| Voltage (V _{CC}) | | 3 | | V | | |
| V _{SELECT} Low | | | 0.8 | V | High Gain mode. Select<0.8V, V _{BIAS} =3V | |
| V _{SELECT} High | 1.8 | | | V | Low Gain mode. Select>1.8V, V _{BIAS} =0V | |
| Power Down | 0 | | 10 | μΑ | Gain Select<0.8V, V _{BIAS} =0V, V _{CC} =3.0V | |

Bias note: Due to the presence of ESD protection circuitry on the RF2370, the maximum allowable collector bias voltage (pin 4) is 4.0V. Higher supply voltages such as 5V are permissible if a series resistor is used to drop V_{CC} to \leq 4.0V for a given I_{CC} .

Bias note 2: In bypass mode, V_{REF} is essentially a "don't care" condition. Pulling V_{REF} low when in bypass mode does conserve the small 1mA to 2mA supplied by V_{REF} .



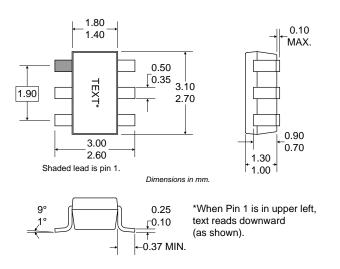


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| Pin | Function | Description | Interface Schematic |
|-----|-------------|---|-------------------------------|
| 1 | BIAS | For low noise amplifier applications, this pin is used to control the bias current. An external resistor can be used to set the bias current for any V_{BIAS} voltage. | V BIAS |
| 2 | GND1 | Ground connection. For best performance, keep traces physically short and connect immediately to ground plane. | |
| 3 | RF IN | RF input pin. This part is designed such that 50Ω is the optimal source impedance for best noise figure. Best noise figure is achieved with only a series capacitor on the input. | To Bias Circuit RF IN O |
| 4 | RF OUT | Amplifier output pin. This pin is an open-collector output. It must be biased to V_{CC} through a choke or matching inductor. This pin is matched to 50Ω with a shunt L, series L topology enhances to stability of the device by reducing the high frequency gain above 6GHz. | |
| 5 | GND2 | See GND1. | |
| 6 | GAIN SELECT | This pin selects high gain and bypass modes. Gain Select ≤ 0.8 V, high gain. Gain Select ≥ 1.8 V, low gain. A series resistor of 100 Ω is required on this pin to enhance stability. | |



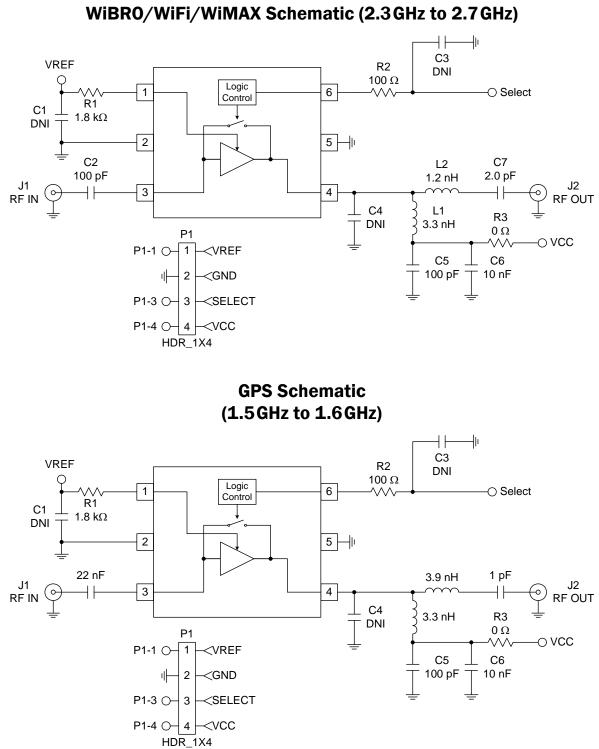




Package Drawing





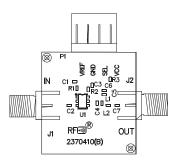


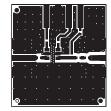
Evaluation Board Schematic

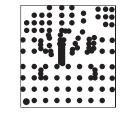




Evaluation Board Layout Board Size 0.835" x 0.900" Board Thickness 0.032", Board Material FR-4





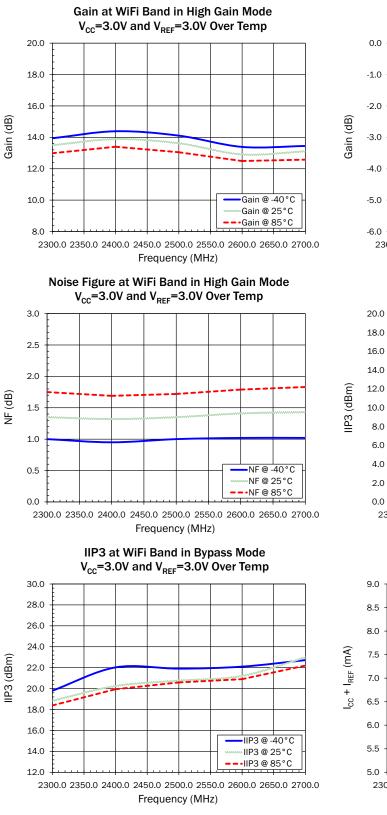


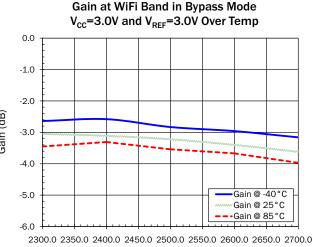


RF2370



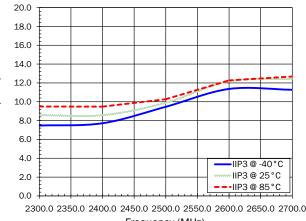
WiBRO/WiFi/WiMAX DATA





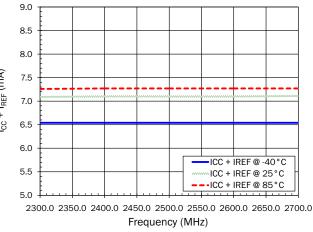
Frequency (MHz)

IIP3 at WiFi Band in High Gain Mode V_{cc} =3.0V and V_{REF} =3.0V Over Temp



Frequency (MHz)

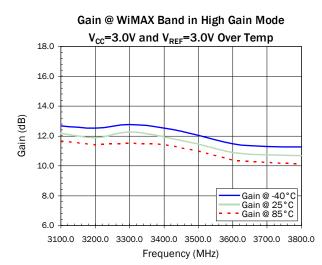
 I_{cc} + I_{REF} at WiFi Band in High Gain Mode V_{cc} =3.0V and V_{REF} =3.0V Over Temp

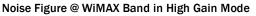


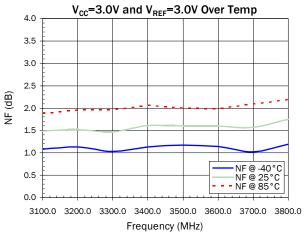


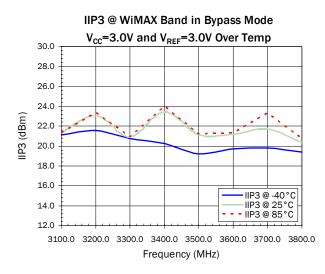


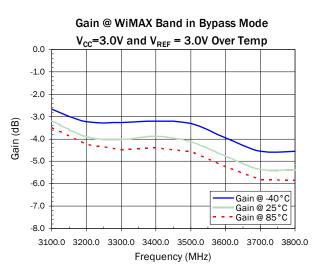
WiMAX Data

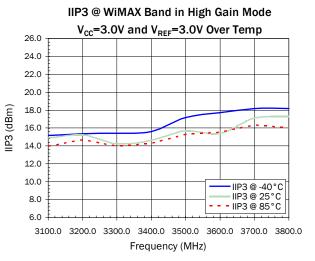


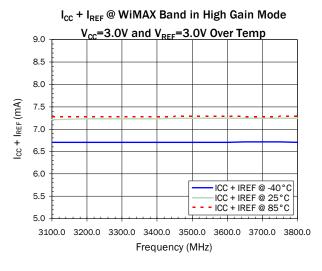










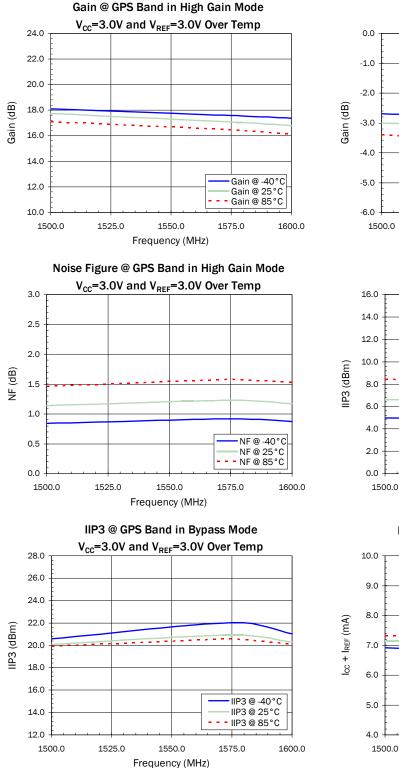


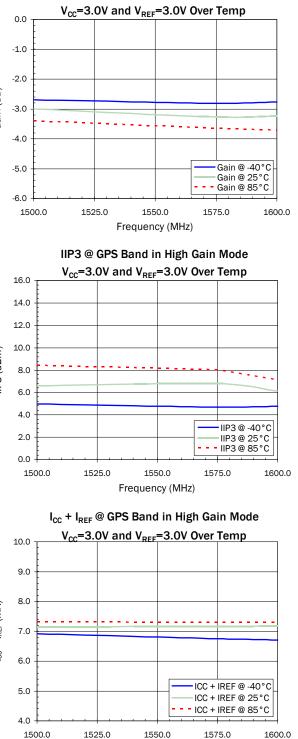
DS110616





GPS Data





Gain @ GPS Band in Bypass Mode



RoHS* Banned Material Content

| RoHS Compliant: | Yes |
|------------------------------------|-------|
| Package total weight in grams (g): | 0.013 |
| Compliance Date Code: | N/A |
| Bill of Materials Revision: | - |
| Pb Free Category: | e3 |

| Bill of Materials | Parts Per Million (PPM) | | | | | | |
|-------------------|-------------------------|----|----|-------|-----|------|--|
| | Pb | Cd | Hg | Cr VI | PBB | PBDE | |
| Die | 0 | 0 | 0 | 0 | 0 | 0 | |
| Molding Compound | 0 | 0 | 0 | 0 | 0 | 0 | |
| Lead Frame | 0 | 0 | 0 | 0 | 0 | 0 | |
| Die Attach Epoxy | 0 | 0 | 0 | 0 | 0 | 0 | |
| Wire | 0 | 0 | 0 | 0 | 0 | 0 | |
| Solder Plating | 0 | 0 | 0 | 0 | 0 | 0 | |

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* DIRECTIVE 2002/95/EC OF THE EUROPEAN PARLIAMENT AND OF THE COUNCIL of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment