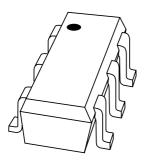
## **DISCRETE SEMICONDUCTORS**

# DATA SHEET



## PBSS5240Y 40 V low V<sub>CEsat</sub> PNP transistor

Product data sheet Supersedes data of 2001 Oct 24 2002 Feb 28



## 40 V low V<sub>CEsat</sub> PNP transistor

**PBSS5240Y** 

#### **FEATURES**

- Low collector-emitter saturation voltage
- · High current capability
- Improved device reliability due to reduced heat generation
- Replacement for SOT89/SOT223 standard packaged transistors due to enhanced performance.

#### **APPLICATIONS**

- · Supply line switching circuits
- · Battery management applications
- DC/DC converter applications
- · Strobe flash units
- Heavy duty battery powered equipment (motor and lamp drivers).

#### **DESCRIPTION**

PNP low V<sub>CEsat</sub> transistor in a SOT363 (SC-88) plastic package.

NPN complement: PBSS4240Y.

#### **MARKING**

TYPE NUMBER	MARKING CODE <sup>(1)</sup>
PBSS5240Y	52*

#### Note

- 1. \* = p: made in Hongkong.
  - \* = t: made in Malaysia.

#### **QUICK REFERENCE DATA**

SYMBOL	PARAMETER	MAX.	UNIT
V <sub>CEO</sub>	collector-emitter voltage	-40	V
I <sub>CM</sub>	peak collector current	-3	Α
I <sub>C</sub>	collector current (DC)	-2	Α
R <sub>CEsat</sub>	equivalent on-resistance	<200	mΩ

#### **PINNING**

PIN	DESCRIPTION					
1	collector					
2	collector					
3	base					
4	emitter					
5	collector					
6	collector					

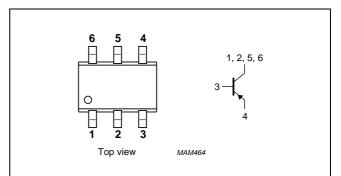


Fig.1 Simplified outline (SOT363; SC-88) and symbol.

## 40 V low V<sub>CEsat</sub> PNP transistor

PBSS5240Y

#### LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter	_	-40	V
$V_{CEO}$	collector-emitter voltage	open base	_	-40	V
V <sub>EBO</sub>	emitter-base voltage	open collector	_	<b>-</b> 5	٧
I <sub>C</sub>	collector current (DC)		-	-2	Α
I <sub>CM</sub>	peak collector current		_	-3	Α
I <sub>BM</sub>	peak base current		_	-300	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C; note 1	-	270	mW
		T <sub>amb</sub> ≤ 25 °C; note 2	-	430	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C
T <sub>j</sub>	junction temperature		_	150	°C
T <sub>amb</sub>	operating ambient temperature		-65	+150	°C

#### **Notes**

- 1. Device mounted on a printed-circuit board, single side copper, tinplated and standard footprint.
- 2. Device mounted on a printed-circuit board, single side copper, tinplated and mounting pad for collector 1 cm<sup>2</sup>.

#### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT	
R <sub>th j-a</sub>	thermal resistance from junction to	note 1	463	K/W	
	ambient	note 2	291	K/W	

#### Notes

- 1. Device mounted on a printed-circuit board, single side copper, tinplated and standard footprint.
- 2. Device mounted on a printed-circuit board, single side copper, tinplated and mounting pad for collector 1 cm<sup>2</sup>.

## 40 V low $V_{\text{CEsat}}$ PNP transistor

PBSS5240Y

#### **CHARACTERISTICS**

 $T_{amb}$  = 25  $^{\circ}C$  unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I <sub>CBO</sub>	collector-base cut-off current	$V_{CB} = -30 \text{ V}; I_E = 0$	_	-100	nA
		$V_{CB} = -30 \text{ V}; I_E = 0; T_j = 150 ^{\circ}\text{C}$	-	-50	μΑ
I <sub>EBO</sub>	emitter-base cut-off current	$V_{EB} = -4 \text{ V}; I_C = 0$	_	-100	nA
h <sub>FE</sub>	DC current gain	$V_{CE} = -2 \text{ V}; I_{C} = -100 \text{ mA}$	300	-	
		$V_{CE} = -2 \text{ V}; I_{C} = -500 \text{ mA}$	260	-	
		$V_{CE} = -2 \text{ V}; I_{C} = -1000 \text{ mA}$	210	-	
		$V_{CE} = -2 \text{ V}; I_{C} = -2000 \text{ mA}$	100	-	
V <sub>CEsat</sub>	collector-emitter saturation	$I_C = -100 \text{ mA}; I_B = -1 \text{ mA}$	_	-100	mV
	voltage	$I_C = -500 \text{ mA}; I_B = -50 \text{ mA}$	_	-110	mV
		$I_C = -750 \text{ mA}; I_B = -15 \text{ mA}$	_	-225	mV
		$I_C = -1000 \text{ mA}; I_B = -50 \text{ mA}$	-	-225	mV
		$I_C = -2000 \text{ mA}; I_B = -200 \text{ mA}$	_	-350	mV
V <sub>BEsat</sub>	base-emitter saturation voltage	$I_C = -2000 \text{ mA}; I_B = -200 \text{ mA}$	_	-1.1	V
V <sub>BEon</sub>	base-emitter turn-on voltage	$V_{CE} = -2 \text{ V}; I_{C} = -100 \text{ mA}$	_	-0.75	V
C <sub>c</sub>	collector capacitance	$V_{CB} = -10 \text{ V}; I_E = I_e = 0; f = 1 \text{ MHz}$	_	40	pF
F <sub>T</sub>	transition frequency	$I_C = -100 \text{ mA}; V_{CE} = -10 \text{ V}; f = 100 \text{ MHz}$	100	_	MHz

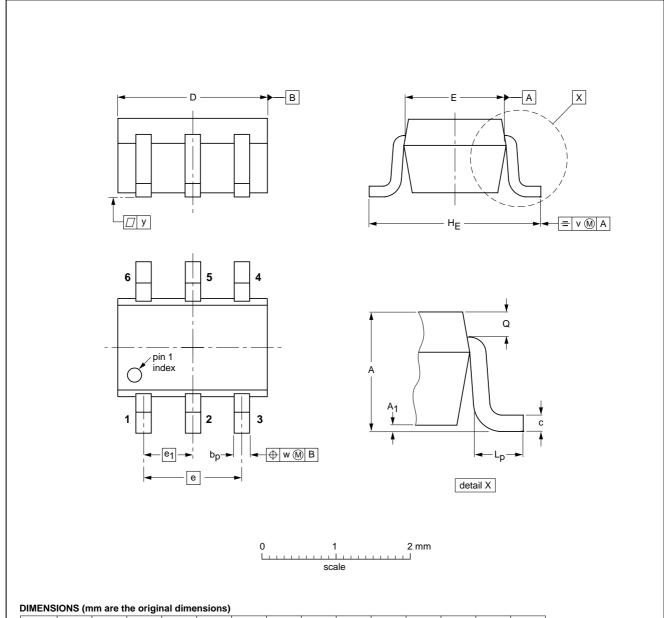
## 40 V low V<sub>CEsat</sub> PNP transistor

PBSS5240Y

#### **PACKAGE OUTLINE**

#### Plastic surface mounted package; 6 leads

**SOT363** 



UNI	- A	A <sub>1</sub> max	bp	С	D	E	е	e <sub>1</sub>	HE	Lp	Q	v	w	у
mm	1.1 0.8	0.1	0.30 0.20	0.25 0.10	2.2 1.8	1.35 1.15	1.3	0.65	2.2 2.0	0.45 0.15	0.25 0.15	0.2	0.2	0.1

OUTLINE		REFERENCES IEC JEDEC EIAJ		REFERENCES		EUROPEAN	ISSUE DATE
VERSION	IEC			PROJECTI		ISSUE DATE	
SOT363			SC-88			97-02-28	

### 40 V low V<sub>CEsat</sub> PNP transistor

PBSS5240Y

#### **DATA SHEET STATUS**

DOCUMENT STATUS <sup>(1)</sup>	PRODUCT STATUS <sup>(2)</sup>	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

#### **Notes**

- 1. Please consult the most recently issued document before initiating or completing a design.
- 2. The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nxp.com.

#### **DISCLAIMERS**

**General** — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in medical, military, aircraft, space or life support equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors accepts no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

**Applications** — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) may cause permanent damage to the device. Limiting values are stress ratings only and operation of the device at these or any other conditions

above those given in the Characteristics sections of this document is not implied. Exposure to limiting values for extended periods may affect device reliability.

Terms and conditions of sale — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at http://www.nxp.com/profile/terms, including those pertaining to warranty, intellectual property rights infringement and limitation of liability, unless explicitly otherwise agreed to in writing by NXP Semiconductors. In case of any inconsistency or conflict between information in this document and such terms and conditions, the latter will prevail.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

**Export control** — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from national authorities.

**Quick reference data** — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

## **NXP Semiconductors**

#### **Customer notification**

This data sheet was changed to reflect the new company name NXP Semiconductors. No changes were made to the content, except for the legal definitions and disclaimers.

#### **Contact information**

For additional information please visit: http://www.nxp.com

For sales offices addresses send e-mail to: salesaddresses@nxp.com

© NXP B.V. 2009

All rights are reserved. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner.

The information presented in this document does not form part of any quotation or contract, is believed to be accurate and reliable and may be changed without notice. No liability will be accepted by the publisher for any consequence of its use. Publication thereof does not convey nor imply any license under patent- or other industrial or intellectual property rights.

Printed in The Netherlands 613514/02/pp7 Date of release: 2002 Feb 28 Document order number: 9397 750 09503

