

3.3V CMOS 35.328MHz DSL VCXO

FRXDSL035



7.0 x 5.0mm Ceramic SMD

ASSP VCXO™ for DSL



Product Features

- Very low Pk to Pk jitter - 50ps Max
- Low supply current - 10mA Max
- Low power standby mode
- RoHS Compliant

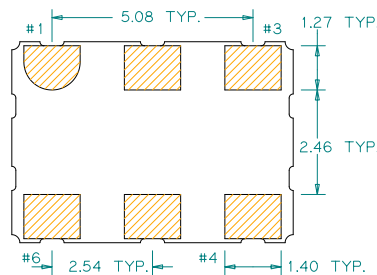
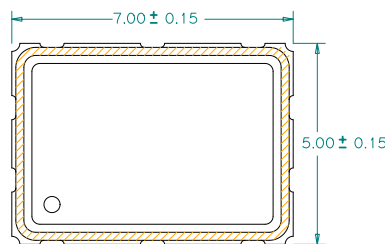
Product Description

This is an enhanced 3.3V, 35.328MHz with superb jitter and low operating current for providing clock references in DSL applications.

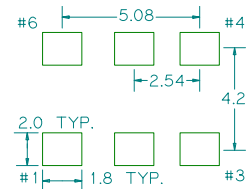
Applications

- xDSL
- CEPT1(x17.25)

Package: (Scale: none, Dimensions are in mm)



Recommended Land Pattern:



Pin Functions:

Pin	Function
1	Control Voltage
2	Enable/Disable
3	Ground
4	Output
5	N/C
6	V _{DD}

*Extended high frequency power decoupling is recommended (see test circuit for minimum recommendation). To ensure optimal performance, do not route RF traces beneath the package.

Part Ordering Information:
FRXDSL035

Electrical Performance

Parameter	Min.	Typ.	Max.	Units	Notes
Output Frequency		35.328		MHz	
Supply Voltage V _{DD}	3.135	3.3	3.465	V	
Supply Current, Output Enabled			10	mA	
Supply Current, Output Disabled			1	μA	
Frequency Stability			±25	ppm	See Note 1 below
Operating Temperature Range	-20		+70	°C	
Output Logic 0, V _{OL}			10% V _{DD}	V	
Output Logic 1, V _{OH}	90% V _{DD}			V	
Output Load			15	pF	
Duty Cycle	45		55	%	Measured 50% V _{DD}
Rise and Fall Time			4	ns	Measured 20/80% of waveform
Jitter, Phase, RMS(1-σ)			1	ps	12kHz~20MHz Frequency Band
Jitter, Peak to Peak (Pk-Pk)			50	ps	100.000 Random Periods

Notes:

- Stability includes all combinations of operating temperature, load changes, rated input (supply) voltage changes, initial calibration tolerance (25°C), aging (10 years at +40°C average effective ambient temperature), shock and vibration.
- For specifications other than those listed, please contact sales.

Voltage Control Function

Parameter	Min.	Typ.	Max.	Units	Notes
Absolute Pull Range (APR)	±100			ppm	See 1 below
Control Voltage Range	0.3		3.0	V	As rated
Center Control Voltage		1.65		V	For RMT Nominal Frequency
Monotonic Linearity			10	%	Positive Transfer Slope
Input Impedance	2000			kΩ	Control Voltage Pin

Notes:

- APR is relative to the nominal output frequency; APR is inclusive of frequency deviation due to stability.

Output Enable / Disable Function

Parameter	Min.	Typ.	Max.	Units	Notes
Input Voltage (pin2), Output Enable	3.0			V	or open
Input Voltage (pin 2), Output Disable (low power standby)			0.3	V	Output is Hi-Z
Internal Pullup Resistance		50		kΩ	
Output Disable Delay		100	200	ns	
Output Enable Delay		0.2		ms	

Absolute Maximum Ratings

Parameter	Min.	Typ.	Max.	Units	Notes
Storage Temperature	-55		+125	°C	

For the latest product information visit: <http://www.pericom.com/products/crystals-and-crystal-oscillators/assp-xo/?part=FRXDSL035>

For test circuit go to: http://www.pericom.com/pdf/sre/tc_vc6cmos.pdf

For soldering reflow profile and reliability test ratings go to: <http://www.pericom.com/pdf/sre/reflow.pdf>

For tape and reel information go to: http://www.pericom.com/pdf/sre/tr_7050_xo.pdf