

1:2 Active HDMI™ Compatible DeMux with Advanced Re-Driver Functionality for Enhanced Signal Integrity

Features

- Supply voltage, $V_{DD} = 3.3V \pm 5\%$
- Compatible w/ DVI, HDMITM 1.1, 1.2, and 1.3 signals
- Supports both AC-coupled and DC-coupled inputs
- 1:2 Demux
- Supports Deep ColorTM Signals
- Configurable output swing control (500mV, 750mV, 1000mV)
- Configurable Pre-Emphasis levels (0dB, 1.5dB, 3.5dB, 6.0dB)
- Configurable De-Emphasis (0dB, -1.5dB, -3.5dB, -6.0dB)
- Configurable Equalization (1dB,3.5dB, 6dB, 8dB)
- Data Rate = 2.5Gbps (Max)
- Inputs w/ built-in termination
- Propagation delay < 2ns input
- · Uni-Directional
- 10kV HBM ESD protection on all high speed data channels (Supplemental contact ESD test results are available upon request)
- Packaging (Pb-free & Green): 56-pad TQFN (ZB56)

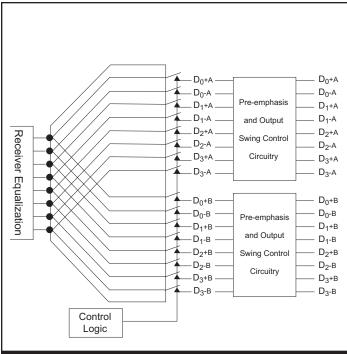
Description

Pericom Semiconductor's PI3HDMI412AD, active-drive switch solution is targeted for high-resolution video networks that are based on DVI/HDMITM standards, and TMDS signal processing. The PI3HDMI412AD is an active single TMDS channel to two TMDS channel DeMux with Hi-Z outputs. The device drives differential signals to multiple video display units. It provides three controllable output swing levels that can be controlled through pin control or I²C control, depending on the mode select pin. The swing levels are 500mV, 700mV & 1000mV. This solution also provides a unique advanced pre-emphasis technique to increase rise and fall times which are reduced during transmission across long distances.

The maximum DVI/HDMI™ data rate of 1.65Gbps provides a 1920x1080 resolution required by the next Gen HDTV and PC graphics products. Due to its active uni-directional feature, this switch is de-signed for usage only for the video driver's side. For PC graphics application, the device sits at the driver's side to switch between multiple display units, such as PC LCD monitor, projector, TV, etc.

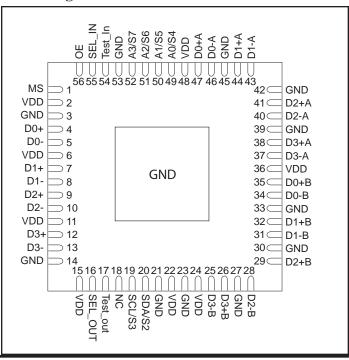
PI3HDMI412AD is the industry's first active DVI/HDMI[™] compliant switch, which ensures transmitting high bandwidth video streams from PC graphics source to end display units. PI3HDMI412AD will also provide enhanced robust ESD/EOS protection, which is required by many consumer video networks today.

Block Diagram



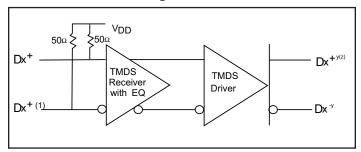
Pin Configuration

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Function Block Description



Notes:

1. X = 0,1,2,3

2. Y = A,B

Pin Description

Maxii	num Ratings (Above which useful life may be im-
paired.	For user guidelines, not tested.)

Storage Temperature65°C to +150°C
Supply Voltage to Ground Potential0.5V to +5V
DC Input Voltage–0.5V to V _{DD}
DC Output Current120mA
Power Dissipation1.0W

Note: Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

Pin#	Pin Name	I/O	Description
2, 6, 11, 15, 22, 24, 36, 48	V_{DD}	Power	3.3V power supply
3, 14, 21, 23, 27, 30, 33, 39, 42, 45, 53	GND	Power	0V power supply
16	SEL_Out	0	Output bit, that provides information to user as to which port is active, if SEL_OUT = 'LOW', then Port A is active, if SEL_OUT = 'HIGH', then Port B is active. Only used when MS pin is 'HIGH'
19	SCL	I	I ² C Clock Input Signal
20	SDA	I/O	I ² C Data Input/Output Signal
18	NC	N/A	No Connect.
1	MS	I	Mode Select Pin. If MS = 'HIGH', then I ² C control is active. Pins 49-52 are I ² C address and pin 19 is SCL and pin 20 is SDA. If MS = 'LOW', then I ² C control is inactive and pin programmability is active. Pins 49-52 are control pins only for Port A, S4-S7 and pin 19 is S2 and pin 20 is S3. If 'MS' = "LOW", Port B cannot be configured and is left as default. Port B default value is 0dB pre-emphasis, 0dB de-emphasis, and 500mV output swing
55	SEL_IN	I	Output port select. (Logically similar to I ² C bit S1 - see page 3)
54	Test_in	I	Input pin for internal testing. Tie to GND for normal operation
56	OE	I	Output is enabled and normal when OE = 'HIGH'. If OE = 'LOW', both outputs, A and B, are disabled and at Hi-Z
17	Test_Out	О	Output pin for internal testing. Not used for normal operation
4, 5, 7, 8, 9, 10, 12, 13	Dx	I	Input TMDS high speed signals
28, 29, 31, 32, 34, 35, 40, 41, 43, 44, 46, 47, 25, 26, 37, 38	Dx ^y	0	Output TMDS high speed signals
49,50, 51, 52	A0, A1, A2, A3	I	I ² C address inputs if MS = 'HIGH'.
49,50, 51, 52	S4, S5, S6, S7	I	If MS = 'LOW', then pins 49-52 are control bits S4-S7 for port A only, as shown in truth table on page 3 of datasheet
19	S3	I	If MS = 'LOW', then pins 19 is control bit S3, as shown in the truth table on page 3
20	S2	I	If MS = 'LOW', then pins 20 is control bit S2, as shown in the truth table on page 3



I²C Truth Table

(At power up, default values are: Port A is active, signal swing is set to 500mV, pre-emphasis and de-emphasis are at 0dB, and equalization is at 1dB)

Byte 2: S[7:0]=00000001 Byte 3: S[7:0]=00000001

Output Swing: 500mV Output Swing: Defined by Byte 2

Pre-emphasis: 0dB Pre-emphasis: De-Emphasis: 0dB De-Emphasis:

Equalization: 1dB Active Port: port A, in normal mode

Active Port: port A

BYTE 1 (Address Assignment)

Address	A6	A5	A4	A3	A2	A1	A0	R/W
Value	1	1	0	A3	A2	A1	A0	R=1/W=0

BYTE 2 (1st Data byte - Port A control and input control)

Port A and Input	S7	S6	S5	S4	S3	S2	S1	S0		Result	
Control									Swing (mV)	Pre-emphasis (dB)	De-emphasis (dB)
Swing Control	0	0	0	0	Х	X	X	X	500	0	0
	0	0	0	1	Х	X	X	X	750	0	0
	0	0	1	0	Х	Х	Х	Х	1000	0	0
	0	0	1	1	Х	Х	Х	Х	N/A	N/A	N/A
Pre-Emphasis	0	1	0	0	Х	Х	Х	Х	500	0	0
	0	1	0	1	Х	Х	Х	Х	500	1.5	0
	0	1	1	0	Х	Х	Х	Х	500	3.5	0
	0	1	1	1	Х	Х	Х	Х	500	6.0	0
De-Emphasis	1	0	0	0	Х	Х	Х	Х	750	0	0
	1	0	0	1	Х	Х	Х	Х	750	0	-1.5
	1	0	1	0	Х	Х	Х	Х	750	0	-3.5
	1	0	1	1	Х	Х	Х	Х	750	0	-6.0
Output Port Select	Х	Х	Х	Х	Х	Х	0	1			Port A is active
	Х	Х	Х	Х	Х	Х	1	1			Port B is active
	Х	Х	Х	Х	Х	Х	Х	0	Port A		
Equalization (dB)	Х	Х	Х	Х	0	0	Х	Х	x		1
	Х	Х	Х	Х	0	1	Х	Х			3.5
	Х	Х	Х	Х	1	0	Х	Х			6
	Х	Х	Х	Х	1	1	Х	Х			8

3



BYTE 3 (2nd Data byte - Port B control)

Port B Control only	S7	S6	S5	S4	S3	S2	S1	S0		Result	
									Swing (mV)	Pre-emphasis (dB)	De-emphasis (dB)
Swing Control	0	0	0	0	X	X	X	X	500	0	0
	0	0	0	1	X	X	X	X	750	0	0
	0	0	1	0	Х	Х	Х	Х	1000	0	0
	0	0	1	1	Х	Х	Х	Х	N/A	N/A	N/A
Pre-Emphasis	0	1	0	0	Х	Х	Х	Х	500	0	0
	0	1	0	1	Х	Х	Х	Х	500	1.5	0
	0	1	1	0	Х	Х	Х	Х	500	3.5	0
	0	1	1	1	Х	Х	Х	Х	500	6.0	0
De-Emphasis	1	0	0	0	Х	Х	Х	Х	750	0	0
	1	0	0	1	Х	Х	Х	Х	750	0	-1.5
	1	0	1	0	Х	Х	Х	Х	750	0	-3.5
	1	0	1	1	Х	Х	Х	Х	750	0	-6.0
Output Port Select	Х	Х	Х	Х	Х	Х	0	1			Normal
	Х	Х	Х	Х	Х	Х	1	1			TEST MODE
	Х	Х	Х	Х	Х	Х	Х	0	Port B = H		



TMDS Compliance Test Results

Item	HDMI™ 1.3 Spec	Pericom TMDS Product Spec		
Operating Conditions				
Termination Supply Voltage, AVDD	$3.3V \le 5\%$	$3.30 \pm 5\%$		
Terminal Resistance	50 Ohm ≤ 10%	45 to 55 Ohm		
Source DC Characteristics at TP1				
Single-ended high level output voltage, V _H	$A_{VDD} \le 10 \text{mV}$	$A_{VDD} \le 10 \text{mV}$		
Single-ended low level output voltage, V_L	$(A_{VDD} - 600 \text{mV}) \le \text{VL} \le (A_{VDD} - 400 \text{mV})$	$(A_{VDD} - 600 mV) \le VL \le (A_{VDD} - 400 mV)$		
Single-ended output swing voltage, V _{swing}	$400 \text{mV} \le \text{V}_{\text{swing}} \le 600 \text{mV}$	$400 mV \le V_{swing} \le 600 mV$		
Single-ended standby (off) output voltage, Voff	$A_{VDD} \pm 10 \text{mV}$	$A_{VDD} \pm 10 \text{mV}$		
Single-ended standby (off) output current, I _{off}	I _{OFF} < 10uA	I _{OFF} <10uA		
Transmitter AC Characteristics at TP1	1	I		
Risetime/Falltime (20%-80%)	$75 ps \le Risetime/Falltime \le 0.4 Tbit$ $(75 ps \le tr/tf \le 242 ps) @ 1.65 Gbps$	240ps		
Intra-Pair Skew at Transmitter Connector, max	0.15 Tbit (90.9ps @ 1.65Gbps)	60ps max		
Inter-Pair Skew at Transmitter Connector, max	0.2 Tpixel (1.2ns @ 1.65Gbps)	100ps max		
Clock Jitter, max	0.25 Tbit (151.5ps @ 1.65Gbps)	82ps max		
Sink Operating DC Characteristics at TP2				
Input Differential Voltage Level, V _{diff}	$150 \le V diff \le 1200 mV$	$150 \text{mV} \le V_{DIFF} \le 1200 \text{mV}$		
Input Common Mode Voltage Level, V _{ICM}	$\begin{array}{l} A_{VDD} \text{ -}300\text{mV} \leq V_{ICM} \leq A_{VDD} \text{ -}37.5\text{mV} \\ \text{or } A_{VDD} \pm 10\% \end{array}$	A_{VDD} -300mV \leq V _{ICM} $<$ A_{VDD} - 37.5mV or A_{VDD} ±10%		
Sink DC Characteristics When Source Disable	ed or Disconnected at TP2			
Differential Voltage Level	$A_{VDD} \pm 10 \text{mV}$	$A_{VDD} \pm 10 \text{mV}$		

5

08-0049



DC Electrical Characteristics ($T_A = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$, unless otherwise noted. $V_{DD} = 3.3\text{V} \pm 0.3\text{V}$)

Symbol	Parameter	Test Min.		Typ. ⁽²⁾	Max.	Units
$V_{\rm H}$	Single-ended high level output voltage		$V_{DD} - 10 \text{mV}$	V_{DD}	$V_{DD} + 10 \text{mV}$	V
$V_{\rm L}$	Single-ended low level output voltage		$V_{DD} - 600 \text{mV}$		V _{DD} - 400mV	V
Vswing	Single-ended output swing voltage		400		600	mV
V _{OFF}	Single-ended standby (off) output voltage		V _{DD} – 10mV	V_{DD}	$V_{DD} + 10 \text{mV}$	V
I _{OFF}	Single-ended standby (off) output current				10	μs
Vos	Offset Voltage				V _{DD} – 250mV	V
V_{IH}	Minimum Input High Voltage		1.8			V
V_{IL}	Minimum Input Low Voltage				0.8	l v
I_{CC}	Power Supply Current				280	mA

AC Electrical Characteristics $(T_A = -40^{\circ}\text{C to } +85^{\circ}\text{C}, V_{DD} = 3.3\text{V} \pm 0.3\text{V})$

Symbol	Paramter	Test Conditions ⁽¹⁾	Min.	Typ. ⁽²⁾	Max.	Units
T ₂₀₋₈₀	Rise time/fall time (20% - 80%)		75		240	ps
	Overshoot				15% of V _{swing} * 2	
	Undershoot				25% of V _{swing} * 2	
	Intra-Pair Skew at Source Connector				60	ps
	Inter-Pair Skew at Connector				100	ps
	Clock duty cycle		40%	50%	60%	
	Through connection impedance		85	100	115	ps
	TMDS differential clock Jitter				82	ps
	At Termination impedance		90	100	110	ps
t _{PHLD}	Differenital Propagation Delay High to Low			1.0		ns
t _{PLHD}	Differential Propagation Delay Low to High			1.0		ns
t_{SKD}	Differential Skew t _{PHLD} - t _{PLHD}			25		ps
t _{PHZ}	Disable Time High to Z			5		,,,
t _{PLZ}	Disable Time Low to Z			5		ns
t _{PZH}	Enable Time Z to High			1		
t _{PZL}	Enable Time Z to Low			1		μs

Notes:

- For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- Typical values are at $V_{DD} = 3.3V$, $T_A = 25$ °C ambient and maximum loading.

Power Supply Characteristics

	Parameters	Description	Test Conditions ⁽¹⁾	Min.	Typ. ⁽²⁾	Max.	Units
ſ			$V_{DD} = Max., V_{IN} = V_{DD}, OE =$				
	I_{CC}	Quiescent Power Supply Current	'LOW'		1		mA

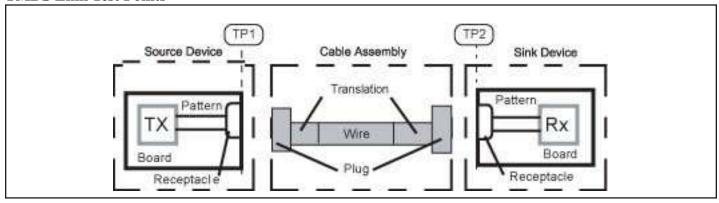
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Notes:

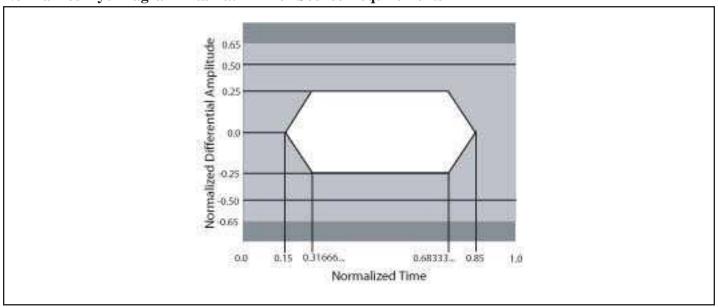
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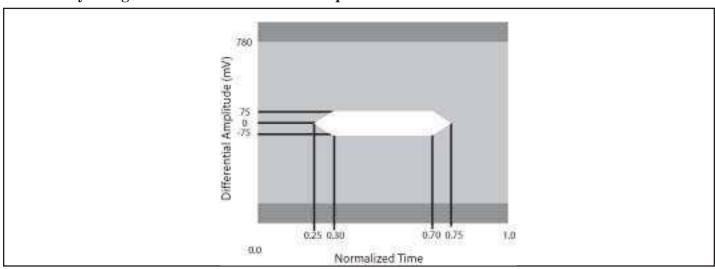
TMDS Link Test Points



Normalized Eye Diagram Mask at TP1 for Source Requirements



Absolute Eye Diagram Mask at TP2 for Sink Requirements

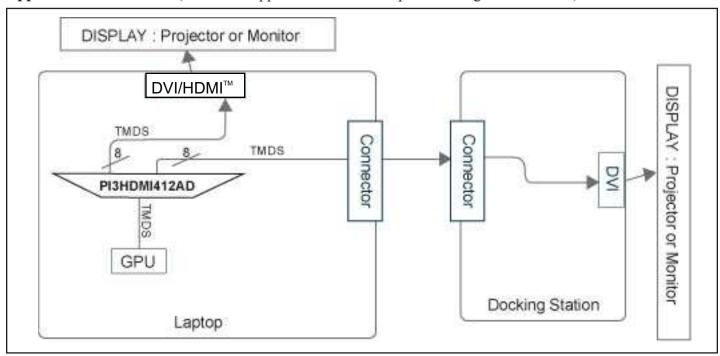


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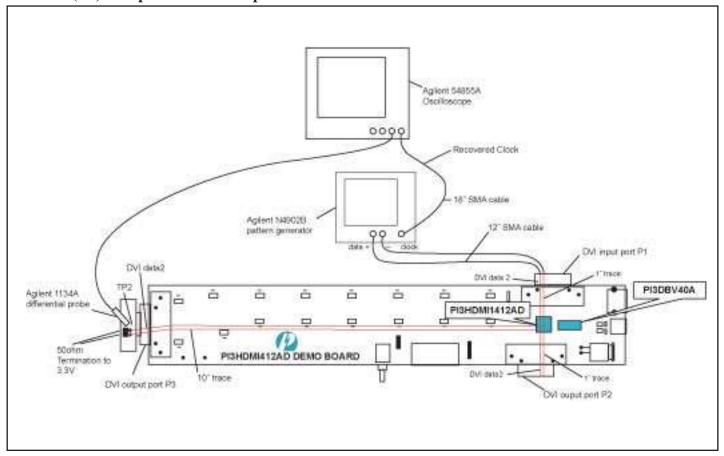
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Application Information (Please see application note for important design information.)

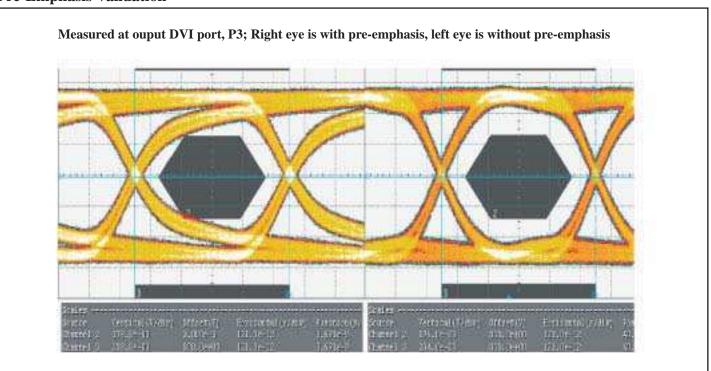


DVI TP2 (Tx) Compliance Test Setup

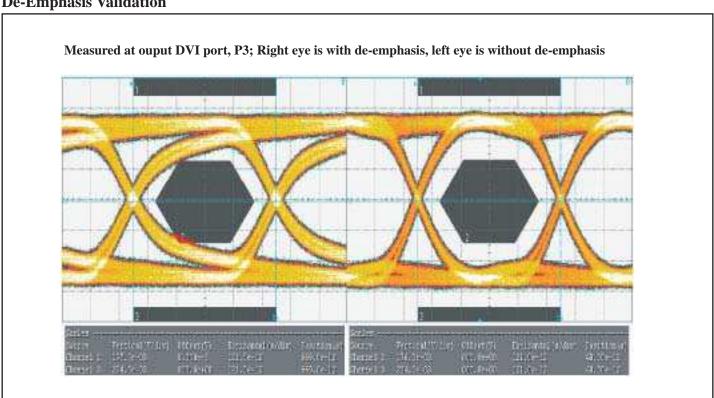




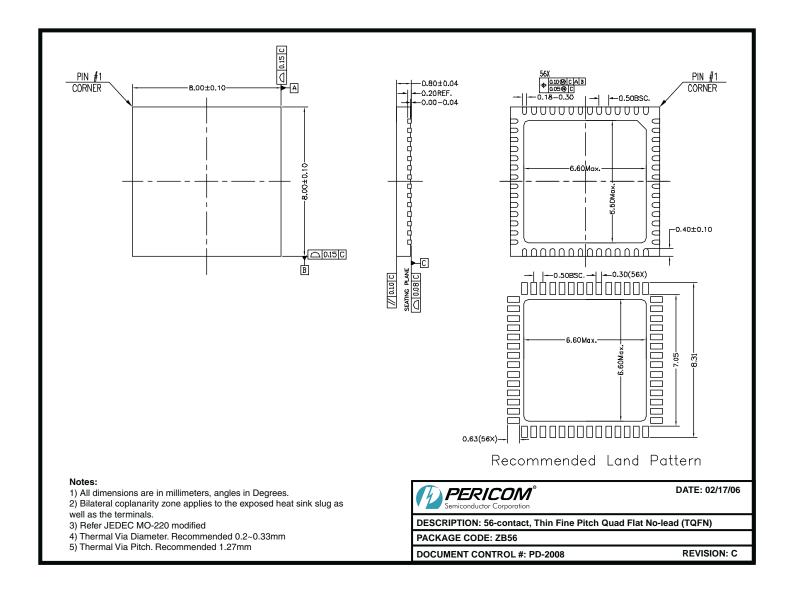
Pre-Emphasis Validation



De-Emphasis Validation







Ordering Information

Ordering Code	Package Code	Package Description
PI3HDMI412ADZBE	ZB	56-pin, Pb-free & Green TQFN

Notes:

- Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
- E = Pb-free and Green
- Adding an X Suffix = Tape/Reel
- HDMI & Deep Color are trademarks of Silicon Image

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10

08-0049



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11