JRC

DUAL J-FET INPUT OPERATIONAL AMPLIFIER

■ GENERAL DESCRIPTION

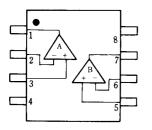
The NJM072B/082B & NJM072/082 are dual JFET input operational amplifiers. They feature low input bias and offset currents, high input impedance and fast slew rate. The low harmonic distortion and low noise make them ideally suit for amplifiers with high fidelity and audio amplifier applications.

The NJM072/082 may cause oscillation in some application like voltage follower.

FEATURES

- Operating Voltage
- J-FET Input
- High Input Resistance
- Low Input Resistance
- High Slew Rate
- Wide Unity Gain Bandwidth
- Package Outline
- Bipolar Technology

■ PIN CONFIGURATION



NJM072BD/082BD, NJM072D/082D NJM072BM/082BM, NJM072M/082M NJM072BE NJM072BV/082BV

■ EQUIVALENT CIRCUIT



- (10¹²Ω typ.)
- (30pA typ.)
- (13V/µs,20V/µs typ.) (3MHz,5MHz typ.) DIP8, DMP8, SIP8 EMP8 (NJM072B only) SSOP8 (NJM072B/082B only)

■ PACKAGE OUTLINE



NJM072BD/082BD NJM072D/082D



NJM072BM/082BM NJM072M/082M

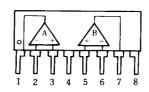




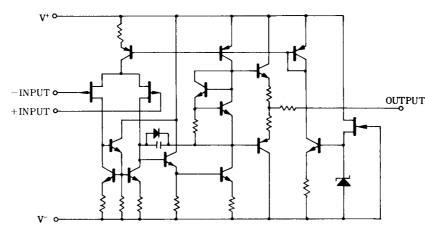


NJM072BL/082BL NJM072L/082L

PIN FUNCTION 1.A OUTPUT 2.A -INPUT 3.A +INPUT 4.V 5.B +INPUT 6.B -INPUT 7.B OUTPUT 8.V⁺



NJM072L/082L NJM072BL/082BL



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■ ABSOLUTE MAXIMUM RATINGS

			(Ta=25°C)
PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V ⁺ ∧∕-	± 18	V
Input Voltage	VIC	± 15(note)	V
Differential Input Voltage	VID	± 30	V
Power Dissipation	P _D	(DIP8) 500 (DMP8) 300 (EMP8) 300 (SSOP8) 250 (SIP8) 800	mW
Operating Temperature Range	T _{opr}	-40~+85	°C
Storage Temperature Range	T _{stg}	-40~+125	°C

(note) For supply voltage less than \pm 15V, the absolute maximum input voltage is equal to the supply voltage.

■ ELECTRICAL CHARACTERISTICS (Ta=+25°C,V⁺/V⁻=±15V)

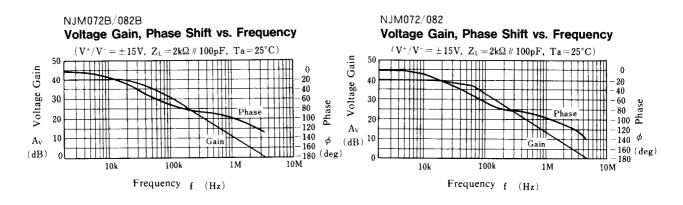
			() Applies to NJM082B,NJM082			
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Input Offset Voltage	VIO	R _S =50Ω	-	3 (5)	10 (15)	mV
Input Offset Current	I _{IO}		-	5	50 (200)	pА
Input Bias Current	IB		-	30	200 (400)	pА
Input Common Mode Voltage Range	VICM		± 10	-	-	V
Maximum Peak-to-peak Output Voltage Swing	VOPP	R _L =10kΩ	24	27	-	V _{P-P}
Large-Signal Voltage Gain	Av	R _L ≥2kΩ,V _O =±10V	88	106	-	dB
Unity Gain Bandwidth	f⊤	072B/082B	-	3	-	MHz
		072/082	-	5		MHz
Input Resistance	R _{IN}		-	10 ¹²	-	Ω
Common Mode Rejection Ratio	CMR	R _s ≤10kΩ	70	76	-	dB
Supply Voltage Rejection Ratio	SVR	R _s ≤10kΩ	70	76	-	dB
Operating Current	lcc		-	3	5 (5.6)	mA
Slew Rate	SR	072B/082B	-	13	-	V/µs
		072/082	-	20	-	V/µs
Equivalent Input Noise Voltage	V _{NI}	R _S =100Ω,B.W.=10~10kHz	-	4	-	μV _{ms}

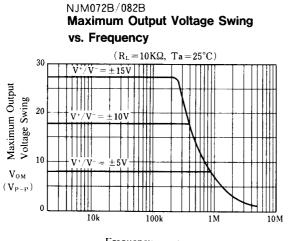
■ NOTICE WHEN APPLICATION

Recommendable product

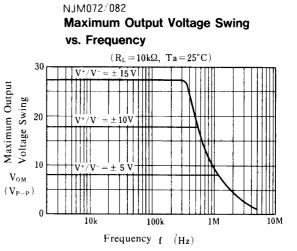
072/082 are the products in which the AC feature have been made much higher comparing to the products of 072B/082B which are compatible with 072/082 type of other company's products. Therefore, 072/082 are unstable in oscillation when the voltage follower application, and it is recommendable to use the standard type 072B/082B when newly designed. Beside these products, we have NJM2082 which is higher up in AC feature, yet stability in oscillation, and then the driving capacity to the load at the output stage is made much higher in operation.

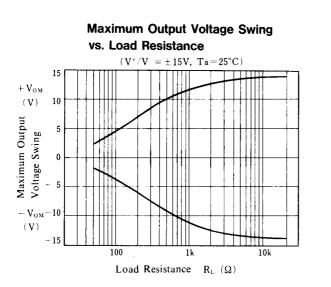
■ TYPICAL CHARACTERISTICS

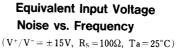


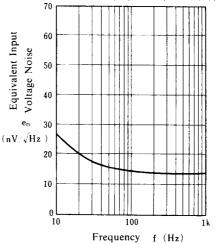


Frequency f (Hz)

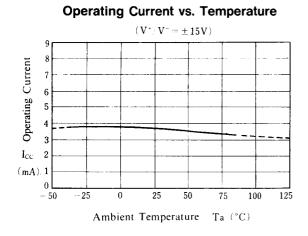








TYPICAL CHARACTERISTICS



Input Offset Voltage vs. Temperature $(V^{+}/V^{-} = \pm 15V)$

50

75

100

25

Ambient Temperature Ta (°C)

0

125

4 3

2

1

0

- 1

- 2

- 3 V_{10} (\mathbf{mV}) - 4

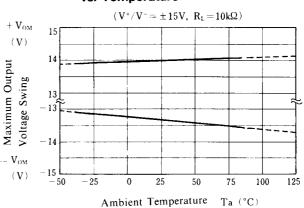
- 50

- 25

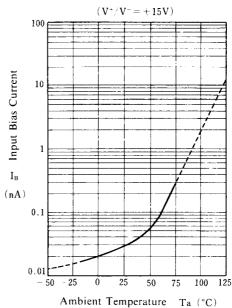
Offset Voltage

Input

Maximum Output Voltage Swing vs. Temperature

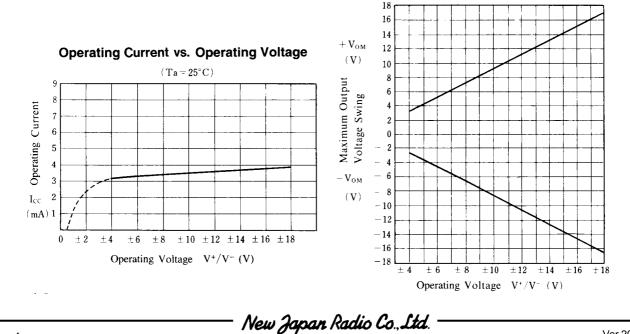


Input Bias Current vs. Temperature



Maximum Output Voltage Swing vs. Operating Voltage

 $(R_L = 10k\Omega, Ta = 25^{\circ}C)$



■ MEMO

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