

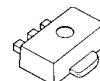
## ADJUSTABLE HIGH PRECISION SHUNT REGULATOR

## ■GENERAL DESCRIPTION

**NJM2373 / NJM2373A / NJM2376** is an adjustable high precision shunt regulator.

The output voltage can be adjusted to any value between reference voltage and 14V by two extend resistors.

## ■PACKAGE OUTLINE



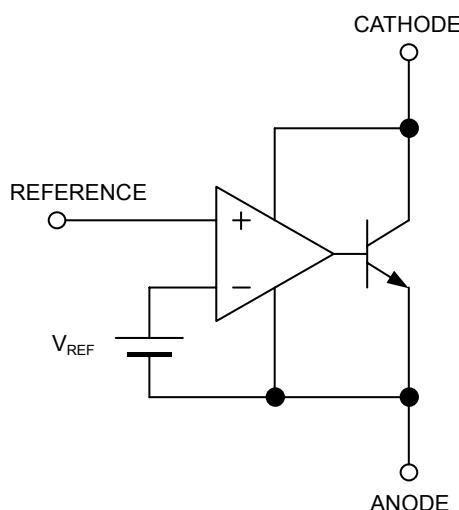
**NJM2373F/AF**  
**NJM2376F**

**NJM2373AU**  
**NJM2376U**

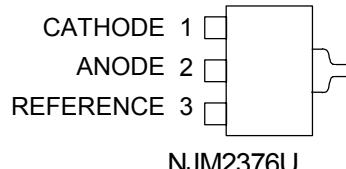
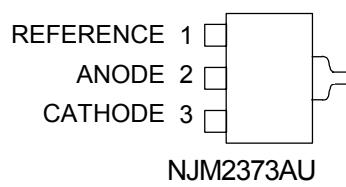
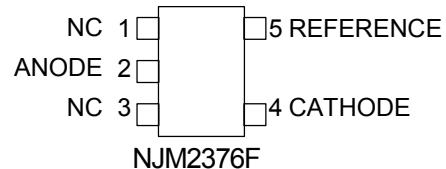
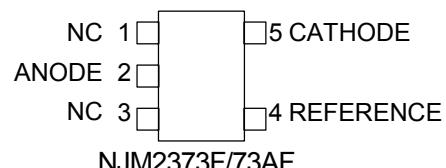
## ■FEATURES

- Operating Voltage  $V_{REF}$  to 13V
- High Precision Voltage Reference NJM2373 1.25V $\pm$ 2%
- NJM2373A/76 1.25V $\pm$ 1%
- Minimum Input Current 80 $\mu$ A typ.
- Adjustable Output Voltage
- Bipolar Technology
- Package Outline SOT-89 (3pin), MTP5

## ■BLOCK DIAGRAM



## ■PIN CONFIGURATION



# NJM2373/73A/76

## ■ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

PARAMETER	SYMBOL	MAXIMUM RATINGS	UNIT
Cathode Voltage	$V_{KA}$	+14	V
Continuous Cathode Current	$I_K$	-30 ~ 50	mA
Reference Input Current	$I_{REF}$	-10 ~ 0.05	mA
Power Dissipation	$P_D$	(SOT-89) 350 (MTP5) 200	mW
Operating Temperature Range	$T_{OPR}$	-40 ~ +85	°C
Storage Temperature Range	$T_{STG}$	-40 ~ +150	°C

## ■RECOMMENDED OPERATING CONDITIONS (Ta=25°C)

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT
Cathode Voltage	$V_{KA}$	$V_{REF}$	—	13	V
Cathode Current	$I_K$	0.5	—	30	mA

## ■ELECTRICAL CHARACTERISTICS ( $I_K=1\text{mA}$ , $Ta=25^\circ\text{C}$ )

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Reference Voltage	$V_{REF}$	$V_{KA}=V_{REF}$ NJM2373A	(*1)	1225	1250	1275
		$V_{KA}=V_{REF}$ NJM2373A/ NJM2376	(*1)	1237	1250	1263
Reference Voltage Change vs. Cathode Voltage Change	$\Delta V_{REF}/\Delta V_{KA}$	$ V_{REF}  \leq V_{KA} \leq 5\text{V}$	(*2)	—	—	$\pm 2.7$
		$5\text{V} \leq V_{KA} \leq 13\text{V}$	(*2)	—	—	$\pm 2.0$
Reference Input Current	$I_{REF}$	$V_{KA}=V_{REF}$ $R1=10\text{k}\Omega$ , $R2=\infty$	(*2)	—	2.0	4.0
Minimum Input Current	$I_{MIN}$	$V_{KA}=V_{REF}$ , $\Delta V_{REF}=\pm 1\%$	(*1)	—	80	500
Cathode Current (Off Cond.)	$I_{OFF}$	$V_{KA}=13\text{V}$ , $V_{REF}=0\text{V}$	(*3)	—	0.01	1.0
Dynamic Impedance	$ Z_{KA} $	$V_{KA}=V_{REF}$ , $f \leq 1\text{kHz}$ $0.5\text{mA} \leq I_K \leq 30\text{mA}$	(*1)	—	0.12	—

## ■TEMPERATURE CHARACTERISTICS ( $I_K=1\text{mA}$ , $Ta= -40^\circ\text{C} \sim 85^\circ\text{C}$ )

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Reference Voltage Change	$\Delta V_{REF}$	$V_{KA}=V_{REF}$	(*1)	—	$\pm 10$	—
Reference Input Current Change	$\Delta I_{REF}$	$V_{KA}=V_{REF}$ $R1=10\text{k}\Omega$ , $R2=\infty$	(*2)	—	0.5	—

$|V_{REF}|$  ...Reference voltage includes error.

(\*1): Test Circuit (Fig.1)

(\*2): Test Circuit (Fig.2)

(\*3): Test Circuit (Fig.3)

## ■TEST CIRCUIT

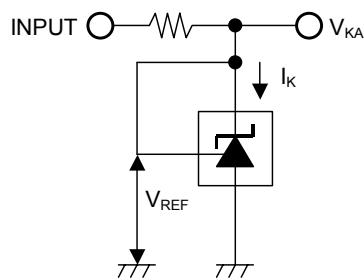


Fig.1  $V_{KA}=V_{REF}$  to test circuit

$$V_O = V_{KA} = V_{REF}$$

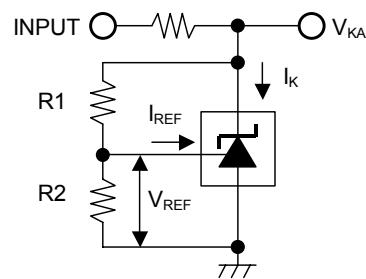


Fig.2  $V_{KA}>V_{REF}$  to test circuit

$$V_O = V_{KA} = V_{REF} \left( 1 + \frac{R_1}{R_2} \right) + I_{REF} \times R_1$$

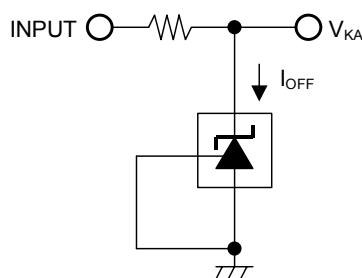


Fig.3  $I_{OFF}$  to test circuit

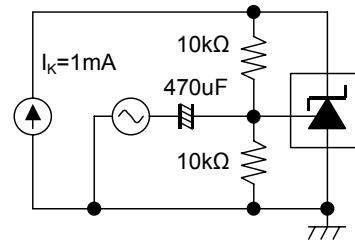


Fig.4 Gain and Phase to test circuit

## ■POWER DISSIPATION VS. AMBIENT TEMPERATURE

