

## FM IF IC FOR PAGER

### ■ GENERAL DESCRIPTION

**NJM2294** is a super low current FM IF IC for pagers. It includes almost all functions of the paging IF system. In those functions, the RSSI function can be used for automatic gain control. When the electric field strength is high, the RSSI output signal can control the attenuation of an RF amplifier to improve the received condition.

### ■ PACKAGE OUTLINE



**NJM2294V**

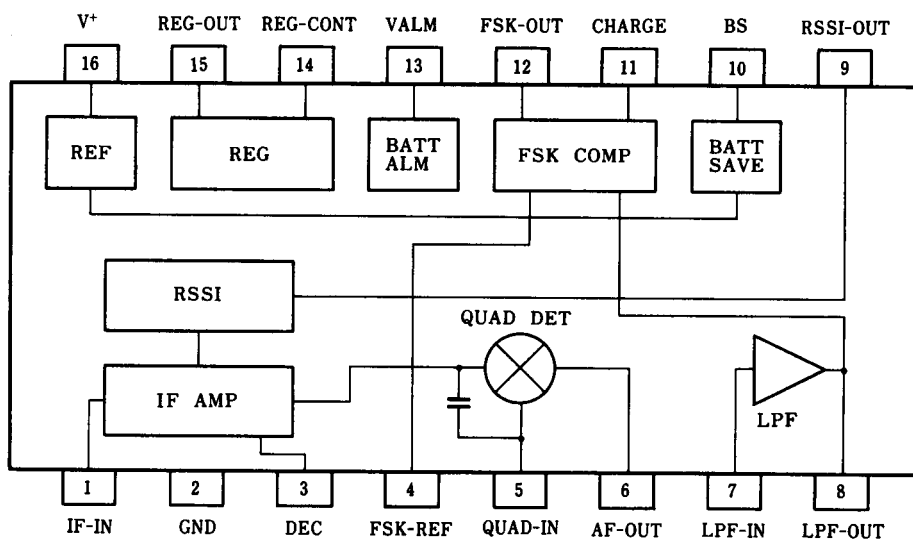
### ■ FEATURES

- Low Operating Voltage ( $V^+ = 1.1$  to  $4.0V$ )
- Super low Operating Current ( $600\mu A$ ,  $V^+ = 1.4V$ )
- RSSI (Received Signal Strength Indicator)
- FSK wave shaper
- Battery check alarm function (Alarm Voltage =  $1.1V$  typ.)
- Battery saving function
- A high output current voltage regulator with an external transistor ( $V_{reg} = 1.0V$  typ.)
- A ceramic discriminator is available.
- Package Outline SSOP16
- Bipolar Technology

### ■ RECOMMENDED OPERATIONAL CONDITION

- Operating Voltage  $V^+$  1.1 to  $4.0V$

### ■ PIN CONFIGURATION



**NJM2294V**

# NJM2294

## ■ ABSOLUTE MAXIMUM RATINGS

( $T_a=25^\circ\text{C}$ )

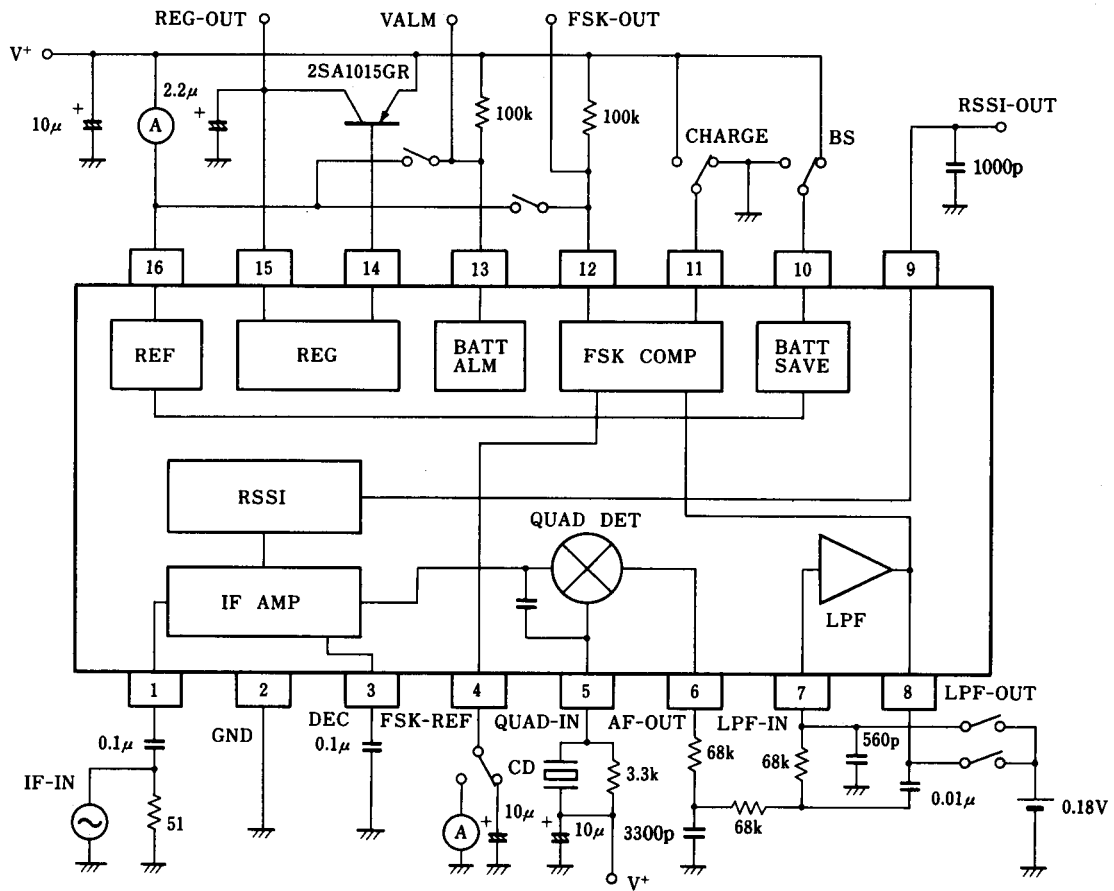
PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V+	5	V
Power Dissipation	P <sub>D</sub>	300	mW
Operating Temperature Range	T <sub>opr</sub>	-20 to +75	°C
Storage Temperature Range	T <sub>stg</sub>	-40 to +125	°C

## ■ ELECTRICAL CHARACTERISTICS

( $V^+=14\text{V}$ ,  $f_i=455\text{kHz}$ ,  $f_{\text{mod}}=600\text{Hz}$ ,  $f_{\text{dev}}=\pm 4\text{kHz}$ ,  $T_a=25^\circ\text{C}$ )

PARAMETER	SYMBOL	TEST CIRCUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
No signal Operating Current	I <sub>CCQ</sub>	11	V <sub>I</sub> =0, 10pin=V <sup>+</sup>	-	600	900	μA
Battery saving Operating Current	I <sub>CCS</sub>	12	V <sub>I</sub> =0, 10pin=GND	-	0	5	μA
IF amplifier input resistance	R <sub>IN</sub>	-		-	2	-	kΩ
S / N 1	S / N 1	1	V <sub>I</sub> =60dBμEMF	-	62	-	dB
S / N 2	S / N 2	1	V <sub>I</sub> =25dBμEMF	-	35	-	dB
-3dB limiting sensitivity	V <sub>IN</sub> (lim)	1		-	22	27	dBμEMF
Demodulated output level	V <sub>OD</sub>	1	V <sub>I</sub> =60dBμEMF	30	46	65	mVrms
AM rejection ratio	AMR	1	V <sub>I</sub> =60dBμEMF, AM=30%	-	50	-	dB
Duty ratio of wave shaped output	DR	2	V <sub>I</sub> =60dBμEMF	40	50	60	%
RSSI output voltage	V <sub>RSSI</sub>	10	V <sub>I</sub> =80dBμEMF	0.48	0.62	0.76	V
RSSI output resistance	R <sub>RSSI</sub>	-		-	62	-	kΩ
Quick charge / discharge current	I <sub>ch</sub>	13	4pin=GND, 8pin=0.18V	35	65	110	μA
Alarm voltage	V <sub>alm</sub>	3		1.05	1.10	1.15	V
Regulator output voltage	V <sub>reg</sub>	8	R <sub>L</sub> =430Ω	0.95	1.00	1.05	V
Low level output voltage of VALM terminal	V <sub>almL</sub>	4	I <sub>L</sub> =100μA	-	0.1	0.4	V
High level leak current of VALM terminal	I <sub>almH</sub>	5		-	0	2	μA
Low level output voltage of FSK-OUT terminal	V <sub>fskL</sub>	6	I <sub>L</sub> =100μA	-	0.1	0.4	V
High level leak current of FSK-OUT terminal	I <sub>fskH</sub>	7		-	0	2	μA
Low level output voltage of REG-CONT terminal	V <sub>regL</sub>	9	I <sub>L</sub> =100μA	-	-	0.6	V

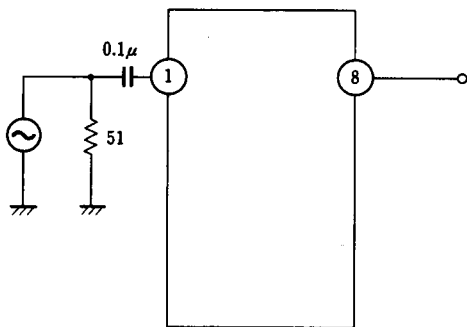
## ■ TEST CIRCUIT



CD:CDBC455CX (MURATA MFG.)

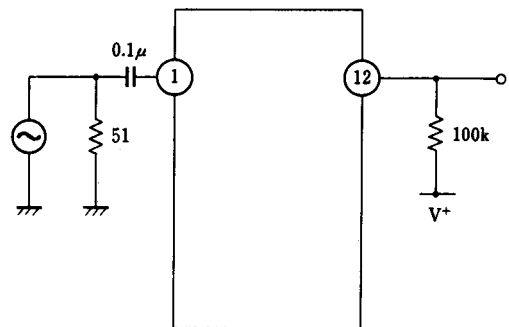
### Test Circuit 1

S/N1, S/N2,  $V_{IN}(lim)$ ,  $V_{OD}$ , AMR



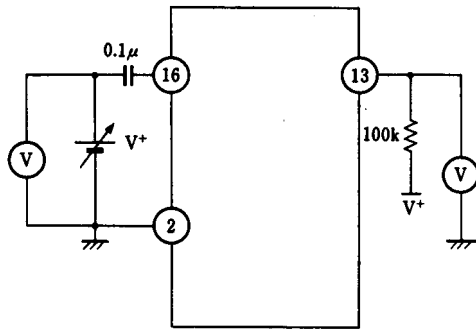
### Test Circuit 2

DR



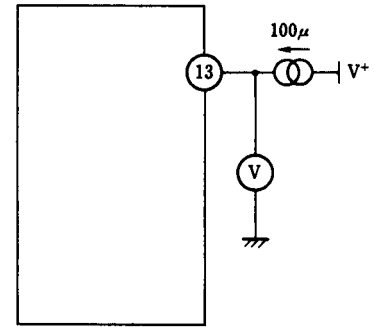
**Test Circuit 3**

Valm



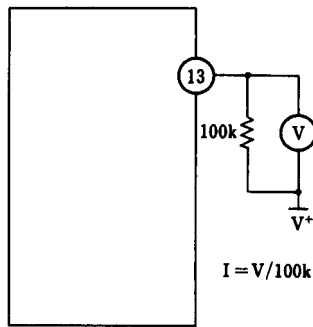
**Test Circuit 4**

ValmL



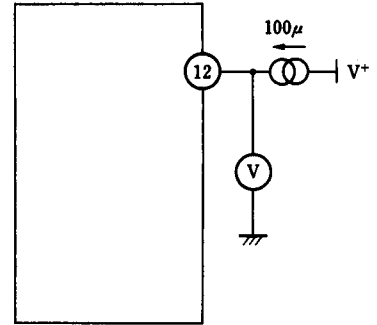
**Test Circuit 5**

IalmH



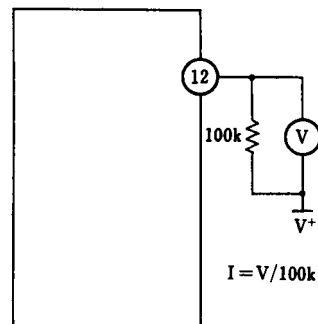
**Test Circuit 6**

VfSkL



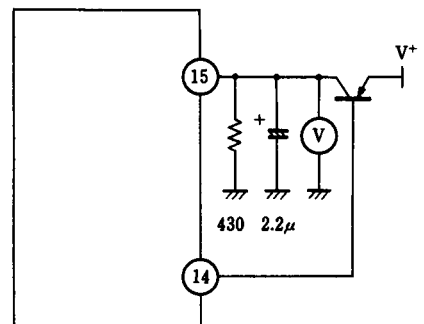
**Test Circuit 7**

IfskH



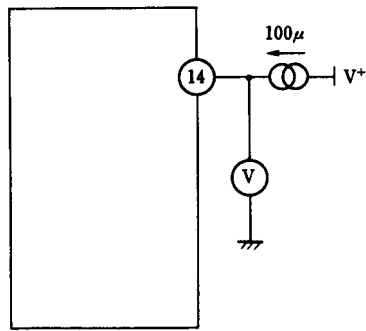
**Test Circuit 8**

Vreg



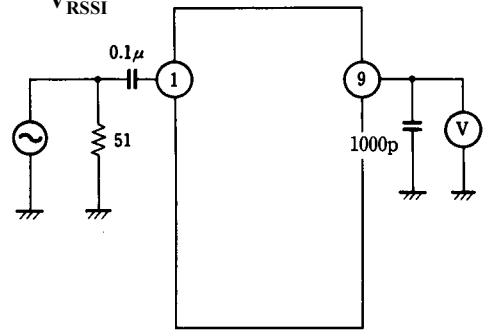
**Test Circuit 9**

VregL



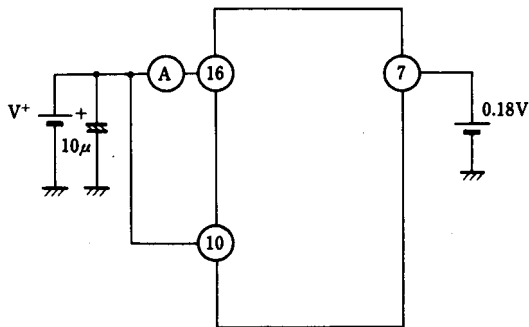
**Test Circuit 10**

V<sub>RSSI</sub>



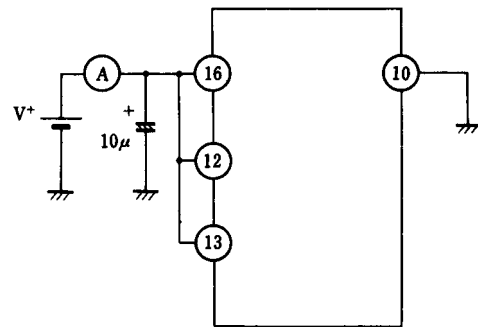
**Test Circuit 11**

I<sub>ccq</sub>



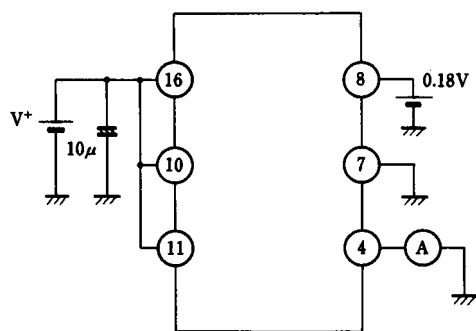
**Test Circuit 12**

I<sub>ccs</sub>



**Test Circuit 13**

I<sub>ch</sub>



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## ■ TERMINAL FUNCTION

PIN NO.	SYMBOL	FUNCTION	EQUIVALENT CIRCUIT
1	IF-IN	An IF amplifier input. Typical input impedance is 2k $\Omega$ .	
3	DEC	A Decoupling terminal which is connected with a decoupling capacitor.	
2	GND	Ground	
5	QUAD-IN	An input terminal of a quadrature detection circuit. This terminal will be connect with a ceramic discriminator.	
9	RSSI OUT	An RSSI Output. This voltage level is in logarithmic proportion to the input signal level.	
6	AF-OUT	An FM demodulated signal output.	

## ■ TERMINAL FUNCTION

PIN NO.	SYMBOL	FUNCTION	EQUIVALENT CIRCUIT
7	LPF-IN	An input terminal of a low pass filter. This terminal is biased from the AF-OUT terminal (6pin) through an external RC filter.	
8	LPF-OUT	An output terminal of a low pass filter.	
4	FSK-REF	A Reference input terminal of a wave shaping comparator. This terminal is connected with an external capacitor.	
12	FSK-OUT	An output terminal of a wave shaping circuit. The Wave shaped signal inverted for the LPF output comes out.	
10	BS	A Control terminal of a battery saving circuit. H : This circuit is OFF. L : This circuit is ON.	

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## ■ TERMINAL FUNCTION

PIN NO.	SYMBOL	FUNCTION	EQUIVALENT CIRCUIT
11	CHARGE	A Control terminal of a quick charge / discharge circuit. H : This circuit is ON. L : This circuit is OFF.	
13	VALM	An output terminal of the alarm signal. When $V^+$ drops down to 1.1V, this output becomes high.	
14	REG CONT	A Control terminal of an external PNP transistor used for the regulator.	
15	REG-OUT	A Monitoring terminal of the regulator.	
16	$V^+$	Supply voltage	



## ■ FSK WAVE SHAPING FUNCTION

When the demodulated FSK signal is weak or noisy, the micro computer may fail to read data.

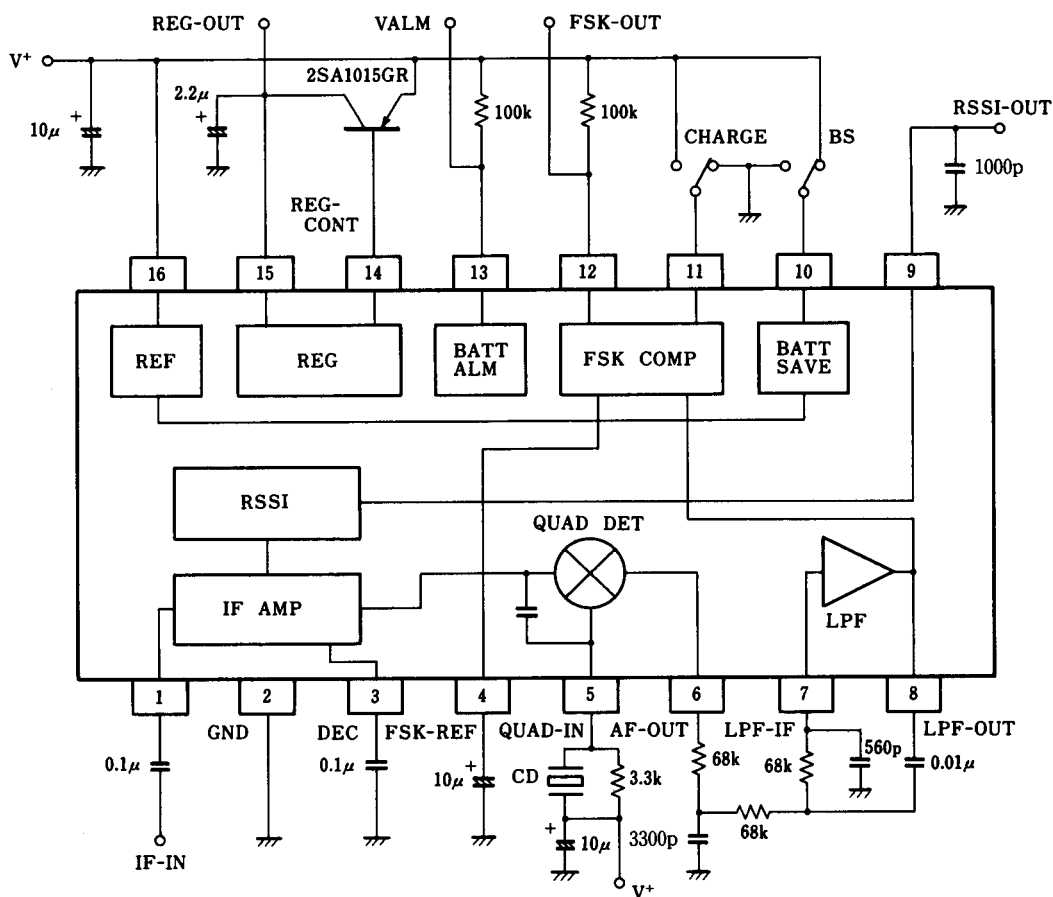
The wave shaping circuit (comparator) will change those signals to the correct logical signal to prevent the readerror.

## ■ QUICK CHARGE / DISCHARGE FUNCTION

The DC voltage of the FSK-REF terminal is equal to that of the demodulated FSK signal. When the battery saving state turns into the ordinary state, the FSK-REF terminal voltage will be late to come up to the reference voltage by the time constant of an external capacitor and an internal resistor, and the wave shaped data may be failed. This circuit will charge/ discharge the external capacitor quickly to prevent the error.

When the DC level of the FM demodulated output changes in the operation mode of this function, the FSK-REF terminal voltage follows to the FSK demodulated output DC voltage, and the FSK output duty ratio can be constant.

## ■ APPLICATION CIRCUIT



CD: CDBC455CX (MURATA MFG.)

[CAUTION]  
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