3-INPUT / 2-INPUT VIDEO SWITCH

■ GENERAL DESCRIPTION

The **NJM2506** is video switch for video and audio signal. It contains 3 input-1 output and 2 input-1 output video switch. 3 input-1 output switch has clamp function and so is applied to fixed DC level of video signal. Its operating voltage is 4.75 to 13V and bahdwidth is 10MHz. Crosstalk is 75dB (at f = 4.43MHz)

 V^+

■ FEATURES

- Wide Operating Supply Range (+4.75V to +13V)
- 3 Input-1 Output and 2 Input-1 Output
- Internal Clamp Function
- Crosstalk 75dB (at 4.43MHz)
- Wide Frequency Range 10MHz (2V_{P-P} Input)
- Package Outline DIP16, DMP16
- Bipolar Technology

■ RECOMMENDED OPERATING CONDITION

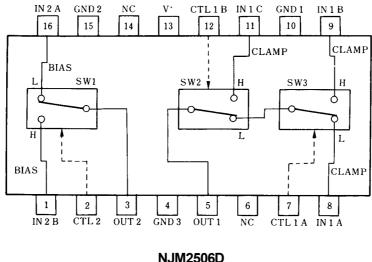
Operating Voltage

4.75V to 13.0V

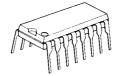
■ APPLICATION

• VCR, Video Camera, AV-TV, Video Disk Player.

BLOCK DIAGRAM



NJM2506M





NJM2506D

NJM2506M

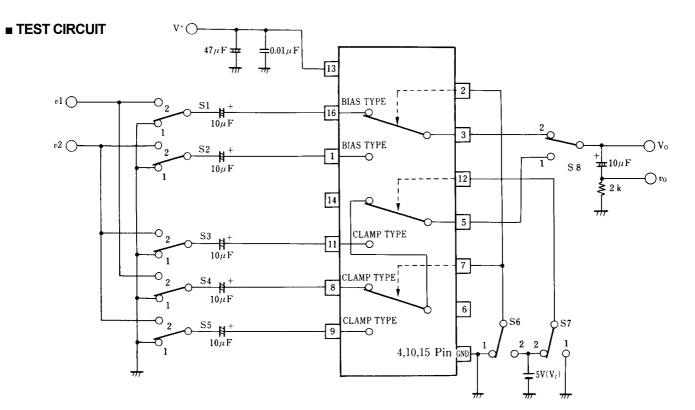
ABSOLUTE MAXIMUM RAT	(T _a = 25°C)		
PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V ⁺	14	V
Power Dissipation	PD	(DIP16) 700 (DMP16) 350	mW mW
Operating Temperature Range	T _{opr}	-40 to +85	°C
Storage Temperature Range	T _{stg}	-40 to +125	°C

ELECTRICAL CHARACTERISTICS

 $(V^+ = 5V, T_a = 25^{\circ}C)$ TEST CONDITION PARAMETER SYMBOL MIN. TYP. MAX. UNIT Operating Current (1) $V^+ = 5V$ (Note1) I_{CC1} 6.7 9.7 12.7 mΑ $V^+ = 9V$ (Note1) 16.0 **Operating Current (2)** ICC2 8.6 12.3 mΑ Voltage Gain Gv $V_{I} = 2V_{P-P} / 100 khz, V_{O} / V_{I}$ -0.6 -0.1 +0.4 dB $V_1 = 2V_{P-P}, V_0 (10MHz / 100kHz)$ Frequency Response Gf -1.0 0 +1.0 dB **Differential Gain** DG $V_{I} = 2V_{P-P}$, Staircase Signal 0.3 % DP **Differential Phasa** VI = 2VP-P, Staircase Signal 0.3 deg V_{OS1} Output offset Voltage (1) (Note2) -10 0 +10 mV Output offset Voltage (2) V_{OS2} (Note2) -30 0 +30 mV Crosstalk СТ dB $V_{I} = 2V_{P-P}, 4.43MHz, V_{O} / V_{I}$ _ -75 _ All inside SW : ON Switch Change Voltage V_{CH} -2.5 V Switch Change Voltage V_{CL} All inside SW : OFF 1.0 V

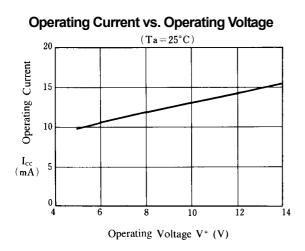
(Note1): S1 = S2 = S3 = S4 = S5 = S6 = S7 = 1

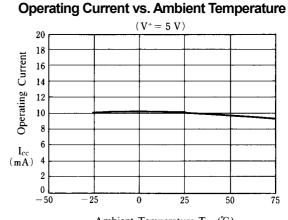
(Note2) : Output DC Voltage Difference is tested on S6 = $1 \rightarrow 2$, S1 = S2 = S3 = S4 = S5 = 1, S8 = 2 and S7 = 1 (Note3): Output DC Voltage Difference is tested on S6 = $1 \rightarrow 2$, S7 = 1 (or S6 = 1, S7 = $1 \rightarrow 2$.), S1 = S2 = S3 = S4 = S5 = 1 and S8 = 1



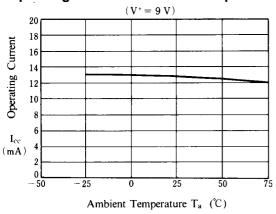
■ PIN FUNCTION

PIN No.	PIN NAME	DC VOLTAGE	INSIDE EQUIVALENT CIRCUIT
16 1	IN 2 A IN 2 B [Input]	2.5V	500 15k 2.5V
8 9 11	IN 1A IN 1B IN 1C [Input]	1.5V	
7 12 2	CTL 1A CTL 1B CTL 2 [Control]		2.3V + 1.9V +
5	OUT1 [Output]	1.8V	
3	OUT2 [Output]	0.8V	O OUT
13	V ⁺	5V	
15 4 10	GND 1 GND 2 GND 3		

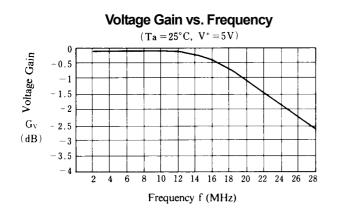


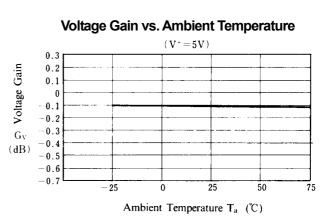


Operating Current vs. Ambient Temperature



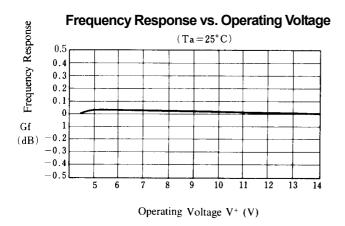
Voltage Gain vs. Operating Voltage $(Ta = 25^{\circ}C)$ 0.3 Voltage Gain 0.2 0.1 0 -0.1-0.2 $G_{\rm v}$ -0.3-0.4(dB)-0.5 -0.6-0.75 6 7 8 9 10 11 12 13 14 Operating Voltage V+ (V)

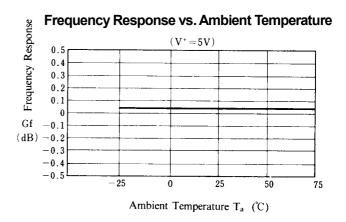


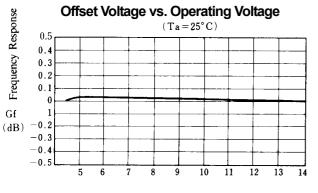


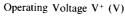
Ambient Temperature T_a (°C)

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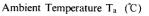


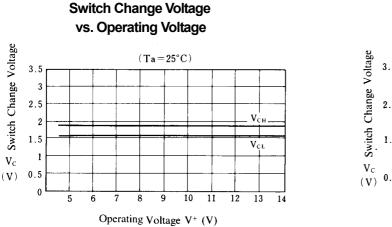




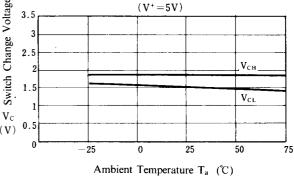


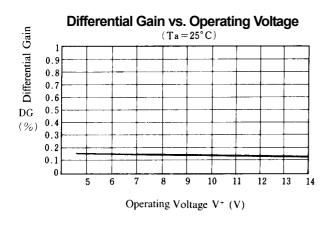
Offset Voltage vs. Ambient Temperature (V'=5V)30 25 Offset Voltage 20 15 10 5 0 5 _ -10Vos -15-20 (\mathbf{mV}) - 25 - 30 -- 25 0 25 50 75

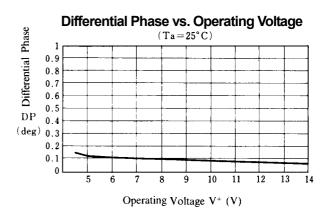


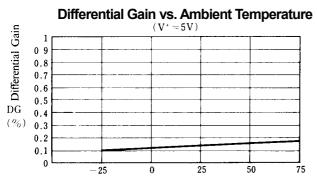


Switch Change Voltage vs. Ambient Temperature

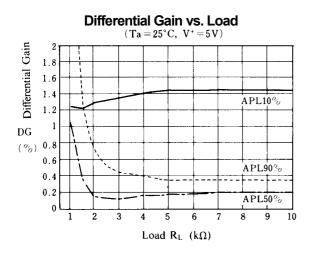


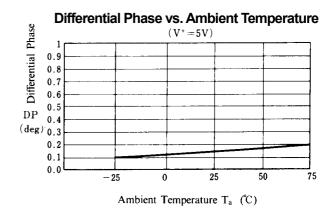


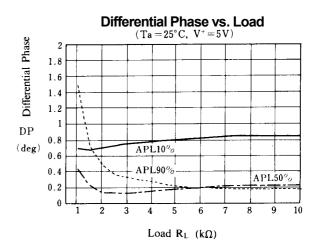




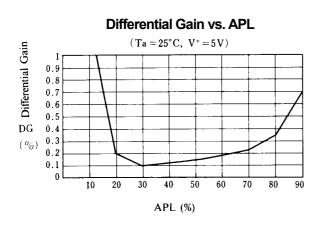
Ambient Temperature T_a (°C)

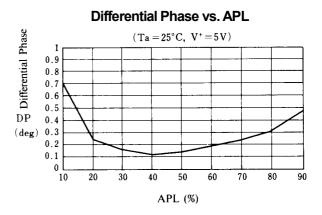


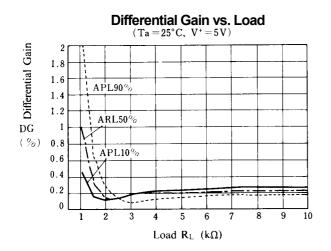


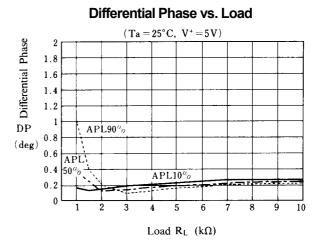


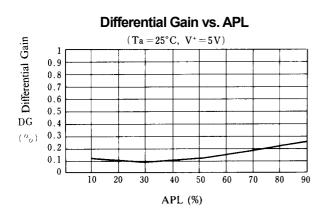




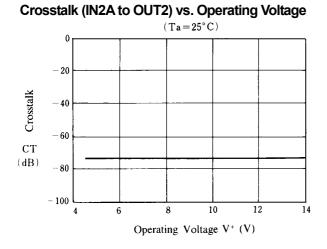




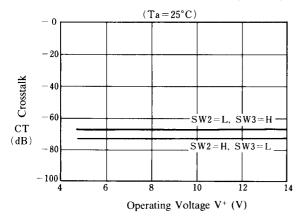


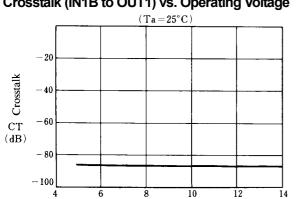


Differential Phase vs. APL Differential Phase $(Ta = 25^{\circ}C, V^{+} = 5V)$ 1 0.9 0.8 0.7 0.6 0.5 DG 0.4 0.3 (deg) 0.2 0.1 0 10 20 30 40 50 60 70 80 90 APL (%)



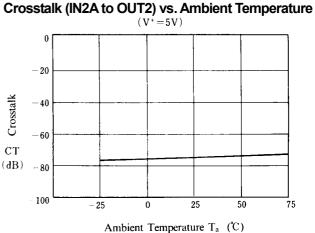
Crosstalk (IN1B to OUT1) vs. Operating Voltage

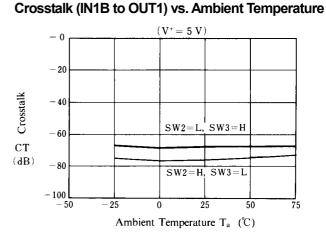


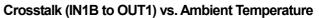


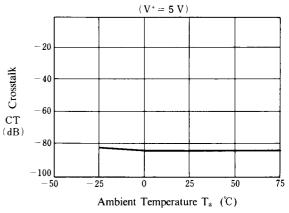
Operating Voltage V⁺ (V)

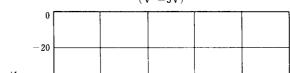
Crosstalk (IN1B to OUT1) vs. Operating Voltage



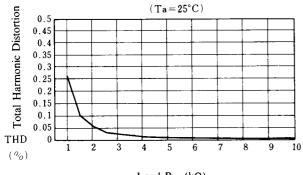








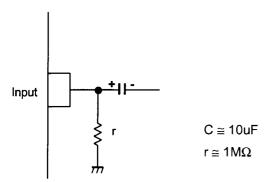




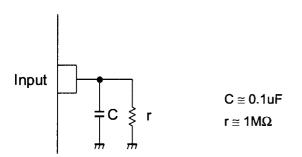


■ APPLICATION

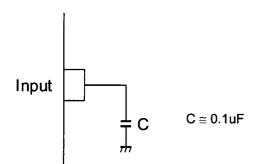
This IC requires $1M\Omega$ resistance between INPUT and GND pin for clamp type input since the minute current causes an unstable pin voltage.



This IC requires 0.1μ F capacitor between INPUT and GND, $1M\Omega$ resistance between INPUT and GND for clamp type input at mute mode.



This IC requires 0.1µF capacitor between INPUT and GND for bias type input at mute mode.



[CAUTION]	
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