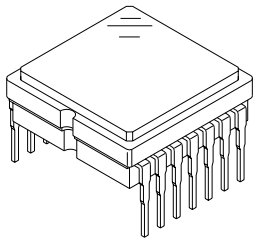
SONY

ICX278AK

Diagonal 4.5mm (Type 1/4) CCD Image Sensor for NTSC Color Video Cameras

14 pin DIP (Plastic)



Description

The ICX278AK is an interline CCD solid-state image sensor suitable for NTSC color video cameras with a diagonal 4.5mm (Type 1/4) system. Compared with the current product ICX208AK, basic characteristics such as sensitivity, smear and dynamic range are improved drastically through the adoption of EXview HAD CCD™ technology.

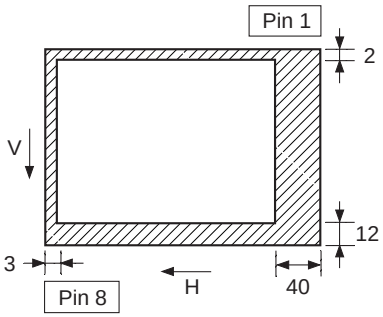
This chip features a field period readout system and an electronic shutter with variable charge-storage time.

The package is a 10mm-square 14-pin DIP (Plastic).

EXview HAD CCD™ has different spectral characteristics from the current CCD.

Features

- High sensitivity (+5dB compared with the ICX208AK)
- Low smear (–20dB compared with the ICX208AK)
- High D range (+2dB compared with the ICX208AK)
- Horizontal register: 3.3 to 5.0V drive
- Reset gate: 3.3 to 5.0V drive
- No voltage adjustment
  (Reset gate and substrate bias are not adjusted.)
- High resolution, low smear and low dark current
- Excellent antiblooming characteristics
- Continuous variable-speed shutter
- Recommended range of exit pupil distance: –20 to –100mm
- Ye, Cy, Mg, and G complementary color mosaic filters on chip



Optical black position
(Top View)

Device Structure

- Interline CCD image sensor
- Image size: Diagonal 4.5mm (Type 1/4)
- Number of effective pixels: 768 (H) × 494 (V) approx. 380K pixels
- Total number of pixels: 811 (H) × 508 (V) approx. 410K pixels
- Chip size: 4.43mm (H) ×3.69mm (V)
- Unit cell size: 4.75µm (H) × 5.55µm (V)
- Optical black: Horizontal (H) direction: Front 3 pixels, rear 40 pixels
  Vertical (V) direction: Front 12 pixels, rear 2 pixels
- Number of dummy bits: Horizontal 22
  Vertical 1 (even fields only)
- Substrate material: Silicon

EXview HAD CCD™

* EXview HAD CCD is a trademark of Sony Corporation.
EXview HAD CCD is a CCD that drastically improves light efficiency by including near infrared light region as a basic structure of HAD (Hole-Accumulation-Diode) sensor.

E00Z49A3Z

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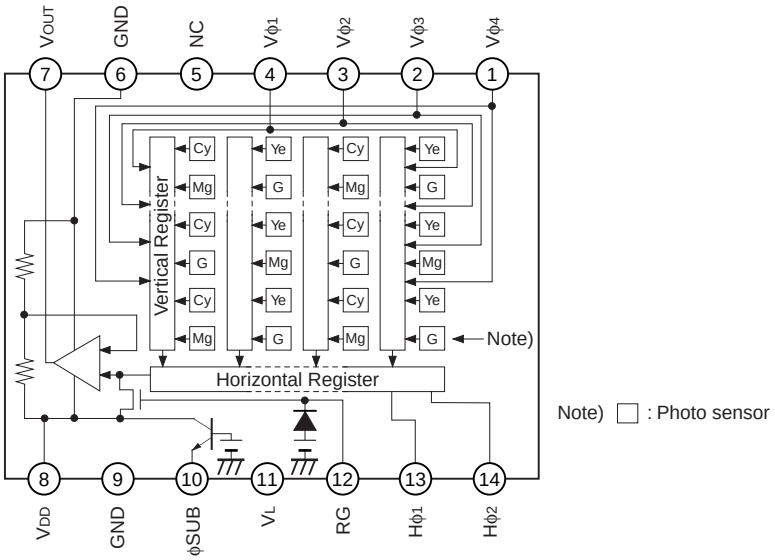
Block Diagram and Pin Configuration

(Top View)



Pin Description

Pin No.	Symbol	Description	Pin No.	Symbol	Description
1	$V\phi_4$	Vertical register transfer clock	8	V_{DD}	Supply voltage
2	$V\phi_3$	Vertical register transfer clock	9	GND	GND
3	$V\phi_2$	Vertical register transfer clock	10	φSUB	Substrate clock
4	$V\phi_1$	Vertical register transfer clock	11	V_L	Protective transistor bias
5	NC		12	RG	Reset gate clock
6	GND	GND	13	$H\phi_1$	Horizontal register transfer clock
7	V_{OUT}	Signal output	14	$H\phi_2$	Horizontal register transfer clock

Absolute Maximum Ratings

Item		Ratings	Unit	Remarks
Against φSUB	V_{DD}, V_{OUT}, RG – φSUB	–40 to +8	V	
	$V\phi_1$, $V\phi_3$ – φSUB	–50 to +15	V	
	$V\phi_2$, $V\phi_4$, V_L – φSUB	–50 to +0.3	V	
	$H\phi_1$, $H\phi_2$, GND – φSUB	–40 to +0.3	V	
Against GND	V_{DD}, V_{OUT}, RG – GND	–0.3 to +18	V	
	$V\phi_1$, $V\phi_2$, $V\phi_3$, $V\phi_4$ – GND	–10 to +18	V	
	$H\phi_1$, $H\phi_2$ – GND	–10 to +6	V	
Against V_L	$V\phi_1$, $V\phi_3$ – V_L	–0.3 to +28	V	
	$V\phi_2$, $V\phi_4$, $H\phi_1$, $H\phi_2$, GND – V_L	–0.3 to +15	V	
Between input clock pins	Voltage difference between vertical clock input pins	to +15	V	*1
	$H\phi_1$ – $H\phi_2$	–5 to +5	V	
	$H\phi_1$, $H\phi_2$ – $V\phi_4$	–13 to +13	V	
Storage temperature		–30 to +80	°C	
Operating temperature		–10 to +60	°C	

*1 +24V (Max.) when clock width < 10µs, clock duty factor < 0.1%.

Bias Conditions

Item	Symbol	Min.	Typ.	Max.	Unit	Remarks
Supply voltage	V_{DD}	14.55	15.0	15.45	V	
Protective transistor bias	V_L	*1				
Substrate clock	ϕSUB	*2				
Reset gate clock	ϕRG	*2				

*1 V_L setting is the V_{VL} voltage of the vertical transfer clock waveform, or the same power supply as the V_L power supply for the V driver should be used.

*2 Do not apply a DC bias to the substrate clock and reset gate clock pins, because a DC bias is generated within the CCD.

DC Characteristics

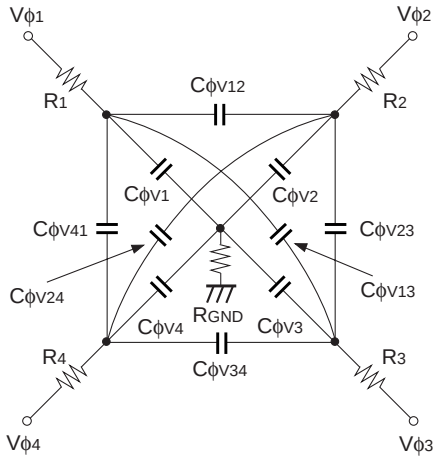
Item	Symbol	Min.	Typ.	Max.	Unit	Remarks
Supply current	I_{DD}		4	6	mA	

Clock Voltage Conditions

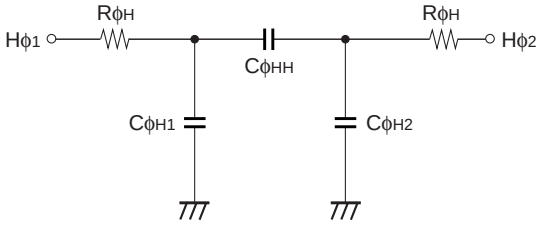
Item	Symbol	Min.	Typ.	Max.	Unit	Waveform diagram	Remarks
Readout clock voltage	V_{VT}	14.55	15.0	15.45	V	1	
Vertical transfer clock voltage	V_{VH1}, V_{VH2}	–0.05	0	0.05	V	2	$V_{VH} = (V_{VH1} + V_{VH2})/2$
	V_{VH3}, V_{VH4}	–0.2	0	0.05	V	2	
	V_{VL1}, V_{VL2}, V_{VL3}, V_{VL4}	–8.0	–7.0	–6.5	V	2	$V_{VL} = (V_{VL3} + V_{VL4})/2$
	$V\phi V$	6.3	7.0	8.05	V	2	$V\phi V = V_{VH}n – V_{VL}n$ (n = 1 to 4)
	$V_{VH3} – V_{VH}$	–0.25		0.1	V	2	
	$V_{VH4} – V_{VH}$	–0.25		0.1	V	2	
	V_{VHH}			0.3	V	2	High-level coupling
	V_{VHL}			0.3	V	2	High-level coupling
	V_{VLH}			0.3	V	2	Low-level coupling
	V_{VLL}			0.3	V	2	Low-level coupling
Horizontal transfer clock voltage	$V\phi H$	3.0	3.3	5.25	V	3	
	V_{HL}	–0.05	0	0.05	V	3	
Reset gate clock voltage	$V\phi RG$	3.0	3.3	5.5	V	4	Input through 0.1µF capacitance
	$V_{RGLH} – V_{RGLL}$			0.4	V	4	Low-level coupling
	$V_{RGL} – V_{RGLm}$			0.5	V	4	Low-level coupling
Substrate clock voltage	$V\phi SUB$	21.0	22.0	23.5	V	5	

Clock Equivalent Circuit Constant

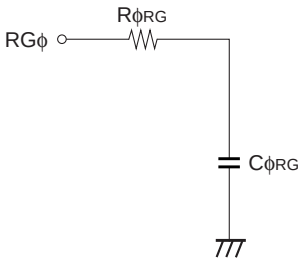
Item	Symbol	Min.	Typ.	Max.	Unit	Remarks
Capacitance between vertical transfer clock and GND	$C\phi V1$, $C\phi V3$		1200		pF	
	$C\phi V2$, $C\phi V4$		560		pF	
Capacitance between vertical transfer clocks	$C\phi V12$, $C\phi V34$		220		pF	
	$C\phi V23$, $C\phi V41$		120		pF	
	$C\phi V13$		82		pF	
	$C\phi V24$		75		pF	
Capacitance between horizontal transfer clock and GND	$C\phi H1$, $C\phi H2$		22		pF	
Capacitance between horizontal transfer clocks	$C\phi HH$		36		pF	
Capacitance between reset gate clock and GND	$C\phi RG$		5		pF	
Capacitance between substrate clock and GND	$C\phi SUB$		180		pF	
Vertical transfer clock series resistor	R_1, R_2, R_3, R_4		82		Ω	
Vertical transfer clock ground resistor	R_{GND}		15		Ω	
Horizontal transfer clock series resistor	$R\phi H$		12		Ω	
Reset gate clock series resistor	$R\phi RG$		51		Ω	



Vertical transfer clock equivalent circuit



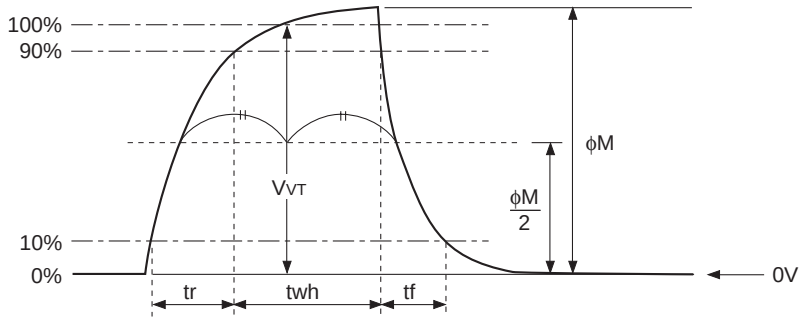
Horizontal transfer clock equivalent circuit



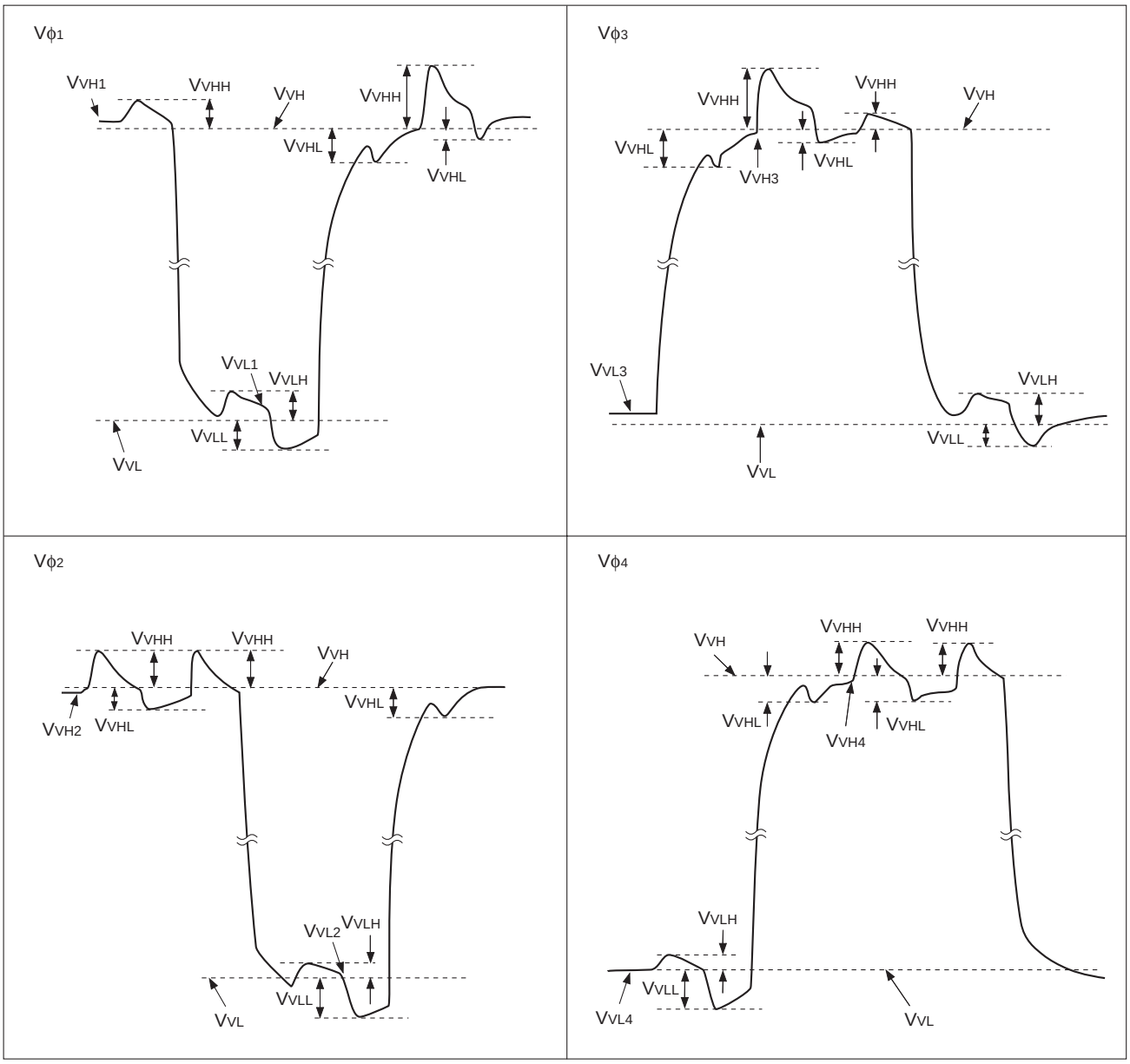
Reset gate clock equivalent circuit

Drive Clock Waveform Conditions

(1) Readout clock waveform



(2) Vertical transfer clock waveform

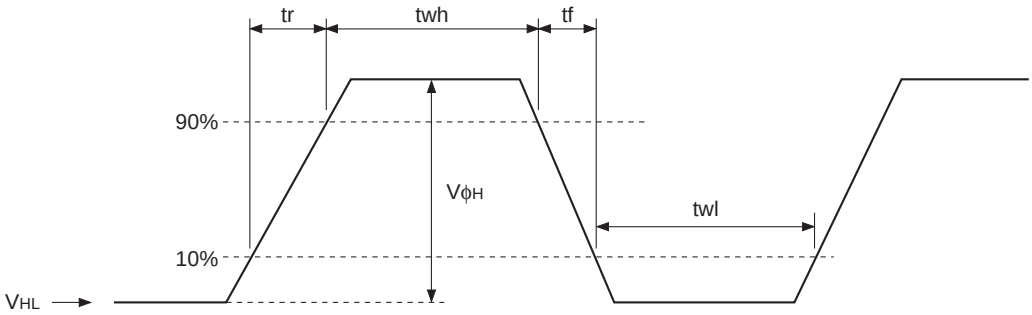


$V_{VH} = (V_{VH1} + V_{VH2})/2$

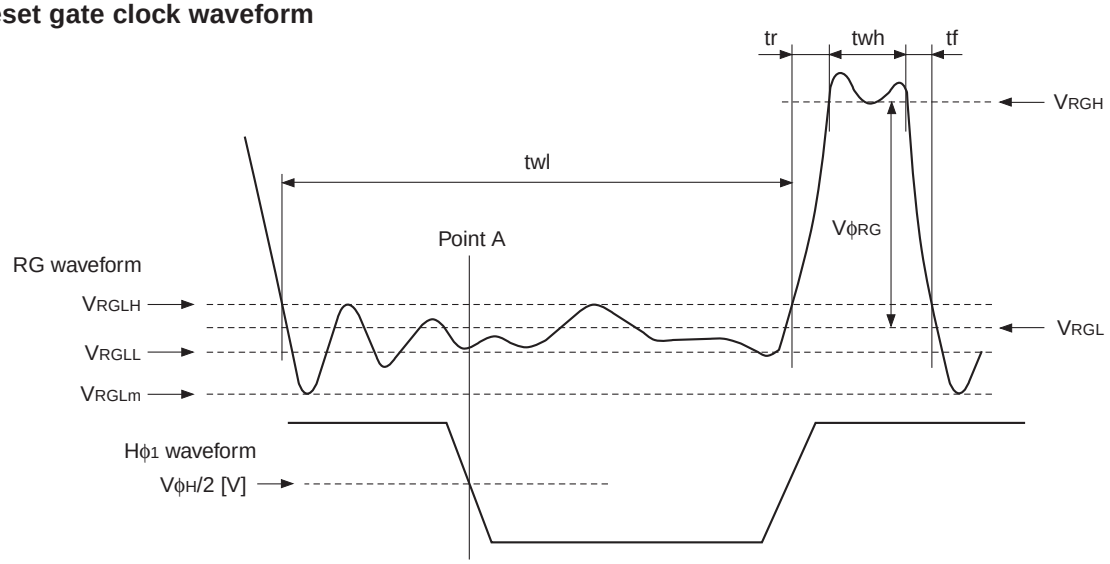
$V_{VL} = (V_{VL3} + V_{VL4})/2$

$V_{\phi V} = V_{VH}n - V_{VL}n$ (n = 1 to 4)

(3) Horizontal transfer clock waveform



(4) Reset gate clock waveform



V_{RGLH} is the maximum value and V_{RGLL} is the minimum value of the coupling waveform during the period from Point A in the above diagram until the rising edge of RG. In addition, V_{RGL} is the average value of V_{RGLH} and V_{RGLL}.

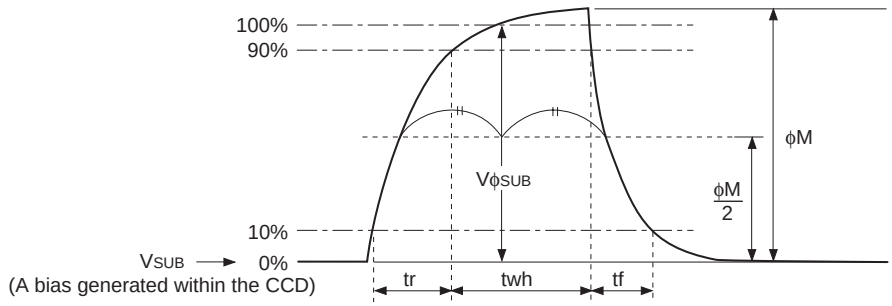
$$V_{RGL} = (V_{RGLH} + V_{RGLL})/2$$

Assuming V_{RGH} is the minimum value during the interval twh, then:

$$V\phi_{RG} = V_{RGH} - V_{RGL}$$

Negative overshoot level during the falling edge of RG is V_{RGLm}.

(5) Substrate clock waveform



Clock Switching Characteristics

Item		Symbol	twh			twl			tr			tf			Unit	Remarks
			Min.	Typ.	Max.	Min.	Typ.	Max.	Min.	Typ.	Max.	Min.	Typ.	Max.		
Readout clock		V_T	2.3	2.5						0.5			0.5		µs	During readout
Vertical transfer clock		$V\phi_1$, $V\phi_2$, $V\phi_3$, $V\phi_4$										15		250	ns	*1
Horizontal transfer clock	During imaging	$H\phi_1$	26	28.5		26	28.5			6.5	9.5		6.5	9.5	ns	*2
		$H\phi_2$	26	28.5		26	28.5			6.5	9.5		6.5	9.5		
	During parallel-serial conversion	$H\phi_1$		5.38						0.01			0.01		µs	
		$H\phi_2$					5.38			0.01			0.01			
Reset gate clock		ϕRG	11	13			51			3			3		ns	
Substrate clock		ϕSUB	1.5	1.8							0.5			0.5	µs	When draining charge

*1 When vertical transfer clock driver CXD1267AN is used.

*2 When $V\phi_H$ = 3.0V. tf ≥ tr – 2ns, and the cross-point voltage (V_{CR}) for the $H\phi_1$ rising side of the $H\phi_1$ and $H\phi_2$ waveforms must be at least $V\phi_H/2$ [V].

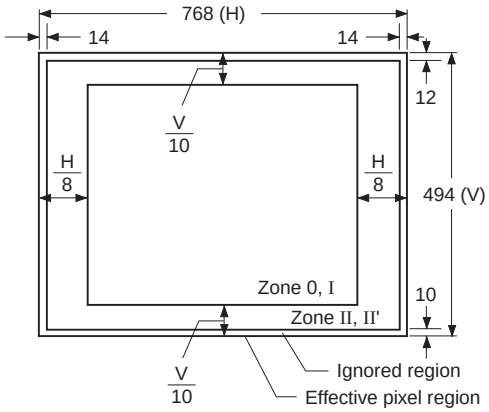
Item	Symbol	two			Unit	Remarks
		Min.	Typ.	Max.		
Horizontal transfer clock	$H\phi_1$, $H\phi_2$	22	26		ns	*3

*3 The overlap period for twh and twl of horizontal transfer clocks $H\phi_1$ and $H\phi_2$ is two.

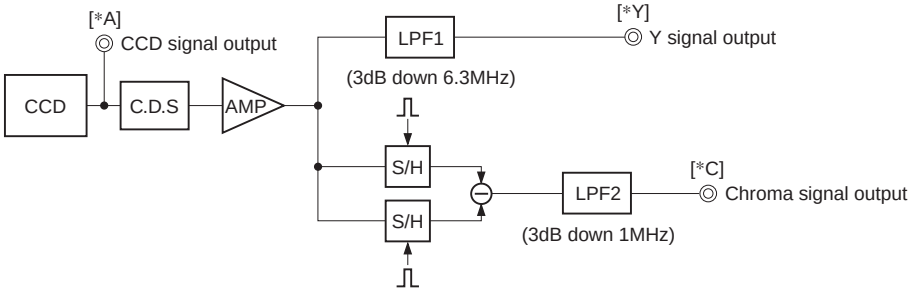
Image Sensor Characteristics (Ta = 25°C)

Item	Symbol	Min.	Typ.	Max.	Unit	Measurement method	Remarks
Sensitivity	S	640	800		mV	1	
Sensitivity ratio	R_{MgG}	0.93		1.35		2	
	R_{YeCy}	1.15		1.53		2	
Saturation signal	Ysat	1000			mV	3	Ta = 60°C
Smear	Sm		–108	–100	dB	4	
Video signal shading	SHy			20	%	5	Zone 0 and I
				25	%	5	Zone 0 to II'
Uniformity between video signal channels	ΔSr			10	%	6	
	ΔSb			10	%	6	
Dark signal	Ydt			2	mV	7	Ta = 60°C
Dark signal shading	ΔYdt			1	mV	8	Ta = 60°C
Flicker Y	Fy			2	%	9	
Flicker R-Y	Fcr			5	%	9	
Flicker B-Y	Fcb			5	%	9	
Line crawl R	Lcr			3	%	10	
Line crawl G	Lcg			3	%	10	
Line crawl B	Lcb			3	%	10	
Line crawl W	Lcw			3	%	10	
Lag	Lag			0.5	%	11	

Zone Definition of Video Signal Shading



Measurement System



Note) Adjust the amplifier gain so that the gain between [*A] and [*Y] , and between [*A] and [*C] equals 1.

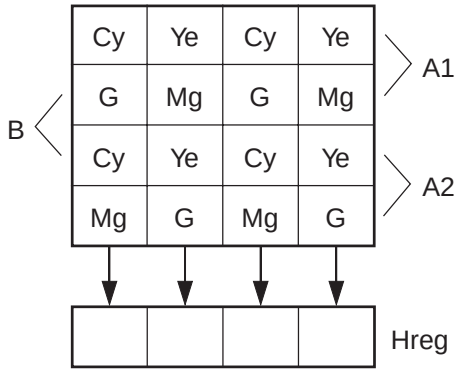
Image Sensor Characteristics Measurement Method

◎ Measurement conditions

1) In the following measurements, the device drive conditions are at the typical values of the bias and clock voltage conditions.

2) In the following measurements, spot blemishes are excluded and, unless otherwise specified, the optical black level (OB) is used as the reference for the signal output, which is taken as the value of Y signal output or chroma signal output of the measurement system.

◎ Color coding of this image sensor & Composition of luminance (Y) and chroma (color difference) signals



Color Coding Diagram

As shown in the left figure, fields are read out. The charge is mixed by pairs such as A1 and A2 in the A field. (pairs such as B in the B field)
As a result, the sequence of charges output as signals from the horizontal shift register (Hreg) is, for line A1, (G + Cy), (Mg + Ye), (G + Cy), and (Mg + Ye).

These signals are processed to form the Y signal and chroma (color difference) signal. The Y signal is formed by adding adjacent signals, and the chroma signal is formed by subtracting adjacent signals. In other words, the approximation:

$$Y = \{(G + Cy) + (Mg + Ye)\} \times 1/2$$
$$= 1/2 \{2B + 3G + 2R\}$$

is used for the Y signal, and the approximation:

$$R - Y = \{(Mg + Ye) - (G + Cy)\}$$
$$= \{2R - G\}$$

is used for the chroma (color difference) signal. For line A2, the signals output from Hreg in sequence are

(Mg + Cy), (G + Ye), (Mg + Cy), (G + Ye).

The Y signal is formed from these signals as follows:

$$Y = \{(G + Ye) + (Mg + Cy)\} \times 1/2$$
$$= 1/2 \{2B + 3G + 2R\}$$

This is balanced since it is formed in the same way as for line A1.
In a like manner, the chroma (color difference) signal is approximated as follows:

$$-(B - Y) = \{(G + Ye) - (Mg + Cy)\}$$
$$= -\{2B - G\}$$

In other words, the chroma signal can be retrieved according to the sequence of lines from R – Y and – (B – Y) in alternation. This is also true for the B field.

◎ Definition of standard imaging conditions

1) Standard imaging condition I:
Use a pattern box (luminance 706cd/m^2, color temperature of 3200K halogen source) as a subject. (Pattern for evaluation is not applicable.) Use a testing standard lens with CM500S (t = 1.0mm) as an IR cut filter and image at F5.6. The luminous intensity to the sensor receiving surface at this point is defined as the standard sensitivity testing luminous intensity.

2) Standard imaging condition II:
Image a light source (color temperature of 3200K) with a uniformity of brightness within 2% at all angles. Use a testing standard lens with CM500S (t = 1.0mm) as an IR cut filter. The luminous intensity is adjusted to the value indicated in each testing item by the lens diaphragm.

3) Standard imaging condition III:
Image a light source (color temperature of 3200K) with a uniformity of brightness within 2% at all angles. Use a testing standard lens (exit pupil distance –33mm) with CM500S (t = 1.0mm) as an IR cut filter. The luminous intensity is adjusted to the value indicated in each testing item by the lens diaphragm.

1. Sensitivity
Set to standard imaging condition I. After selecting the electronic shutter mode with a shutter speed of 1/250s, measure the Y signal (Ys) at the center of the screen and substitute the value into the following formula.

$$S = Ys \times \frac{250}{60} \text{ [mV]}$$

2. Sensitivity ratio
Set to standard imaging condition II. Adjust the luminous intensity so that the average value of the Y signal output is 200mV, and then measure the Mg signal output (S_{Mg} [mV]) and G signal output (S_G [mV]), and Ye signal output (S_{Ye} [mV]) and Cy signal output (S_{Cy} [mV]) at the center of the screen with frame readout method. Substitute the values into the following formula.

$$R_{MgG} = S_{Mg}/S_G$$
$$R_{YeCy} = S_{Ye}/S_{Cy}$$

3. Saturation signal
Set to standard imaging condition II. After adjusting the luminous intensity to 10 times the intensity with average value of the Y signal output, 200mV, measure the minimum value of the Y signal.

4. Smear
Set to standard imaging condition II. With the lens diaphragm at F5.6 to F8, adjust the luminous intensity to 500 times the intensity with average value of the Y signal output, 200mV. When the readout clock is stopped and the charge drain is executed by the electronic shutter at the respective H blankings, measure the maximum value (YSm [mV]) of the Y signal output and substitute the value into the following formula.

$$Sm = 20 \times \log\left(\frac{YSm}{200} \times \frac{1}{500} \times \frac{1}{10}\right) \text{ [dB] (1/10V method conversion value)}$$

5. Video signal shading
   Set to standard imaging condition III. With the lens diaphragm at F5.6 to F8, adjust the luminous intensity so that the average value of the Y signal output is 200mV. Then measure the maximum (Ymax [mV]) and minimum (Ymin [mV]) values of the Y signal and substitute the values into the following formula.

   $SHy = (Ymax - Ymin)/200 \times 100$ [%]

6. Uniformity between video signal channels
   Set to standard imaging condition II. Adjust the luminous intensity so that the average value of the Y signal output is 200mV, and then measure the maximum (Crmax, Cbmax [mV]) and minimum (Crmin, Cbmin [mV]) values of the R – Y and B – Y channels of the chroma signal and substitute the values into the following formula.

   $\Delta Sr = | (Crmax - Crmin)/200 | \times 100$ [%]
   $\Delta Sb = | (Cbmax - Cbmin)/200 | \times 100$ [%]

7. Dark signal
   Measure the average value of the Y signal output (Ydt [mV]) with the device ambient temperature 60°C and the device in the light-obstructed state, using the horizontal idle transfer level as a reference.

8. Dark signal shading
   After measuring 7, measure the maximum (Ydmax [mV]) and minimum (Ydmin [mV]) values of the Y signal output and substitute the values into the following formula.

   $\Delta Ydt = Ydmax - Ydmin$ [mV]

9. Flicker
   1) Fy
   Set to standard imaging condition II. Adjust the luminous intensity so that the average value of the Y signal output is 200mV, and then measure the difference in the signal level between fields (ΔYf [mV]). Then substitute the value into the following formula.

   $Fy = (\Delta Yf/200) \times 100$ [%]

   2) Fcr, Fcb
   Set to standard imaging condition II. Adjust the luminous intensity so that the average value of the Y signal output is 200mV, insert an R or B filter, and then measure both the difference in the signal level between fields of the chroma signal (ΔCr, ΔCb) as well as the average value of the chroma signal output (CAr, CAb). Substitute the values into the following formula.

   $Fci = (\Delta Ci/CAi) \times 100$ [%] (i = r, b)

10. Line crawls

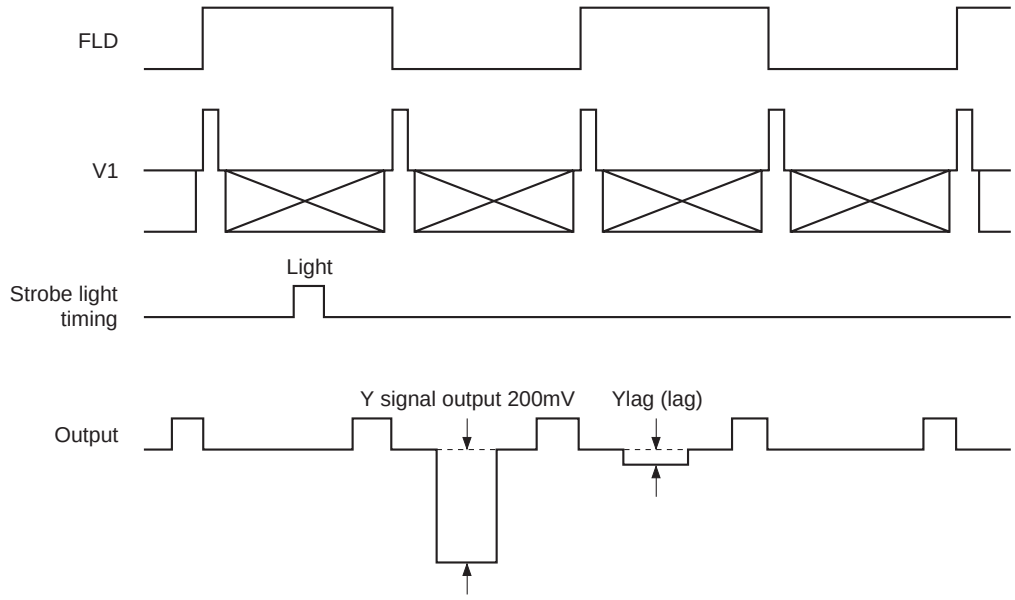
Set to standard imaging condition II. Adjust the luminous intensity so that the average value of the Y signal output is 200mV, and then insert a white subject and R, G, and B filters and measure the difference between Y signal lines for the same field (ΔYlw, ΔYlr, ΔYlg, ΔYlb [mV]). Substitute the values into the following formula.

$Lci = (\Delta Yli/200) \times 100$ [%] (i = w, r, g, b)

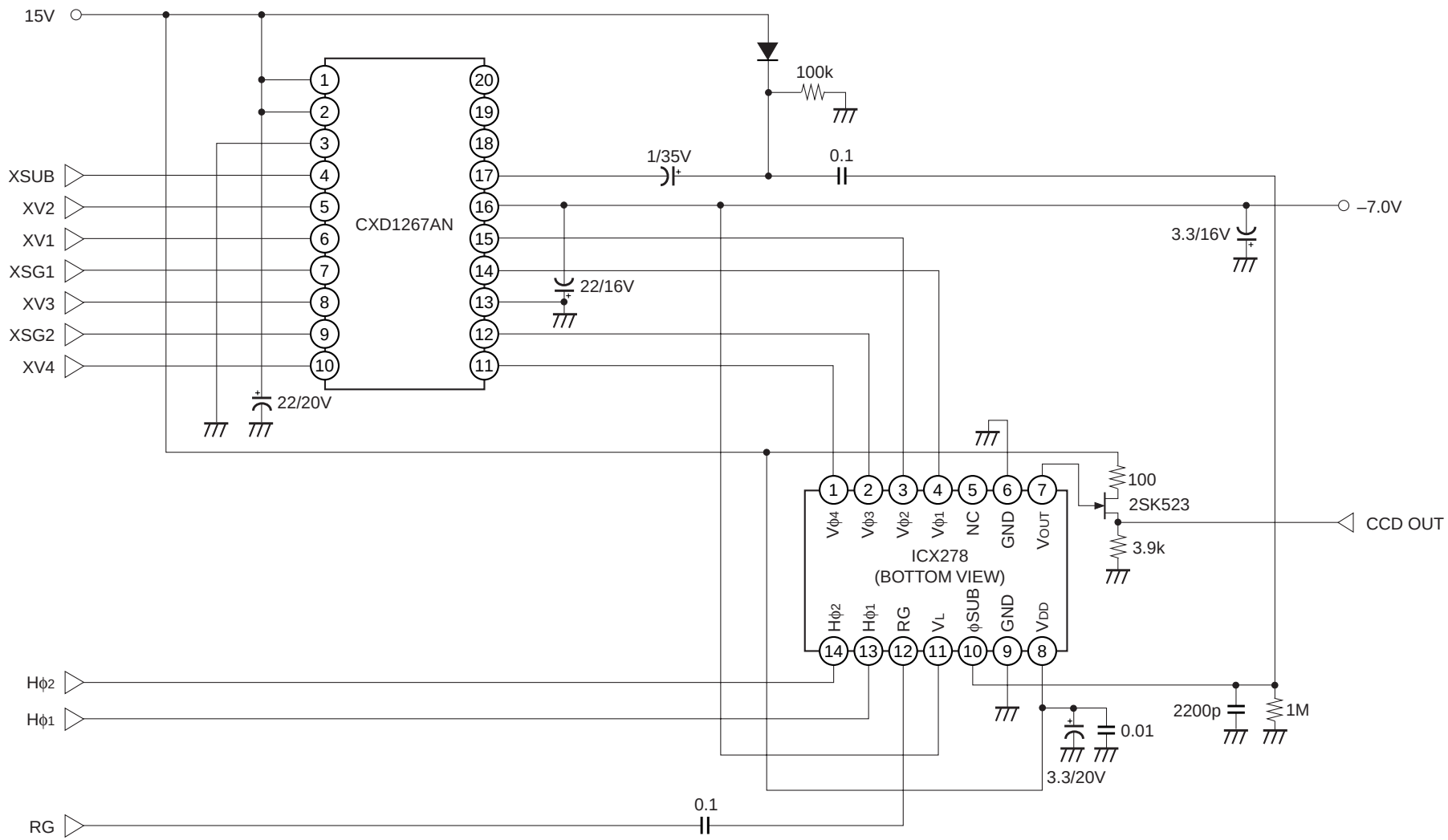
11. Lag

Adjust the Y signal output value generated by strobe light to 200mV. After setting the strobe light so that it strobes with the following timing, measure the residual signal (Ylag). Substitute the value into the following formula.

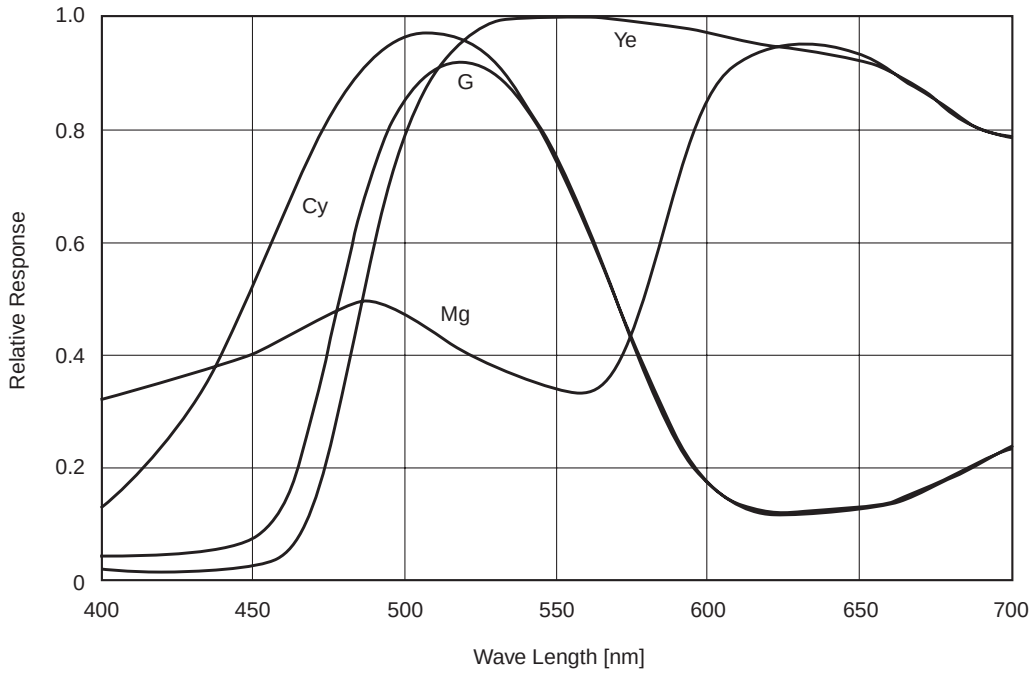
$Lag = (Ylag/200) \times 100$ [%]



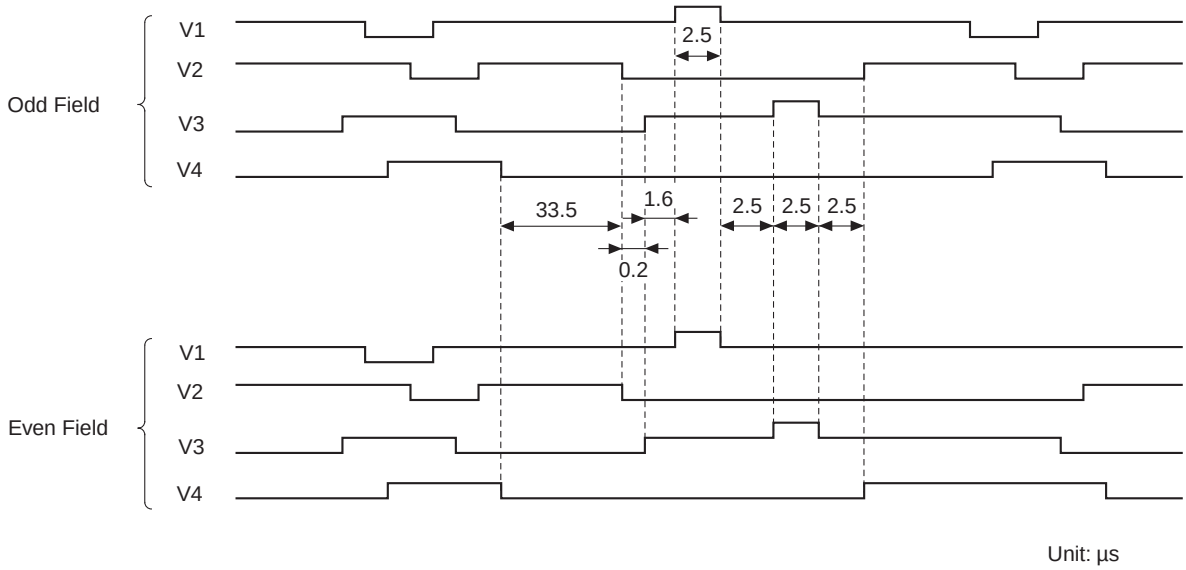
Drive Circuit



Spectral Sensitivity Characteristics (excludes both lens characteristics and light source characteristics)

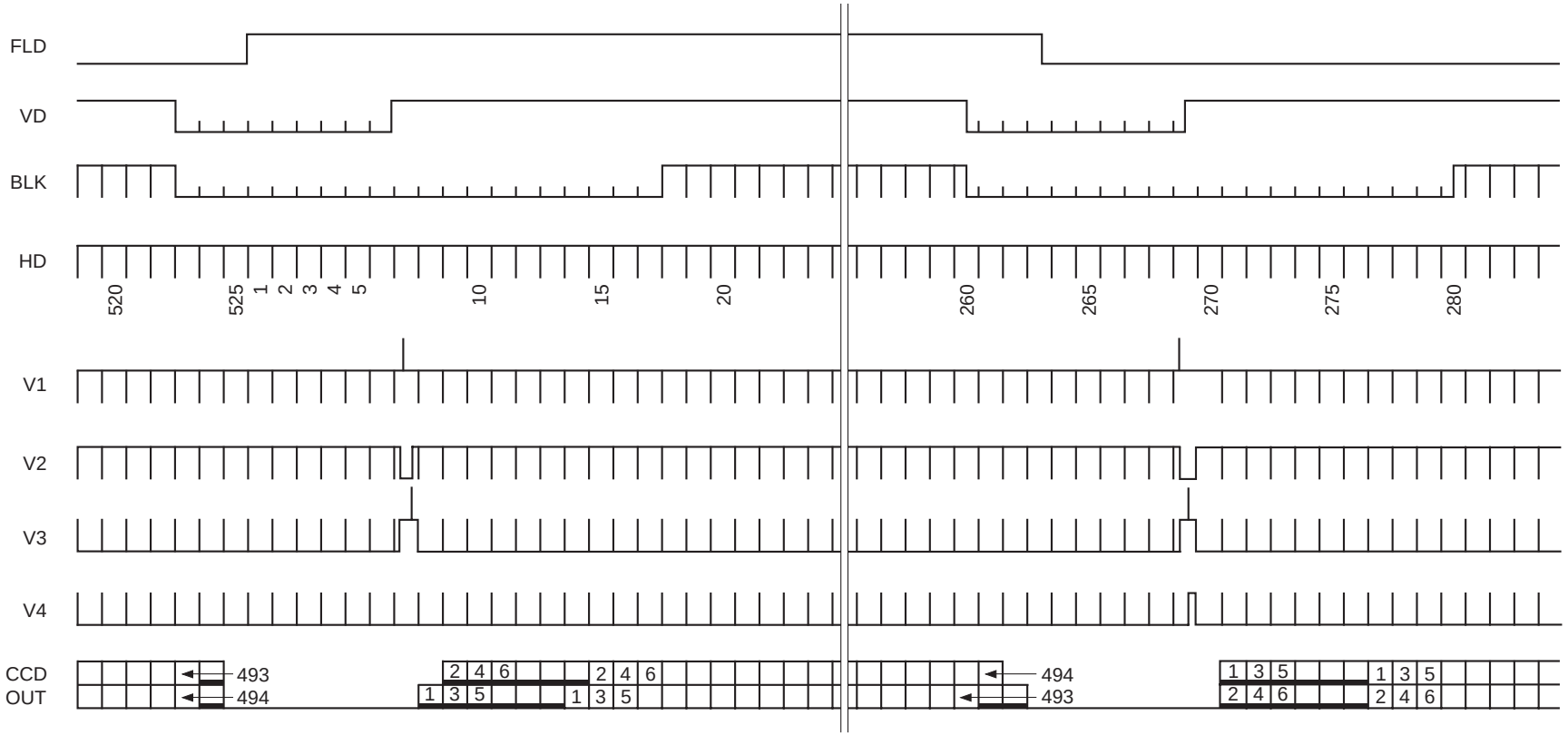


Sensor Readout Clock Timing Chart

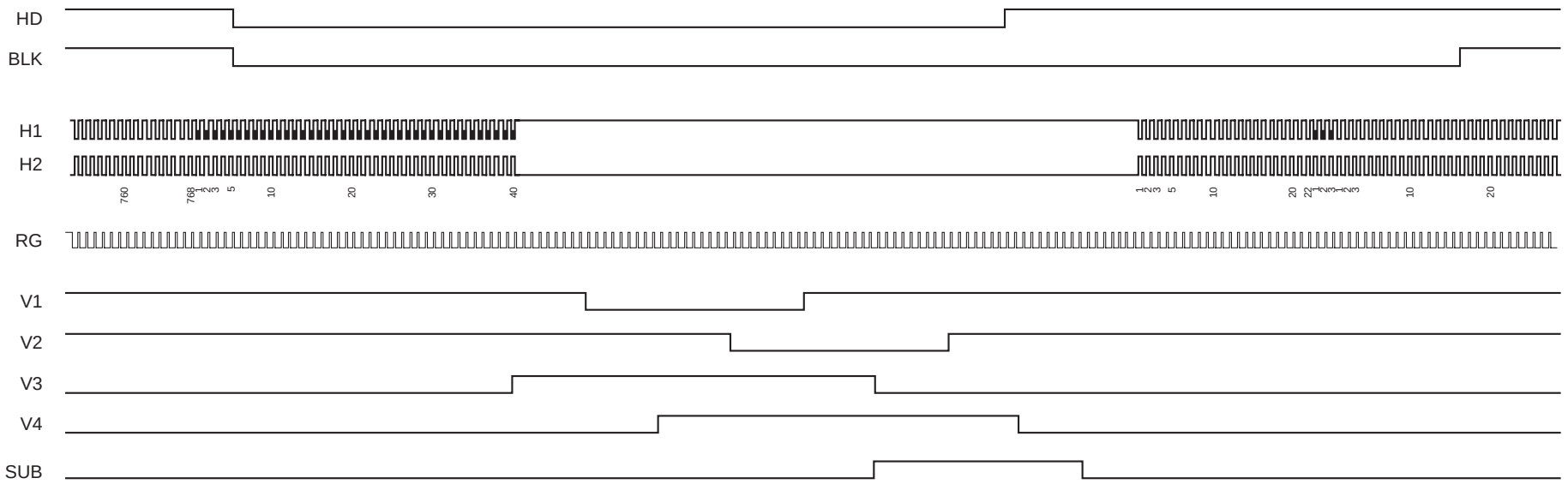


Unit: µs

Drive Timing Chart (Vertical Sync)



Drive Timing Chart (Horizontal Sync)



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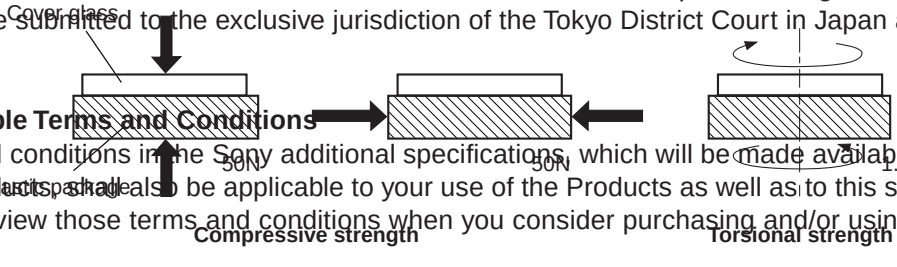
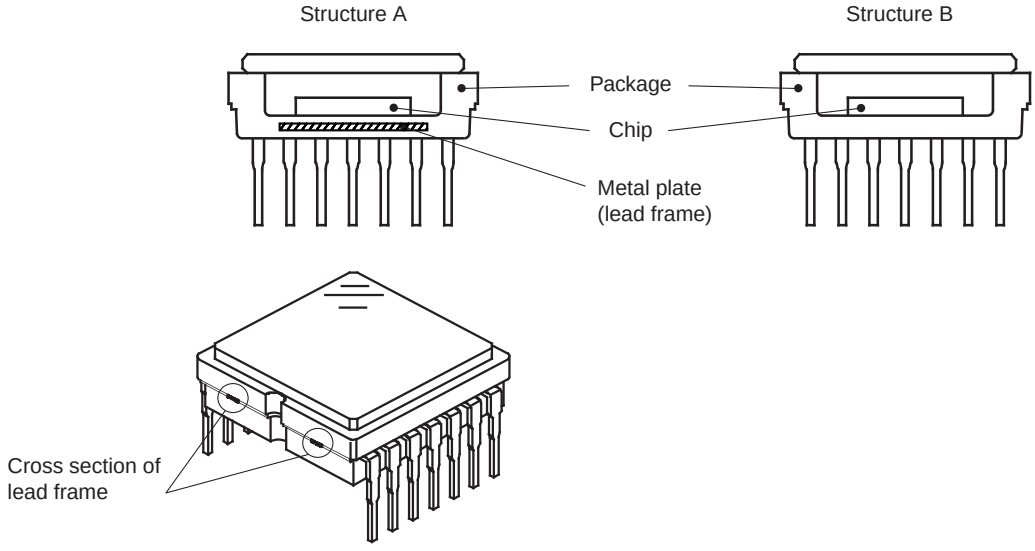


b) If a load is applied to the entire surface by a hard component, bending stress may be generated and the package may fracture, etc., depending on the flatness of the bottom of the package. Therefore, for installation, use either an elastic load, such as a spring plate, or an adhesive.
c) The adhesive may cause the marking on the rear surface to disappear, especially in case the regulated voltage value is indicated on the rear surface. Therefore, the adhesive should not be applied to this area, and indicated values should be transferred to the other locations as a precaution.
d) The notch of the package is used for directional index, and that can not be used for reference of fixing. In addition, the cover glass and seal resin may overlap with the notch of the package.
e) If the lead bend repeatedly and the metal, etc., clash or rub against the package, the dust may be generated by the fragments of resin.
f) Acrylate anaerobic adhesives are generally used to attach CCD image sensors. In addition, cyano-acrylate instantaneous adhesives are sometimes used jointly with acrylate anaerobic adhesives. (reference)

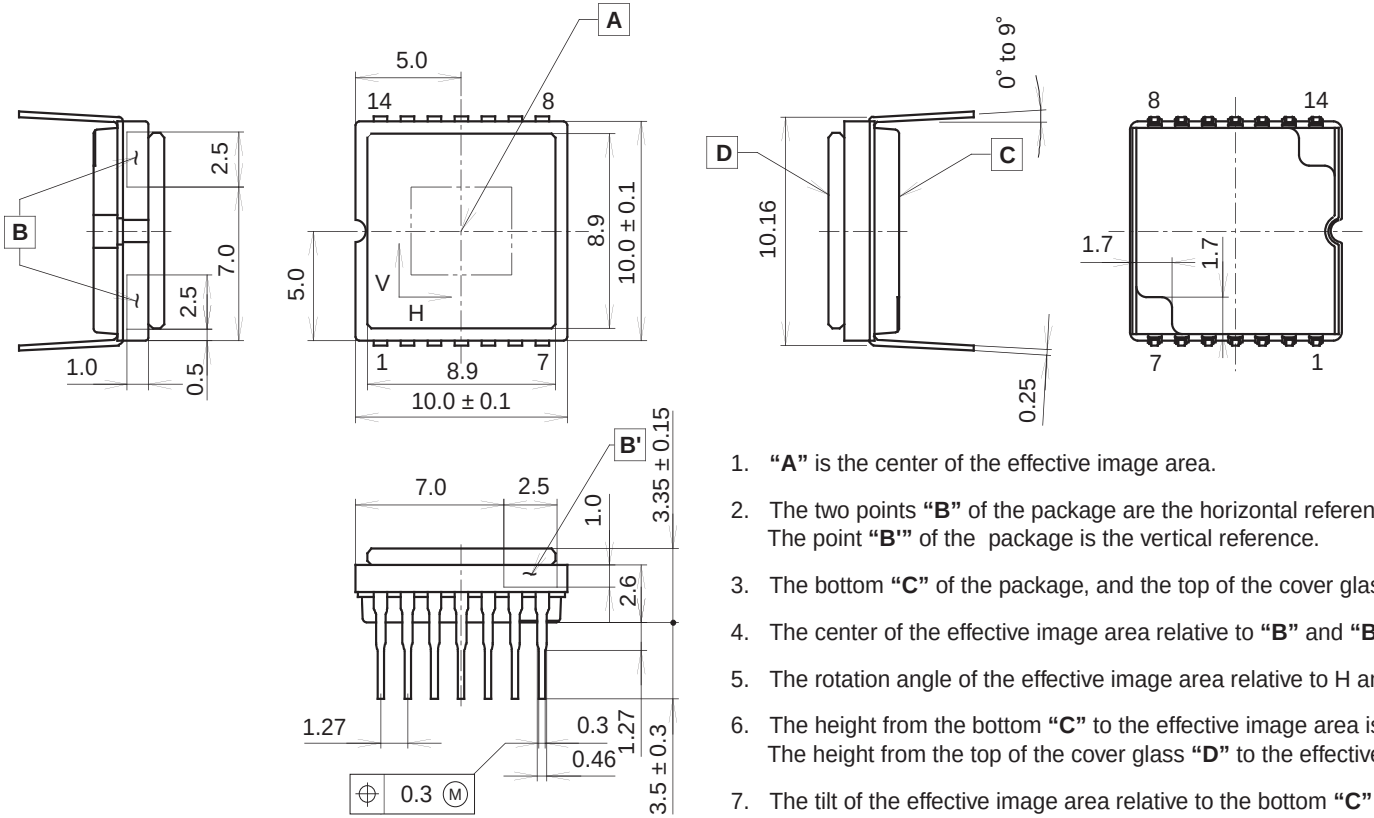
5) Others
a) Do not expose to strong light (sun rays) for long periods, color filters will be discolored. When high luminance objects are imaged with the exposure level control by electronic-iris, the luminance of the image-plane may become excessive and discolor of the color filter will possibly be accelerated. In such a case, it is advisable that taking-lens with the automatic-iris and closing of the shutter during the power-off mode should be properly arranged. For continuous using under cruel condition exceeding the normal using condition, consult our company.
b) Exposure to high temperature or humidity will affect the characteristics. Accordingly avoid storage or usage in such conditions.
c) The brown stain may be seen on the bottom or side of the package. But this does not affect the CCD characteristics.
d) This package has 2 kinds of internal structure. However, their package outline, optical size, and strength are the same.



The cross section of lead frame can be seen on the side of the package for structure A.

Package Outline Unit: mm

14 pin DIP (400mil)



PACKAGE STRUCTURE

PACKAGE MATERIAL	Plastic
LEAD TREATMENT	GOLD PLATING
LEAD MATERIAL	42 ALLOY
PACKAGE MASS	0.60g
DRAWING NUMBER	AS-D3-02(E)

1. **"A"** is the center of the effective image area.
2. The two points **"B"** of the package are the horizontal reference.
   The point **"B'"** of the package is the vertical reference.
3. The bottom **"C"** of the package, and the top of the cover glass **"D"** are the height reference.
4. The center of the effective image area relative to **"B"** and **"B'"** is (H, V) = (5.0, 5.0) ± 0.15mm.
5. The rotation angle of the effective image area relative to H and V is ± 1°.
6. The height from the bottom **"C"** to the effective image area is 1.41 ± 0.10mm.
   The height from the top of the cover glass **"D"** to the effective image area is 1.94 ± 0.15mm.
7. The tilt of the effective image area relative to the bottom **"C"** is less than 25µm.
   The tilt of the effective image area relative to the top **"D"** of the cover glass is less than 25µm.
8. The thickness of the cover glass is 0.75mm, and the refractive index is 1.5.
9. The notch of the package is used only for directional index, that must not be used for reference of fixing.
10. Cover glass defect
    Edge part
    Length : no matter, Width : less than 0.5mm, Depth : less than the thickness of the glass.
    Corner part
    Length : less than 1.5mm, Depth : less than the thickness of the glass.