



MPSA44

NPN Silicon High Voltage Transistor 625mW

Features

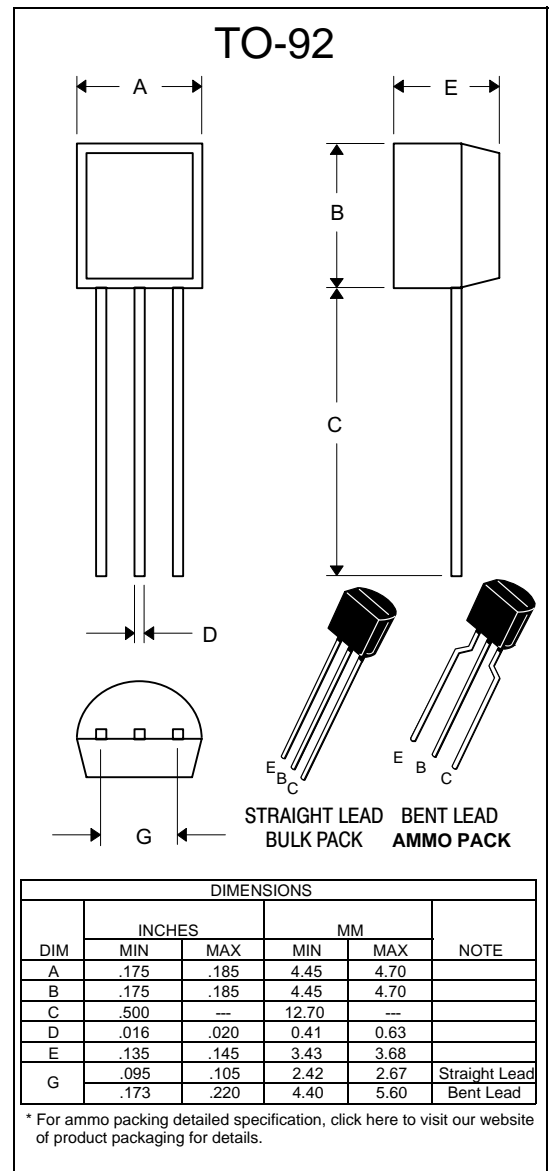
- Through Hole Package
- 150°C Junction Temperature
- Epoxy meets UL 94 V-0 flammability rating
- Moisture Sensitivity Level 1
- Lead Free Finish/RoHS Compliant ("P" Suffix designates RoHS Compliant. See ordering information)
- Halogen free available upon request by adding suffix "-HF"

Mechanical Data

- Case: TO-92, Molded Plastic
- Marking: A44

Maximum Ratings @ 25°C Unless Otherwise Specified

Charateristic	Symbol	Value	Unit
Collector-Emitter Voltage	V_{CEO}	400	V
Collector-Base Voltage	V_{CBO}	500	V
Emitter-Base Voltage	V_{EBO}	6.0	V
Collector Current(DC)	I_C	300	mA
Power Dissipation@ $T_A=25^\circ\text{C}$	P_d	625	mW
Thermal Resistance, Junction to Ambient Air	$R_{\theta JA}$	200	$^\circ\text{C}/\text{W}$
Operating & Storage Temperature	T_j, T_{STG}	-55~150	$^\circ\text{C}$



ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector–Emitter Breakdown Voltage ⁽¹⁾ ($I_C = 1.0\text{ mAdc}$, $I_B = 0$)	$V_{(BR)CEO}$	400	—	Vdc
Collector–Base Breakdown Voltage ($I_C = 100\ \mu\text{Adc}$, $I_E = 0$)	$V_{(BR)CBO}$	500	—	Vdc
Emitter–Base Breakdown Voltage ($I_E = 100\ \mu\text{Adc}$, $I_C = 0$)	$V_{(BR)EBO}$	6.0	—	Vdc
Collector Cutoff Current ($V_{CB} = 400\text{ Vdc}$, $I_E = 0$)	I_{CBO}	—	0.1	μAdc
Emitter Cutoff Current ($V_{EB} = 4.0\text{ Vdc}$, $I_C = 0$)	I_{EBO}	—	0.1	μAdc

ON CHARACTERISTICS⁽¹⁾

DC Current Gain ⁽¹⁾ ($I_C = 1.0\text{ mAdc}$, $V_{CE} = 10\text{ Vdc}$) ($I_C = 10\text{ mAdc}$, $V_{CE} = 10\text{ Vdc}$) ($I_C = 50\text{ mAdc}$, $V_{CE} = 10\text{ Vdc}$) ($I_C = 100\text{ mAdc}$, $V_{CE} = 10\text{ Vdc}$)	h_{FE}	40 50 45 40	200	
Collector–Emitter Saturation Voltage ⁽¹⁾ ($I_C = 10\text{ mAdc}$, $I_B = 1.0\text{ mAdc}$) ($I_C = 50\text{ mAdc}$, $I_B = 5.0\text{ mAdc}$)	$V_{CE(sat)}$	— —	0.5 0.75	Vdc
Base–Emitter Saturation Voltage ($I_C = 10\text{ mAdc}$, $I_B = 1.0\text{ mAdc}$)	$V_{BE(sat)}$	—	0.75	Vdc

SMALL–SIGNAL CHARACTERISTICS

Output Capacitance ($V_{CB} = 20\text{ Vdc}$, $I_E = 0$, $f = 1.0\text{ MHz}$)	C_{obo}	—	7.0	pF
Input Capacitance ($V_{EB} = 0.5\text{ Vdc}$, $I_C = 0$, $f = 1.0\text{ MHz}$)	C_{ibo}	—	130	pF

1. Pulse Test: Pulse Width $\leq 300\ \mu\text{s}$, Duty Cycle $\leq 2.0\%$.

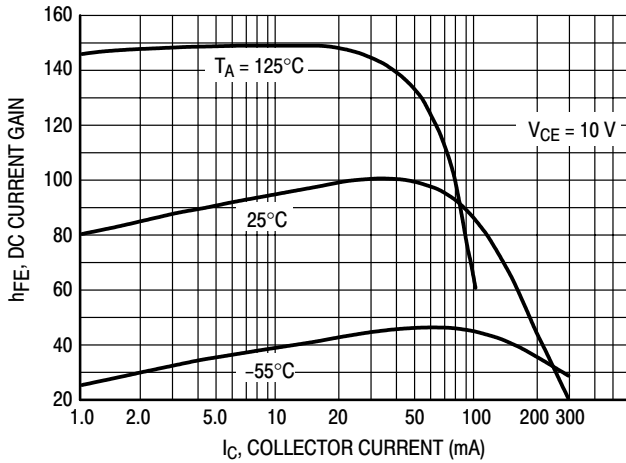


Figure 1. DC Current Gain

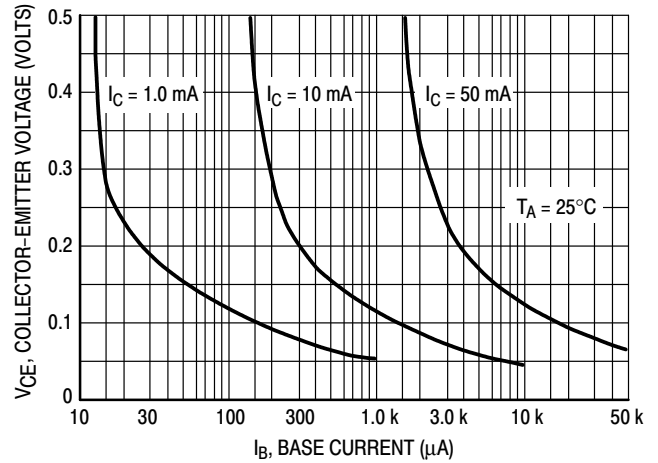


Figure 2. Collector Saturation Region

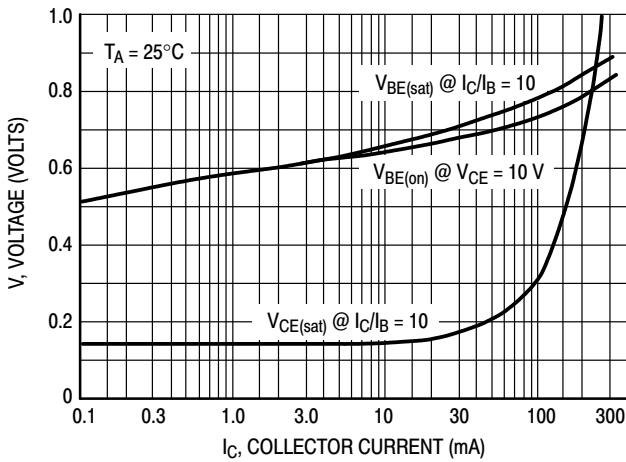


Figure 3. "On" Voltages

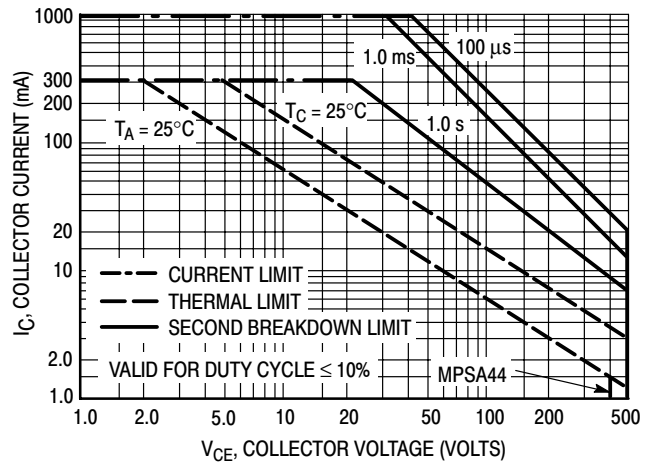


Figure 4. Active Region — Safe Operating Area

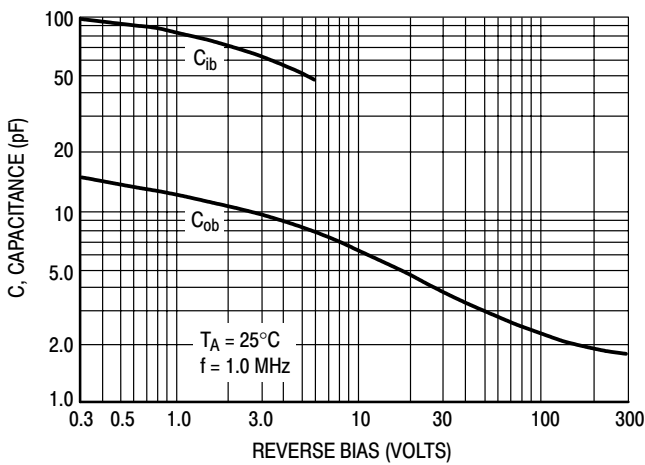


Figure 5. Capacitance



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Ordering Information :

Device	Packing
Part Number-AP	Am mo Packing: 20Kpcs/Carton
Part Number-BP	Bulk: 100Kpcs/Carton

Note : Adding "-HF" suffix for halogen free, eg. Part Number-AP-HF

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