

SANYO Semiconductors

DATA SHEET

An ON Semiconductor Company

2SJ652 — General-Purpose Switching Device Applications

Features

- ON-resistance RDS(on)1=28.5m Ω (typ.)
- Input capacitance Ciss=4360pF (typ.)
- · 4V drive

Specifications

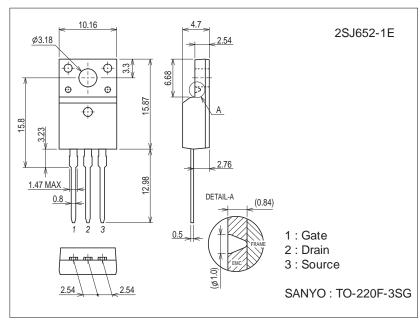
Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		-60	V
Gate-to-Source Voltage	VGSS		±20	V
Drain Current (DC)	ID		-28	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	-112	Α
Allowable Power Dissipation	Do		2.0	W
	PD	Tc=25℃	30	W
Channel Temperature	Tch		150	C
Storage Temperature	Tstg		-55 to +150	C
Avalanche Energy (Single Pulse) *1	EAS		343	mJ
Avalanche Current *2	IAV		-28	А

Note: *1 V_{DD} =-30V, L=500 μ H, IAV=-28A (Fig.1)

Package Dimensions

unit : mm (typ) 7529-001



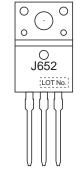
Product & Package Information

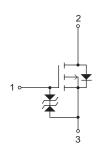
• Package : TO-220F-3SG

• JEITA, JEDEC : SC-67

• Minimum Packing Quantity : 50 pcs./magazine

Marking Electrical Connection





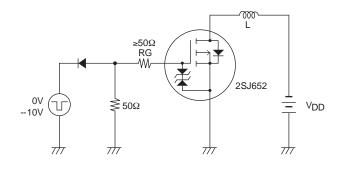
^{*2} L≤500µH, single pulse

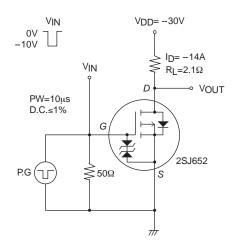
Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
Farameter	Syllibol	Conditions	min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=-1mA, VGS=0V	-60			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =-60V, V _{GS} =0V			-1	μΑ
Gate-to-Source Leakage Current	IGSS	V _{GS} =±16V, V _{DS} =0V			±10	μΑ
Cutoff Voltage	VGS(off)	V _{DS} =-10V, I _D =-1mA	-1.2		-2.6	V
Forward Transfer Admittance	yfs	V _{DS} =-10V, I _D =-14A	18	26		S
Static Drain-to-Source On-State Resistance	R _{DS} (on)1	I _D =-14A, V _G S=-10V		28.5	38	mΩ
	R _{DS} (on)2	I _D =-14A, V _G S=-4V		39	55.5	mΩ
Input Capacitance	Ciss			4360		pF
Output Capacitance	Coss	V _{DS} =-20V, f=1MHz		470		pF
Reverse Transfer Capacitance	Crss			335		pF
Turn-ON Delay Time	t _d (on)			33		ns
Rise Time	t _r	San Fig 2		210		ns
Turn-OFF Delay Time	t _d (off)	See Fig.2		310		ns
Fall Time	tf			180		ns
Total Gate Charge	Qg			80		nC
Gate-to-Source Charge	Qgs	V _{DS} =-30V, V _{GS} =-10V, I _D =-28A		15		nC
Gate-to-Drain "Miller" Charge	Qgd			12		nC
Diode Forward Voltage	VSD	IS=-28A, VGS=0V		-0.96	-1.2	V

Fig.1 Avalanche Resistance Test Circuit

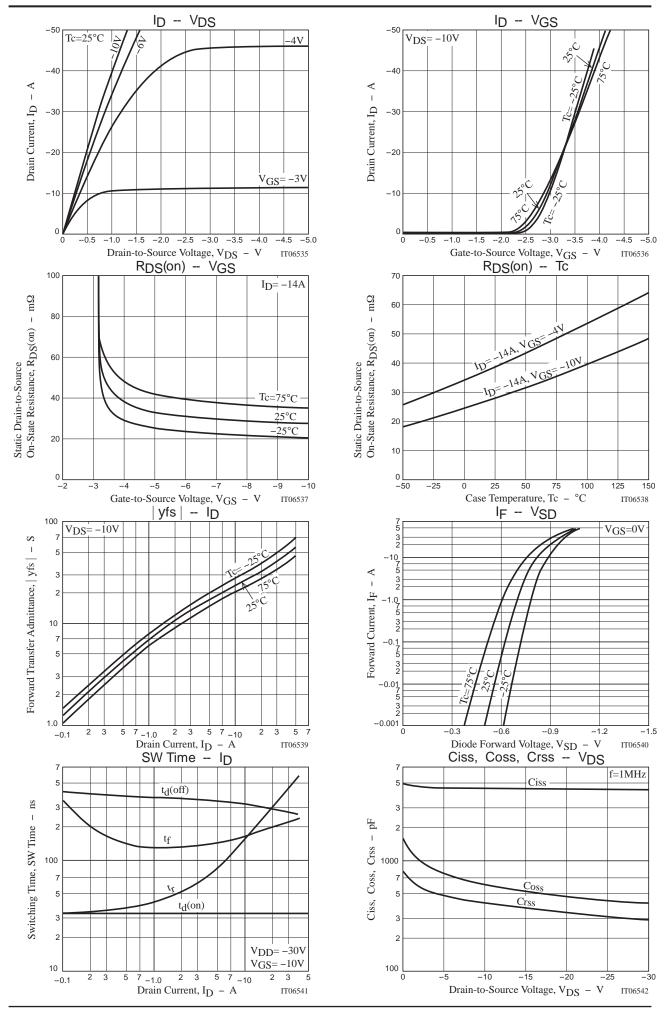
Fig.2 Switching Time Test Circuit

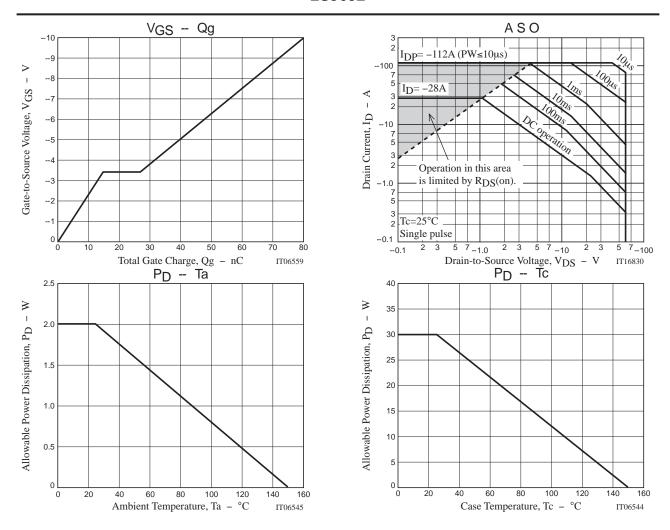




Ordering Information

Device	Package	Shipping	memo
2SJ652-1E	TO-220F-3SG	50pcs./magazine	Pb Free



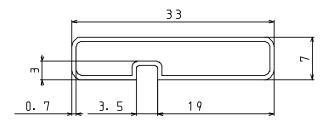


Magazine Specification

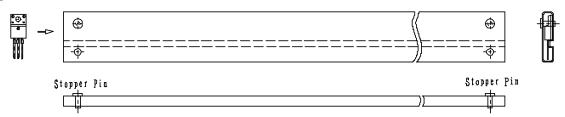
2SJ652-1E

1. Packing Format

Package Name	Magazine Name	Maximum Number of devices contained (pc			Packing format		
1111100 1111111	Ida 9 a b i a c i i a a a	l	Inner box	Outer box	Inner BOX	Outer BOX	
TO-220F-3SG	TO-220F	50	1, 000	4,000	SPD-0V0001 20 magazines contained Dimensions:mm (external) 568×150×55	SPT-081029 4 inner boxes contained Dinensions:mm (external) 590×225×178	



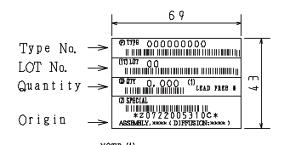
3. Storage method to magazine

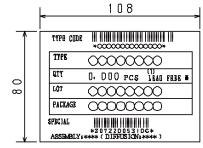


4. Inner box label (unit:mm)

5. Outer box label (unit:mm)

It is a label at the time of fattory shigments. The form of a label may change in physical distribution process.





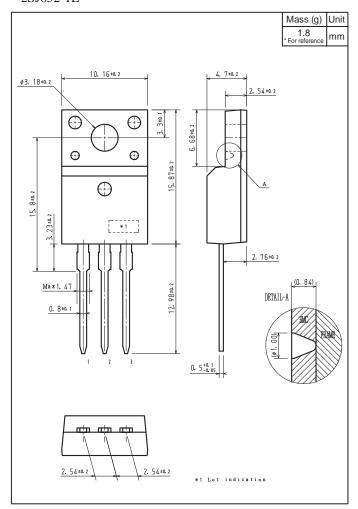
NOTE(1)

The LEAD FREE * description shows that the surface treatment of the terminal is lead free.

Label		JEITA Phase			
LEAD FREE	3	JEITA Phase 3A			

Outline Drawing

2SJ652-1E



Note on usage: Since the 2SJ652 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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