

SANYO Semiconductors DATA SHEET

An ON Semiconductor Company

2SK4124 — General-Purpose Switching Device Applications

Features

- · Low ON-resistance, low input capacitance, ultrahigh-speed switching
- · Adoption of high reliability HVP process
- · Avalanche resistance guarantee

Specifications

Absolute Maximum Ratings at Ta=25°C

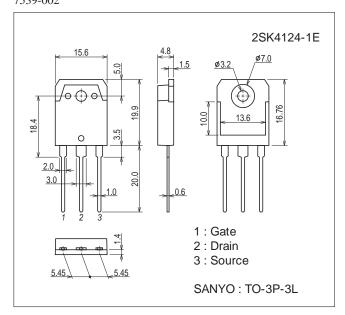
Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		500	V
Gate-to-Source Voltage	VGSS		±30	V
Drain Current (DC)	ID		20	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	60	Α
Allowable Power Dissipation	D-		2.5	W
Allowable Power Dissipation	PD	Tc=25°C (SANYO's ideal heat dissipation condition)*1	170	W
Channel Temperature	Tch		150	C
Storage Temperature	Tstg		-55 to +150	C
Avalanche Energy (Single Pulse) *1	EAS		110	mJ
Avalanche Current *2	I _{AV}		20	Α

^{*1} SANYO's condition is radiation from backside.

The method is applying silicone grease to the backside of the device and attaching the device to water-cooled radiator made of aluminium.

Package Dimensions

unit : mm (typ) 7539-002



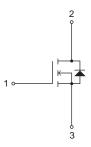
Product & Package Information

• Package: TO-3P-3L

JEITA, JEDEC: SC-65, TO-247, SOT-199
Minimum Packing Quantity: 30 pcs./magazine

Marking Electrical Connection





^{*2} VDD=50V, L=500 μ H, IAV=20A (Fig.1)

^{*3} L≤500µH, single pulse

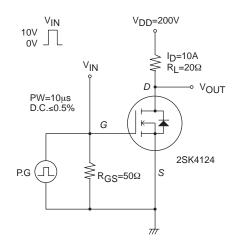
Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
Farameter	Symbol	Conditions	min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=10mA, VGS=0V	500			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =400V, V _{GS} =0V			100	μΑ
Gate-to-Source Leakage Current	IGSS	V _{GS} =±30V, V _{DS} =0V			±100	nA
Cutoff Voltage	VGS(off)	V _{DS} =10V, I _D =1mA	3		5	V
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =10A	4.9	9.7		S
Static Drain-to-Source On-State Resistance	R _{DS} (on)	I _D =8A, V _G S=10V		0.33	0.43	Ω
Input Capacitance	Ciss			1200		pF
Output Capacitance	Coss	V _{DS} =30V, f=1MHz		250		pF
Reverse Transfer Capacitance	Crss			55		pF
Turn-ON Delay Time	t _d (on)			26.5		ns
Rise Time	t _r	0 Fin 0		95		ns
Turn-OFF Delay Time	t _d (off)	See Fig.2		145		ns
Fall Time	tf			58		ns
Total Gate Charge	Qg			46.6		nC
Gate-to-Source Charge Qgs		V _{DS} =200V, V _{GS} =10V, I _D =20A		8.7		nC
Gate-to-Drain "Miller" Charge	Qgd			27.3		nC
Diode Forward Voltage	V _{SD}	I _S =20A, V _{GS} =0V		1.0	1.3	V

Fig.1 Avalanche Resistance Test Circuit

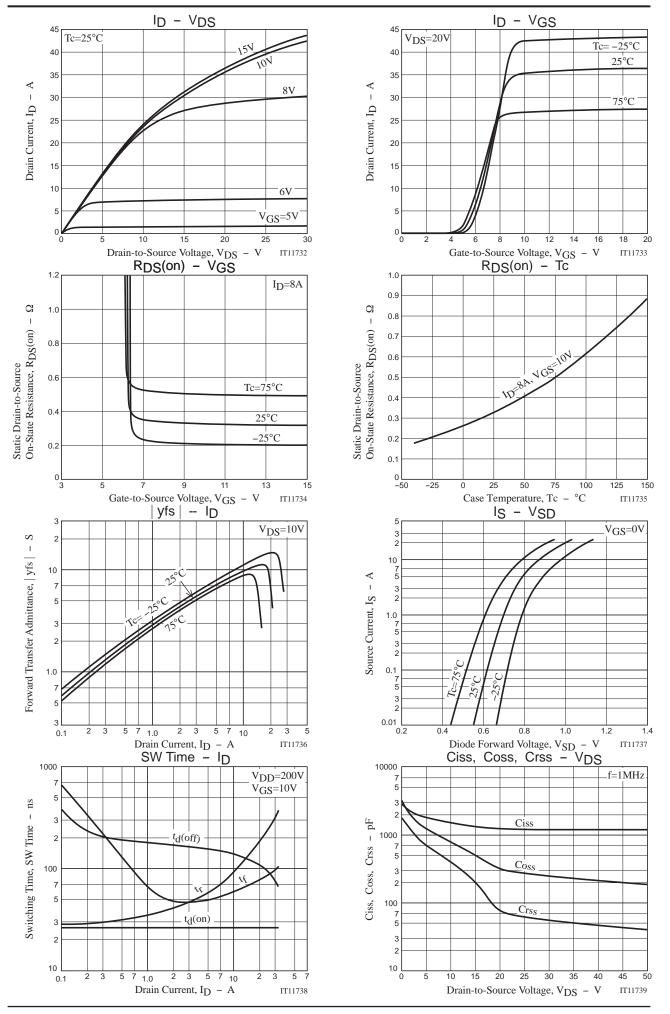
250Ω RG W 10V 0V 2SK4124 = V_{DD}

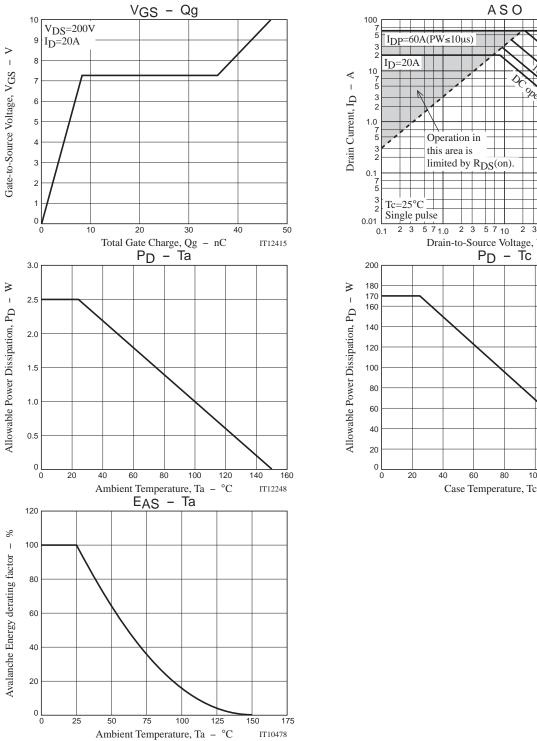
Fig.2 Switching Time Test Circuit



Ordering Information

Device	Package	Shipping	memo
2SK4124-1E	TO-3P-3L	30pcs./magazine	Pb Free



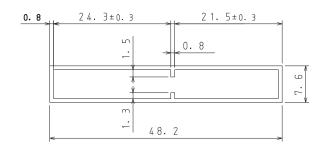


Magazine Specification

2SK4124-1E

1. Packing Format

Package Name		Maximum Number of devices contained (pcs)			Packing format		
		Magazine	Inner box	Outer box	Inner BOX	Outer BOX	
TO-3P-3	L	30	450	1800	012 0.0001	SPD-LV0010 4 inner boxes contained Dimensions:mm (external) 590x225x178	



Tolerance=±0.2mm
Thickness=0.8±0.2mm
Length =508.0±1mm
Material =PVC or PET
(Antistatic treatment)

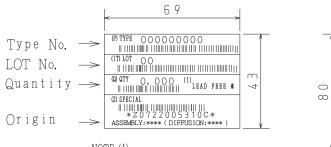
3. Storage method to magazine

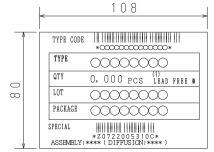


4. Inner box label (unit:mm)



It is a label at the time of factory shipments. The form of a label may change in physical distribution process.



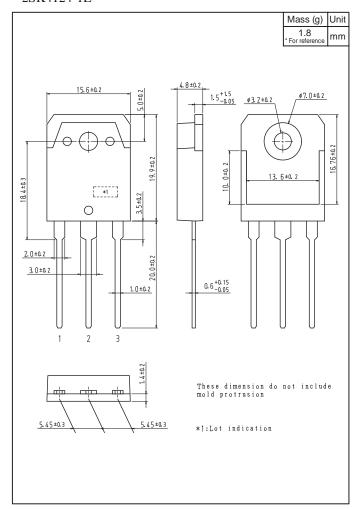


The LEAD FREE * description shows that the surface treatment of the terminal is lead free.

Label		JEITA Phase			
LEAD FREE	3	JEITA Phase 3A			

Outline Drawing

2SK4124-1E



Note on usage: Since the 2SK4124 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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