

SANYO Semiconductors DATA SHEET

N-Channel Silicon MOSFET **2SK4197FS** — General-Purpose Switching Device **Applications**

Features

- · High-speed switching.
- · Avalanche resistance guarantee.
- 10V drive.

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		600	V
Gate-to-Source Voltage	VGSS		±30	V
Drain Current (DC)	I _{Dc} *1	Limited only by maximum temperature Tch=150°C	3.5	А
	I _{Dpack} *2	Tc=25°C (SANYO's ideal heat dissipation condition)*3	3.3	А
Drain Current (Pulse)	IDP	PW≤10µs, duty cycle≤1%	13	А
Allowable Power Dissipation	D -		2.0	W
	PD	Tc=25°C (SANYO's ideal heat dissipation condition)*3	28	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C
Avalanche Energy (Single Pulse) *4	EAS		29	mJ
Avalanche Current *5	IAV		3.3	А

Note :*1 Shows chip capability.

*2 Package limited.

*3 SANYO's condition is radiation from backside.

The method is applying silicone grease to the backside of the device and attaching the device to water-cooled radiator made of aluminium. *4 VDD=50V, L=5mH, IAV=3.3A

*5 L≤5mH, Single pulse

Marking : K4197FS

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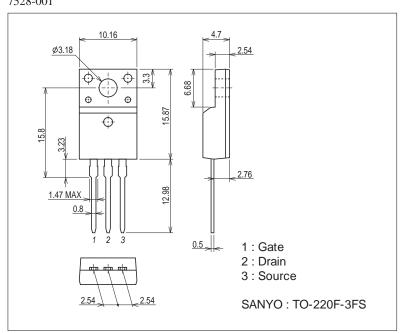
> SANYO Semiconductor Co., Ltd. www.semiconductor-sanyo.com/network

Electrical Characteristics at Ta= $25^{\circ}C$

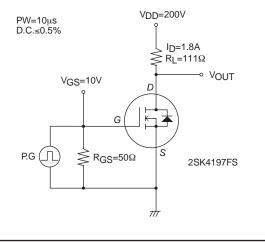
Parameter	Symbol	Conditions	Ratings			1.1
			min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=10mA, VGS=0V	600			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =480V, V _{GS} =0V			100	μΑ
Gate-to-Source Leakage Current	IGSS	V _{GS} =±30V, V _{DS} =0V			±100	nA
Cutoff Voltage	V _{GS} (off)	V _{DS} =10V, I _D =1mA	3		5	V
Forward Transfer Admittance	yfs	VDS=10V, ID=1.8A	0.8	1.6		S
Static Drain-to-Source On-State Resistance	R _{DS} (on)	ID=1.8A, VGS=10V		2.5	3.25	Ω
Input Capacitance	Ciss	V _{DS} =30V, f=1MHz		260		pF
Output Capacitance	Coss	V _{DS} =30V, f=1MHz		50		pF
Reverse Transfer Capacitance	Crss	VDS=30V, f=1MHz		9.7		pF
Turn-ON Delay Time	t _d (on)	See specified Test Circuit.		12		ns
Rise Time	tr	See specified Test Circuit.		20		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit.		28		ns
Fall Time	tf	See specified Test Circuit.		12		ns
Total Gate Charge	Qg	VDS=200V, VGS=10V, ID=3.5A		11		nC
Gate-to-Source Charge	Qgs	V _{DS} =200V, V _{GS} =10V, I _D =3.5A		2.6		nC
Gate-to-Drain "Miller" Charge	Qgd	V _{DS} =200V, V _{GS} =10V, I _D =3.5A		5.8		nC
Diode Forward Voltage	V _{SD}	IS=3.5A, VGS=0V		0.9	1.2	V

Package Dimensions

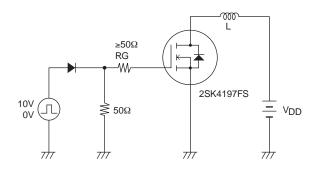
unit : mm (typ) 7528-001

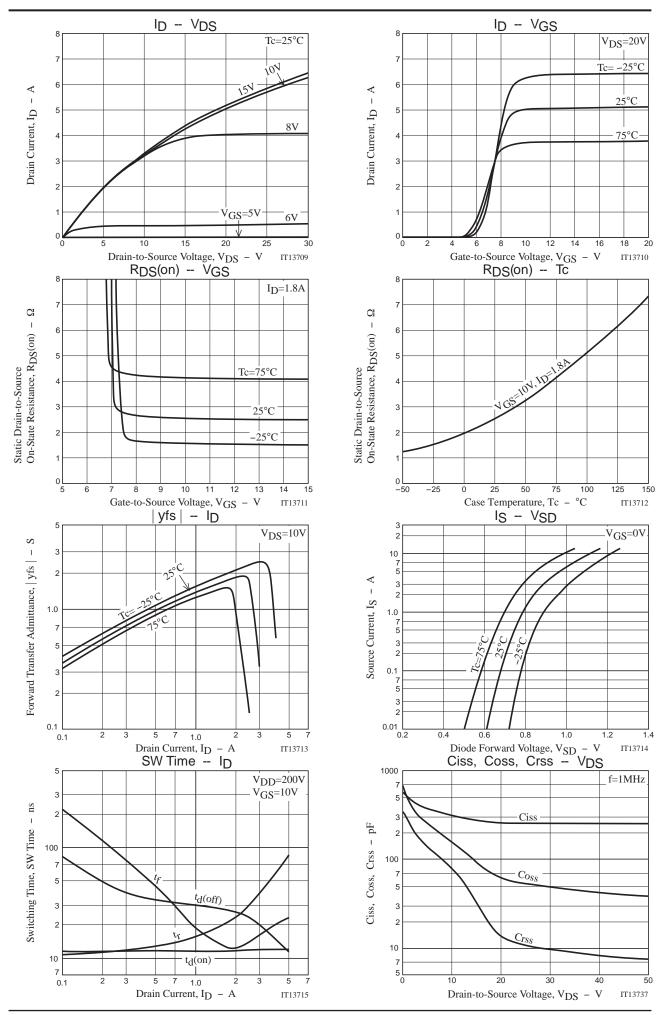


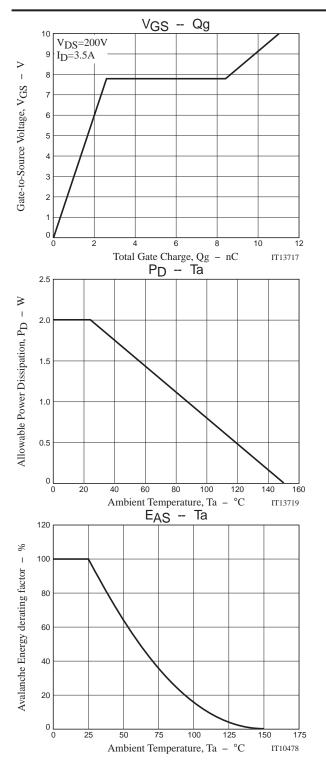
Switching Time Test Circuit

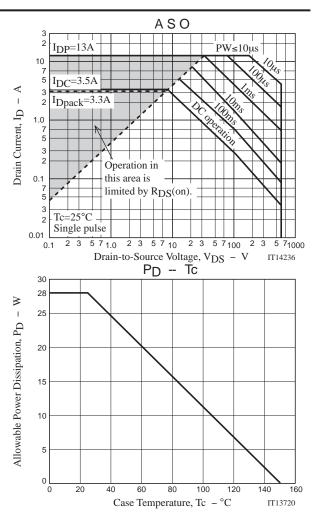


Avalanche Resistance Test Circuit









Note on usage : Since the 2SK4197FS is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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