

SANYO Semiconductors DATA SHEET

An ON Semiconductor Company

ATP201 — N-Channel Silicon MOSFET General-Purp

General-Purpose Switching Device Applications

Features

- · Low ON-resistance
- · Slim package
- · Protection diode in

- 4.5V drive
- · Halogen free compliance

Specifications

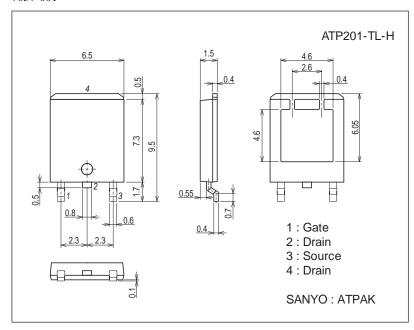
Absolute Maximum Ratings at Ta=25°C

| Parameter | Symbol | Conditions | Ratings | Unit |
|------------------------------------|-----------------|------------------------|-------------|------|
| Drain-to-Source Voltage | VDSS | | 30 | V |
| Gate-to-Source Voltage | VGSS | | ±20 | V |
| Drain Current (DC) | ID | | 35 | Α |
| Drain Current (PW≤10μs) | I _{DP} | PW≤10μs, duty cycle≤1% | 105 | Α |
| Allowable Power Dissipation | PD | Tc=25°C | 30 | W |
| Channel Temperature | Tch | | 150 | °C |
| Storage Temperature | Tstg | | -55 to +150 | °C |
| Avalanche Energy (Single Pulse) *1 | EAS | | 10 | mJ |
| Avalanche Current *2 | I _{AV} | | 18 | Α |

Note :*1 VDD=10V, L=50 μ H, IAV=18A

Package Dimensions

unit : mm (typ) 7057-001



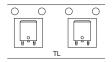
Product & Package Information

• Package : ATPAK

• JEITA, JEDEC :-

• Minimum Packing Quantity : 3,000 pcs./reel

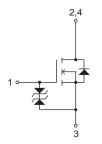
Packing Type: TL



Marking



Electrical Connection



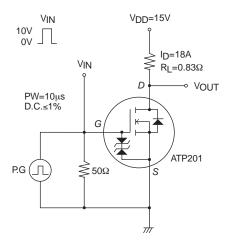
^{*2} L≤50µH, Single pulse

ATP201

Electrical Characteristics at Ta=25°C

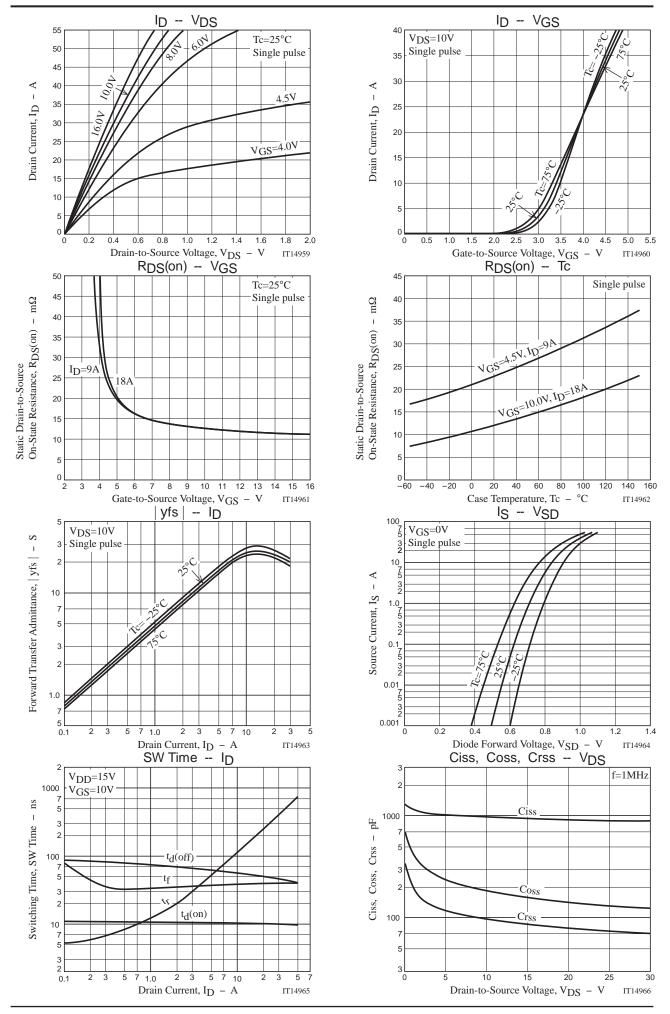
| Parameter | Symbol | Conditions | Ratings | | | Unit | |
|--|-----------------------|---|---------|------|-----|------|--|
| Parameter | Symbol | Conditions | min | typ | max | Unit | |
| Drain-to-Source Breakdown Voltage | V(BR)DSS | ID=1mA, VGS=0V | 30 | | | V | |
| Zero-Gate Voltage Drain Current | IDSS | V _{DS} =30V, V _{GS} =0V | | | 1 | μΑ | |
| Gate-to-Source Leakage Current | IGSS | V _{GS} =±16V, V _{DS} =0V | | | ±10 | μΑ | |
| Cutoff Voltage | VGS(off) | V _{DS} =10V, I _D =1mA | 1.2 | | 2.6 | V | |
| Forward Transfer Admittance | yfs | V _{DS} =10V, I _D =18A | | 24 | | S | |
| Static Drain-to-Source On-State Resistance | R _{DS} (on)1 | I _D =18A, V _G S=10V | | 13 | 17 | mΩ | |
| | R _{DS} (on)2 | I _D =9A, V _G S=4.5V | | 23 | 33 | mΩ | |
| Input Capacitance | Ciss | | | 985 | | pF | |
| Output Capacitance | Coss | V _{DS} =10V, f=1MHz | | 180 | | pF | |
| Reverse Transfer Capacitance | Crss | | | 100 | | pF | |
| Turn-ON Delay Time | t _d (on) | | | 10 | | ns | |
| Rise Time | t _r | See an edified Test Circuit | | 230 | | ns | |
| Turn-OFF Delay Time | t _d (off) | See specified Test Circuit. | | 51 | | ns | |
| Fall Time | tf | | | 39 | | ns | |
| Total Gate Charge | Qg | | | 17 | | nC | |
| Gate-to-Source Charge | Qgs | V _{DS} =15V, V _{GS} =10V, I _D =35A | | 4.7 | | nC | |
| Gate-to-Drain "Miller" Charge | Qgd | | | 2.8 | | nC | |
| Diode Forward Voltage | VSD | IS=35A, VGS=0V | | 0.97 | 1.2 | V | |

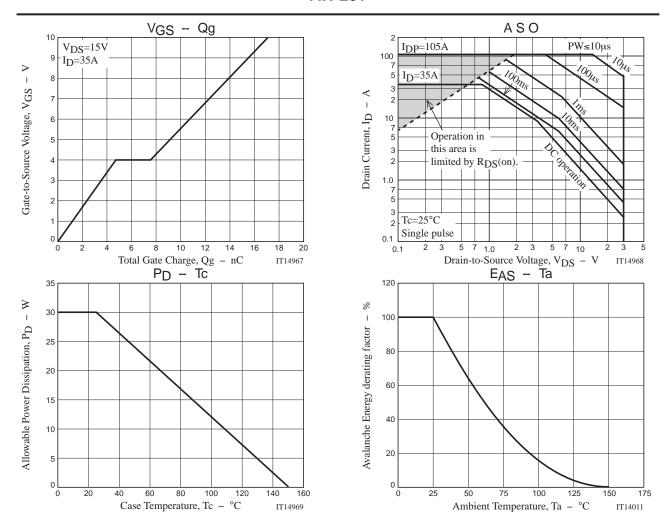
Switching Time Test Circuit



Ordering Information

| Device | Package | Shipping | memo | |
|-------------|---------|----------------|--------------------------|--|
| ATP201-TL-H | ATPAK | 3,000pcs./reel | Pb Free and Halogen Free | |



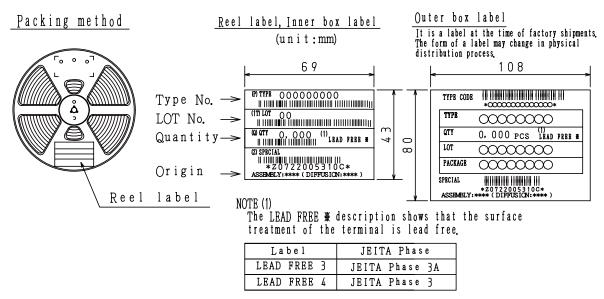


Taping Specification

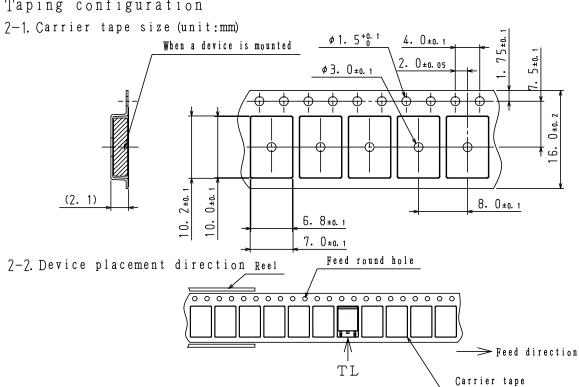
ATP201-TL-H

1. Packing Format (TL)

| Package Name Carrier Tape Type | | Maximum Number of devices contained (pcs) | | | Packing format | | |
|--------------------------------|-----|---|-----------|-----------|--------------------------|--------------------------|--|
| | | Reel | Inner box | Outer box | INNER BOX SD-C-18 | OUTER BOX SD-A-18 | |
| | | | | | 1 reels contained | 5 inner boxes contained | |
| ATPAK | ATP | 3, 000 3, | 000 3,000 | 15,000 | Dimensions:mm (external) | Dimensions:mm (external) | |
| | | | | | 340×340×28 | 355×355×165 | |



7. Taping configuration



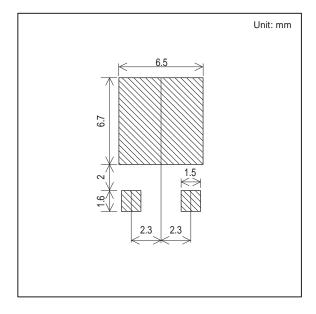
The one erectrode terminals on feed hole side····TL

Outline Drawing

ATP201-TL-H

Mass (g) Unit 0.266 mm 6 5 at 15 2 5 at 15 3 5 at 15 4 at 15 4

Land Pattern Example



Note on usage: Since the ATP201 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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