# Low-Saturation 300mA Regulators

# Monolithic IC MM168 ☐ ☐ Series

### **Outline**

This IC is low saturation regulator IC with 300mA output realizing low current consumption, low noise, and high ripple rejection. The output capacitor is a ceramic capacitor, and the IC has the pin to reduce noise and control ON/OFF.

#### **Features**

1. Low current consumption 85µA typ. 2. High accuracy output voltage ±1.5%

3. Dropout voltage 0.12V typ. (lo=150mA)

4. High ripple rejection 70dB typ. 5. Operating temperature range -30 to +85°C

6. Output voltage 1.5 to 5.2V (0.1V steps)

7. Output capacitor 1µF (Ceramic)

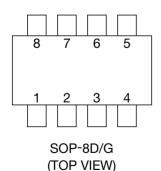
### **Packages**

- 1. SOP-8D/G
- 2. SOT-25A

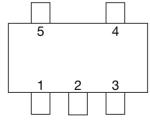
# **Applications**

- 1. Constant voltage power supplies for battery-powered device
- 2. Constant voltage sources for portable communication equipment
- 3. Constant voltage sources for home appliances

# Pin Assignment



1	Vout
2	NC
3	Cn
4	GND
5	CONT
6	NC
7	NC
8	$V_{ m IN}$
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SOT-25A (TOP VIEW)

1	Vin
2	GND
3	CONT
4	Noise
5	Vout

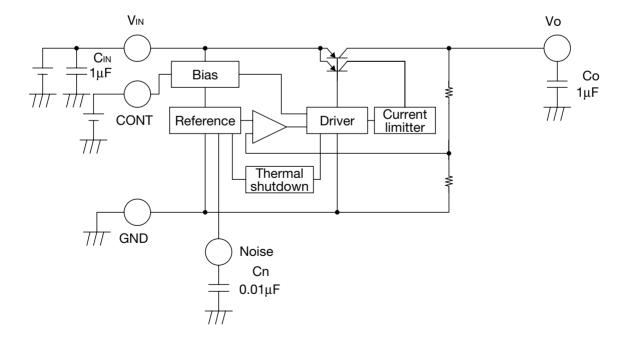
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# R No. table

### ■ Taping: R housing

Parts No.	Vo (V)	R No. (R59)	Parts No.	Vo (V)	R No. (R59)
MM1681FFBE	1.5	1499	MM1683EFBE	3.4	1521
MM1681GFBE	1.6	1500	MM1683FFBE	3.5	1522
MM1681HFBE	1.7	1501	MM1683GFBE	3.6	1523
MM1681JFBE	1.8	1502	MM1683HFBE	3.7	1524
MM1681KFBE	1.9	1503	MM1683JFBE	3.8	1525
MM1682AFBE	2.0	1504	MM1683KFBE	3.9	1526
MM1682BFBE	2.1	1505	MM1684AFBE	4.0	1527
MM1682CFBE	2.2	1506	MM1684BFBE	4.1	1528
MM1682DFBE	2.3	1507	MM1684CFBE	4.2	1529
MM1682EFBE	2.4	1508	MM1684DFBE	4.3	1530
MM1682FFBE	2.5	1509	MM1684EFBE	4.4	1531
MM1682GFBE	2.6	1510	MM1684FFBE	4.5	1532
MM1682HFBE	2.7	1511	MM1684GFBE	4.6	1533
MM1682JFBE	2.8	1512	MM1684HFBE	4.7	1534
MM1682KFBE	2.9	1513	MM1684JFBE	4.8	1535
MM1683AFBE	3.0	1514	MM1684KFBE	4.9	1536
MM1683BFBE	3.1	1515	MM1685AFBE	5.0	1537
MM1683CFBE	3.2	1518	MM1685BFBE	5.1	1538
MM1683DFBE	3.3	1519	MM1685CFBE	5.2	1539

# Block Diagram



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# Pin Description SOP-8D/G

Pin No.	Pin name	Function	Internal equivalent circuit diagram
1	Vout	Output pin  The capacitor must be connected with output pin more than 1µF.	
2	NC	No connection	
3	Cn	Noise decrease pin  Connecting 0.01µF capacitor can decrease output noise. If the noise decrease capacitor is not connected, the pin may be influenced by outside noise.	***
4	GND	Ground	
5	CONT	ON/OFF Control pin  CONT  H  L  CONT pin must be connected with V <sub>IN</sub> pin, if it is not used.	900κΩ ≸ //// ////
6	NC	No connection	
7	NC	No connection	
8	Vin	Input pin  The capacitor is required to connect with input pin more than 1µF.	Input circuit

# Absolute Maximum Ratings (Ta=25°C)

Item	Symbol	Ratings	Units
Storage temperature	Tstg	-400~+150	°C
Operating temperature	Topr	-300~+85	°C
Supply voltage	Vin	-0.30~+12	V
Output current	Iout	400	mA
Power dissipation	Pd	950 (*1)	mW

Note1: \*1 With the double sided PC Board of glass epoxy. (Copper plane 80%, 192×142×1.2mm)

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# **Recommended Operating Conditions**

Item	Symbol	Ratings	Units
Output current	Iout	0~300	mA
Operating voltage	Vop	2.2~12	V

# Electrical Characteristics 1 (Except where noted otherwise, Ta=25°C, VIN=Vo (Typ.) +1V, Io=1mA, VCONT=1.6V)

Item	Symbol	Measurement conditions	Min.	Тур.	Max.	Units	
No-Load input current	Icc	Io=0mA		85	140	μA	
Input current (OFF)	Iccoff	Vcont=0V		0	0.1	μA	
Output voltage *2	Vout	Io=1mA	×0.985		×1.015	V	
Dropout voltage *3	Vio	V <sub>IN</sub> =V <sub>O</sub> -0.2V, I <sub>O</sub> =150mA		0.12	0.24	V	
Line regulation	⊿V1	V <sub>IN</sub> =V <sub>0</sub> +1.5~V <sub>0</sub> +2.5V, I <sub>0</sub> =1mA		10	20	mV	
Load regulation	∠/V2	Io=0~300mA		15	60	mV	
Vоит temperature coefficient *1	$\triangle V$ out/ $\triangle T$	Tj=-40~+85°C	Tj=-40~+85°C			ppm/°C	
Disale veienties 44	RR	f=120Hz	50	70		dB	
Ripple rejection *1		Vripple=1V, Io=100mA	30			ub	
Output noise voltage *1	Vn	fBW=20~80kHz, Cn=0.01 $\mu$ F, Iout=100mA		30		μVrms	
Output noise voitage *1	V 11	fBW=20~80kHz, Cn=OPEN, Iout=100mA		150		μVIIIIS	
CONT pin input current	Icont		10	20	30	μA	
CONT pin high threshold level	VcontH		1.6		V <sub>IN</sub> +0.3	V	
CONT pin low threshold level	VcontL		-0.3		0.4	V	

Note 1: \*1 The parameter is guaranteed by design.

Note 2: \*2 Please refer to Electrical Characteristics 2.

Note 3: \*3 The parameter is not guaranteed in the model less than Vout=2V.

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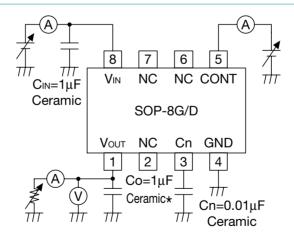
# Electrical Characteristics 2 (Except where noted otherwise, Ta=25°C, VIN=Vo (Typ.)+1V, Io=1mA, Vcont=1.6V)

#### Output Voltage

Madal N-	Test	Output voltage		
Model No.	conditions	Min.	Тур.	Max.
MM1681F		1.478	1.5	1.523
MM1681G		1.576	1.6	1.624
MM1681H		1.675	1.7	1.726
MM1681J		1.773	1.8	1.827
MM1681K		1.872	1.9	1.929
MM1682A		1.970	2.0	2.030
MM1682B		2.069	2.1	2.132
MM1682C		2.167	2.2	2.233
MM1682D		2.266	2.3	2.335
MM1682E		2.364	2.4	2.436
MM1682F	Io=1mA	2.463	2.5	2.538
MM1682G	10=1111A	2.561	2.6	2.639
MM1682H		2.660	2.7	2.741
MM1682J		2.758	2.8	2.842
MM1682K		2.857	2.9	2.944
MM1683A		2.955	3.0	3.045
MM1683B		3.054	3.1	3.147
MM1683C		3.152	3.2	3.248
MM1683D		3.251	3.3	3.350
MM1683E		3.349	3.4	3.451
MM1683F		3.448	3.5	3.553
MM1683G		3.546	3.6	3.654

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Test	Outp	Output voltage		
conditions	Min.	Тур.	Max.	
	3.645	3.7	3.756	
	3.743	3.8	3.857	
	3.842	3.9	3.959	
	3.940	4.0	4.060	
	4.039	4.1	4.162	
	4.137	4.2	4.263	
T 1A	4.236	4.3	4.365	
	4.334	4.4	4.466	
10=1111A	4.433	4.5	4.568	
	4.531	4.6	4.669	
	4.630	4.7	4.771	
	4.728	4.8	4.872	
	4.827	4.9	4.974	
	4.925	5.0	5.075	
	5.024	5.1	5.177	
	5.122	5.2	5.278	
	Test conditions  Io=1mA	Io=1mA Hina 4.236 4.433 4.531 4.630 4.728 4.925 5.024	To=1mA  Io=1mA  Min. Typ.  3.645 3.7 3.743 3.8 3.842 3.9 3.940 4.0 4.039 4.1 4.137 4.2 4.236 4.3 4.334 4.4 4.433 4.5 4.531 4.6 4.630 4.7 4.728 4.8 4.827 4.9 4.925 5.0 5.024 5.1	

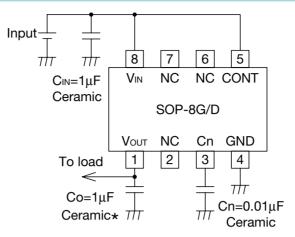
# **Measuring Circuit**



\*Temperature Characteristics: B Type

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# **Application Circuit**



\*Temperature Characteristics: B Type

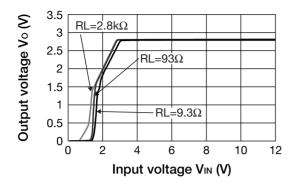
#### Note

- 1. The output capacitor is required between output and GND to prevent oscillation.
- 2. The output capacitor must be used in ESR stable area. It is possible to use a ceramic capacitor without ESR resistance for output. The ceramic capacitor must be more than 1µF with B type temperature characteristics.
- 3. The wire of Vcc and GND is required to print full ground plane for noise and stability.
- 4. The input capacitor must be connected in 1cm from input pin.
- 5. When the output voltage oversteps the input voltage, the overcurrent can flow by internal parasitic diode. In such application, the external bypass diode must be connected between output and input pin.

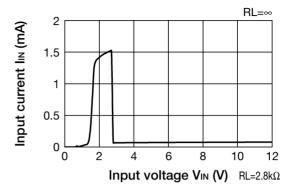
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# Characteristics (Vo=2.8V Except where noted otherwise, Ta=25°C, Vin=Vo+1V, Vcont=2V, Cin=1µF, Co=1µF, Cn=0.01µF)

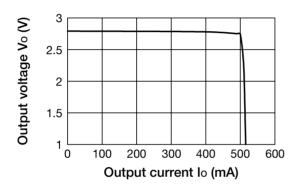
#### Output-Input Voltage



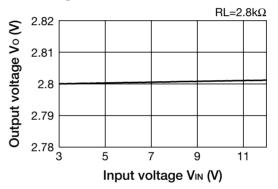
#### Input Current-Input Voltage



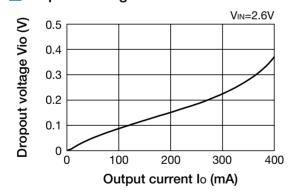
#### Load Regulation



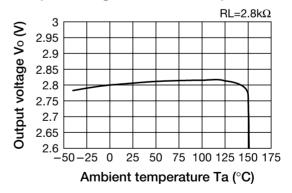
#### Line Regulation



#### Dropout Voltage



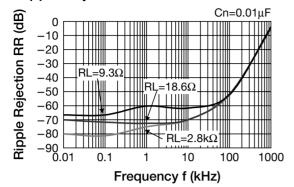
#### Output Voltage- Ambient Temperature



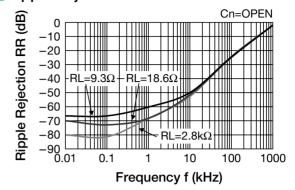
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## Characteristics (Vo=2.8V Except where noted otherwise, Ta=25°C, Vin1=Vo+1V, Vcont=2V, Cin=1µF, Co=1µF, Cn=0.01µF)

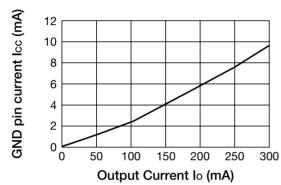
#### Ripple Rejection



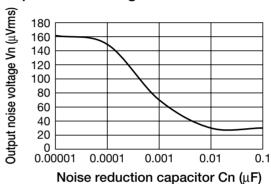
#### Ripple Rejection



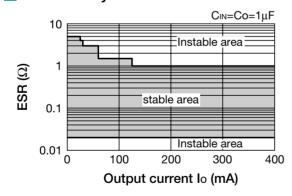
#### ■ GND Pin Current



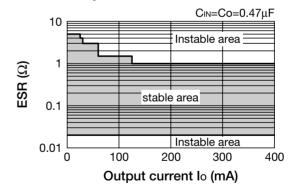
#### Output Noise Voltage



#### **ESR Stability Area**



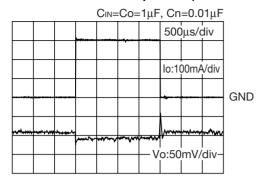
#### ESR Stability Area



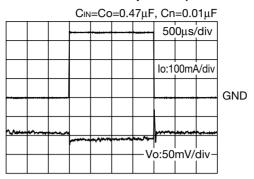
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### Characteristics (Vo=2.8V Except where noted otherwise, Ta=25°C, Vin1=Vo+1V, Vcont=2V, Cin=1µF, Co=1µF, Cn=0.01µF)

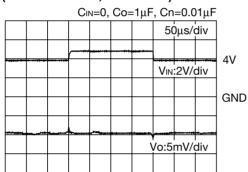
#### Load Transient Respones (Io=0 → 300mA)



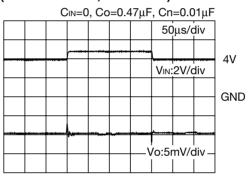
#### Load Transient Respones (Io=0 → 300mA)



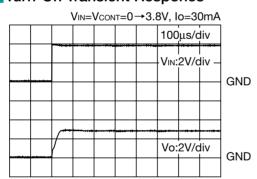
#### Line Transient Respones $(V_{IN}=3.8 \rightarrow 4.8V, Io=30mA)$



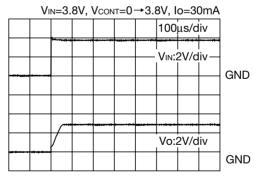
#### Line Transient Respones $(V_{IN}=3.8 \rightarrow 4.8 \text{V, Io}=30 \text{mA})$



#### Turn-On Transient Respones



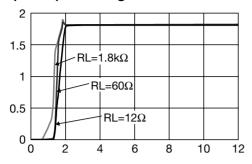
### Turn-On Transient Tespones



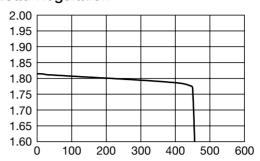
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# Characteristics (Vo=1.8V Except where noted otherwise, Ta=25°C, Vin1=Vo+1V, Vcont=2V, Cin=1µF, Co=1µF, Cn=0.01µF)

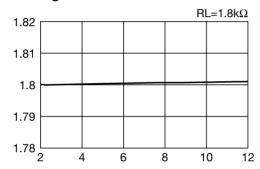
#### Output-Input Voltage



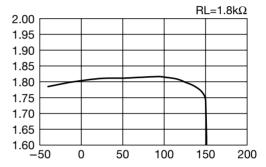
#### Load Regulation



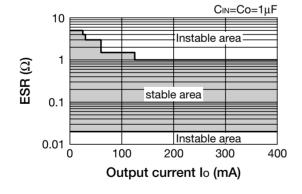
#### Line Regulation



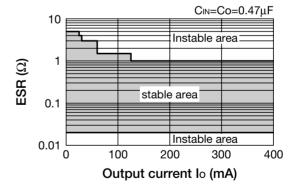
#### Output Voltage- Ambient Temperature



#### **ESR Stability Area**



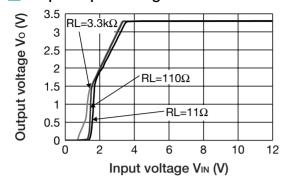
#### **ESR Stability Area**



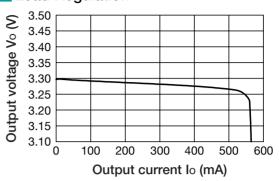
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#### Characteristics (Vo=3.3V Except where noted otherwise, Ta=25°C, Vin1=Vo+1V, Vcont=2V, Cin=1µF, Co=1µF, Cn=0.01µF)

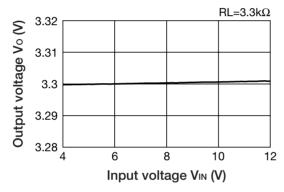
#### Output-Input Voltage



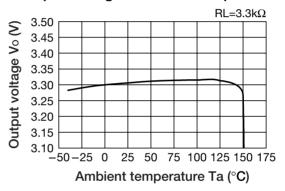
#### Load Regulation



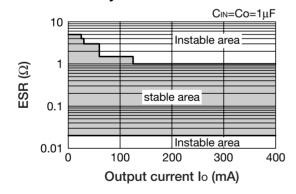
#### Line Regulation



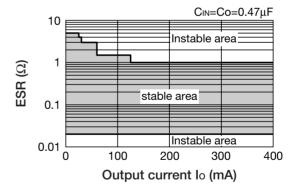
#### Output Voltage- Ambient Temperature



#### **ESR Stability Area**



#### ESR Stability Area



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