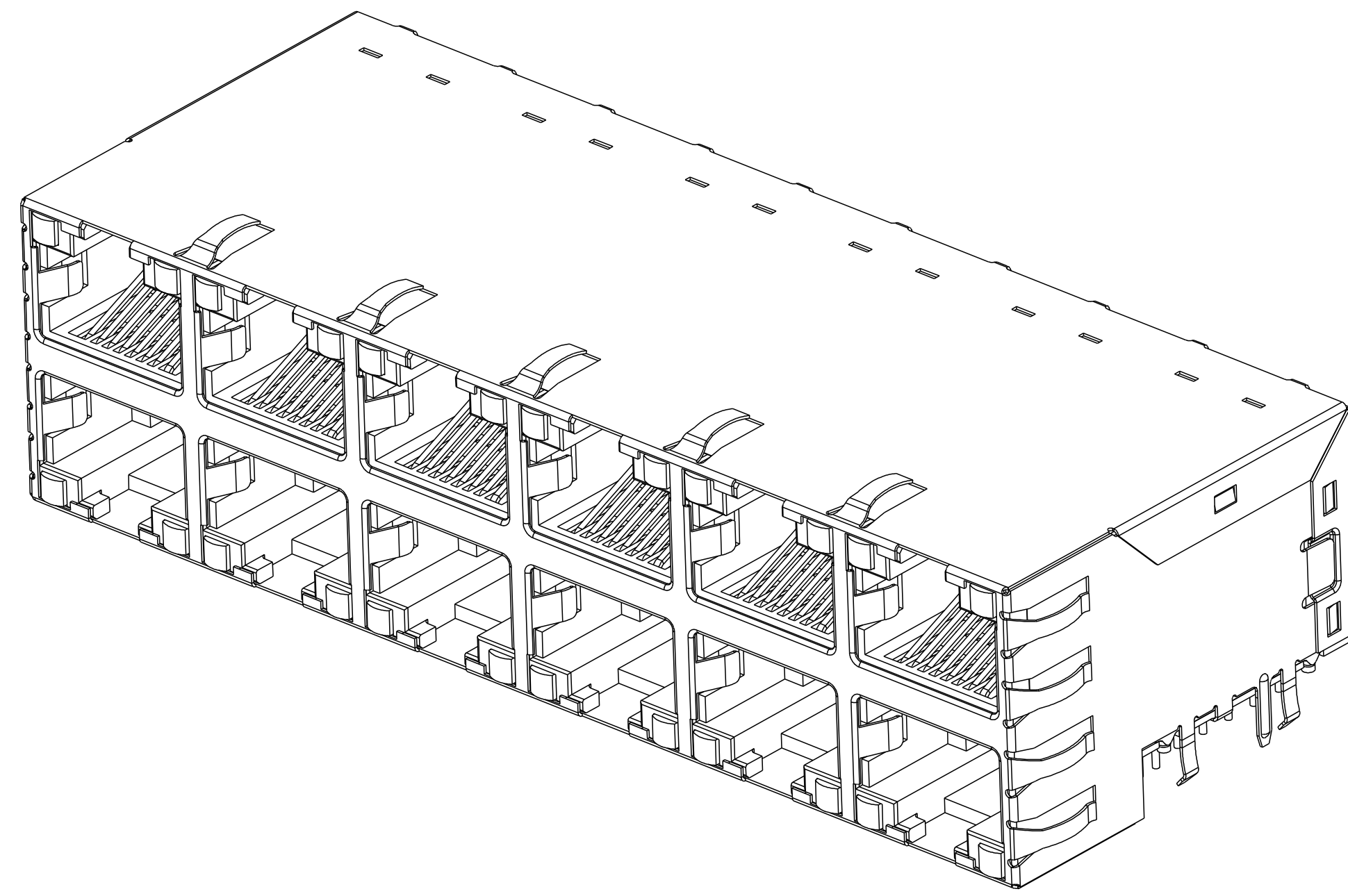
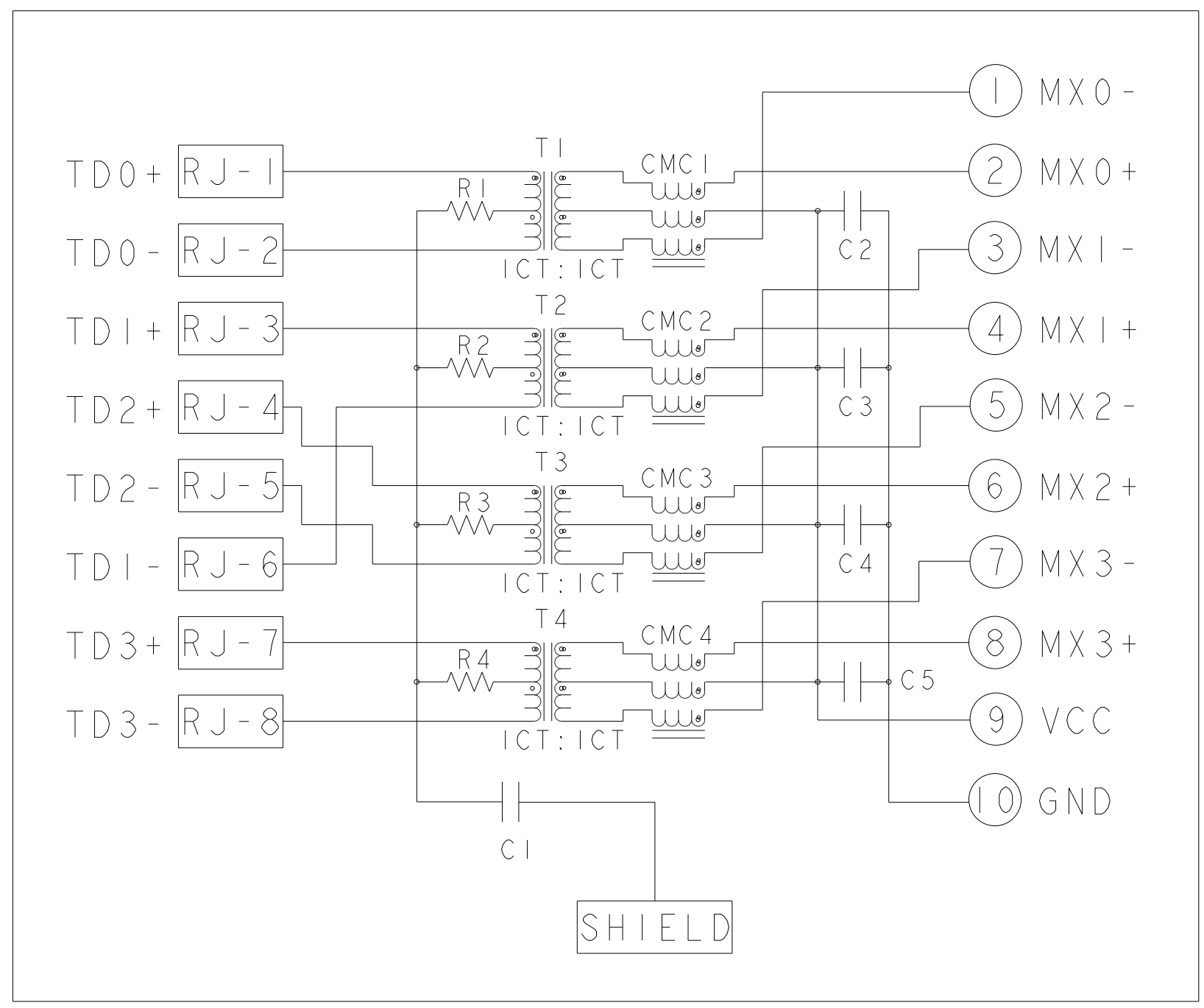


REVISIONS				
P	LTR	DESCRIPTION	DATE	DWN APVD
C		LOGO CHANGE	06 JUN 2013	RL KZ

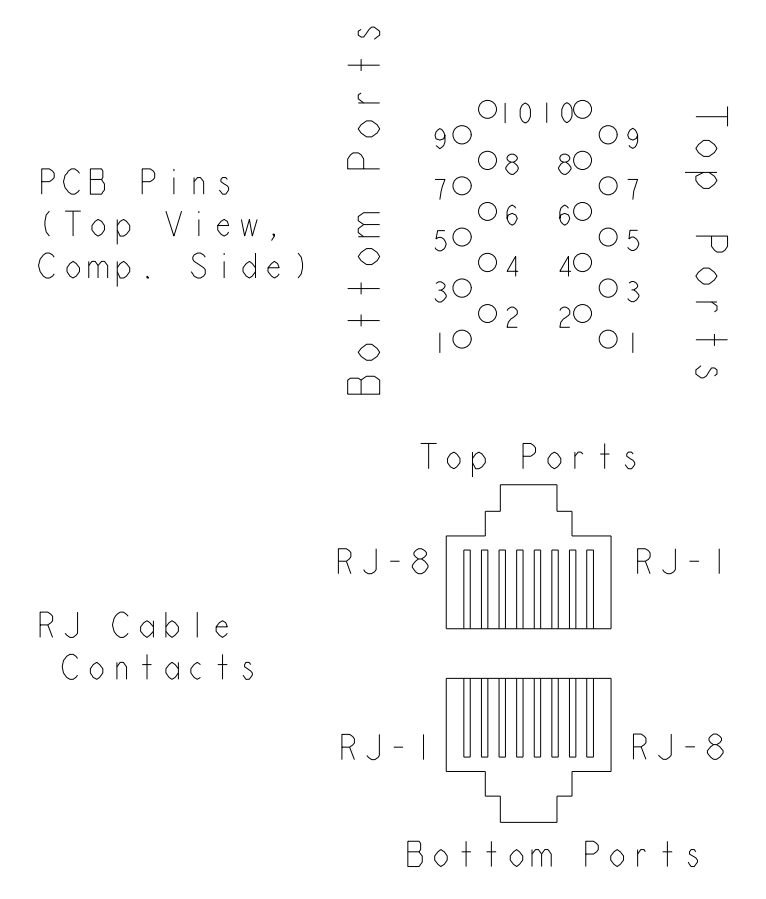


S8G31 GIGABIT CIRCUIT $\Delta\Delta$
 Top and Bottom Ports



C1 = 1000pF, 2kV
 C2-C5 = 470pF, 10%, 50V
 R1-R4 = 75ohms, 1/16W

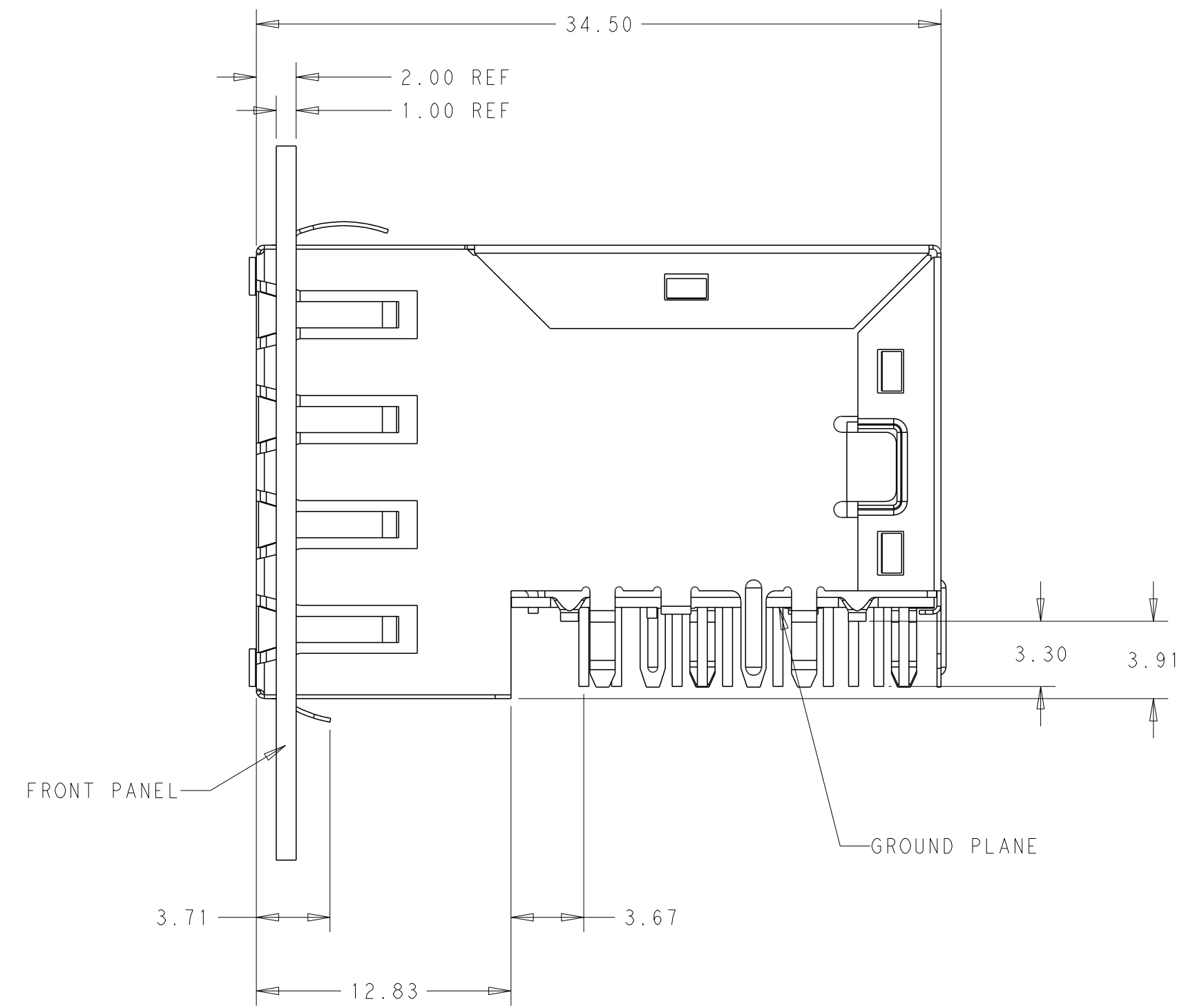
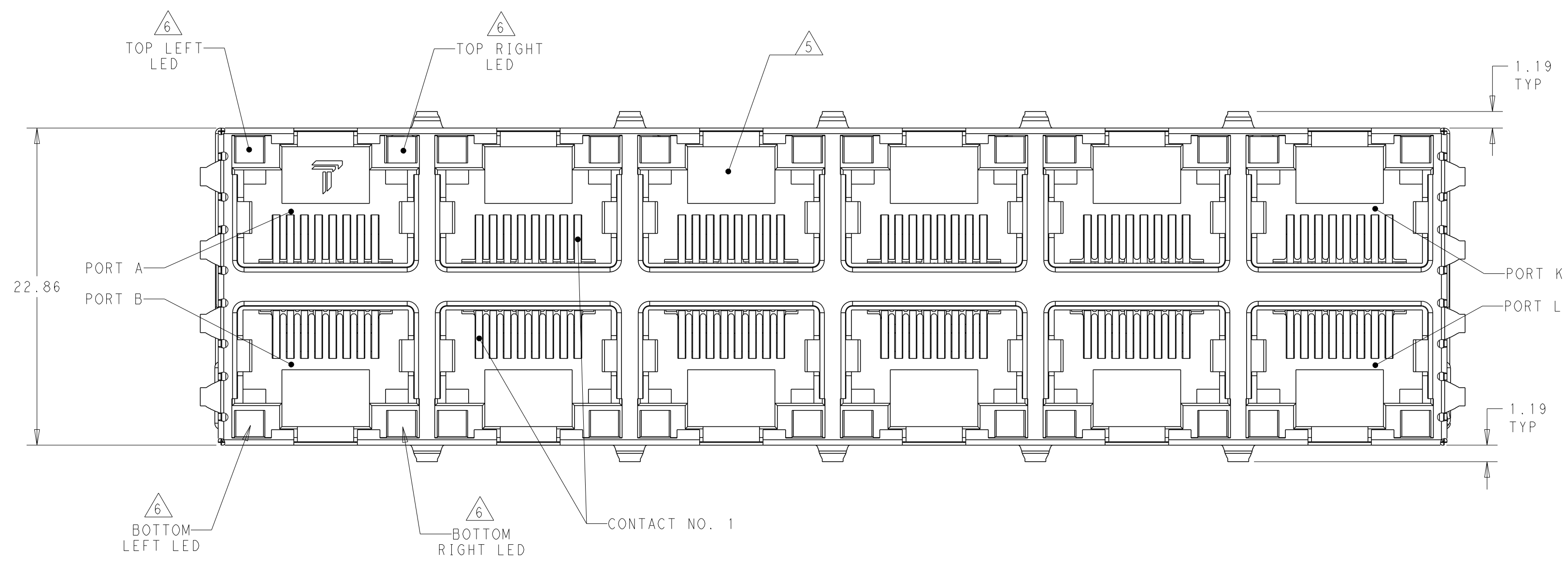
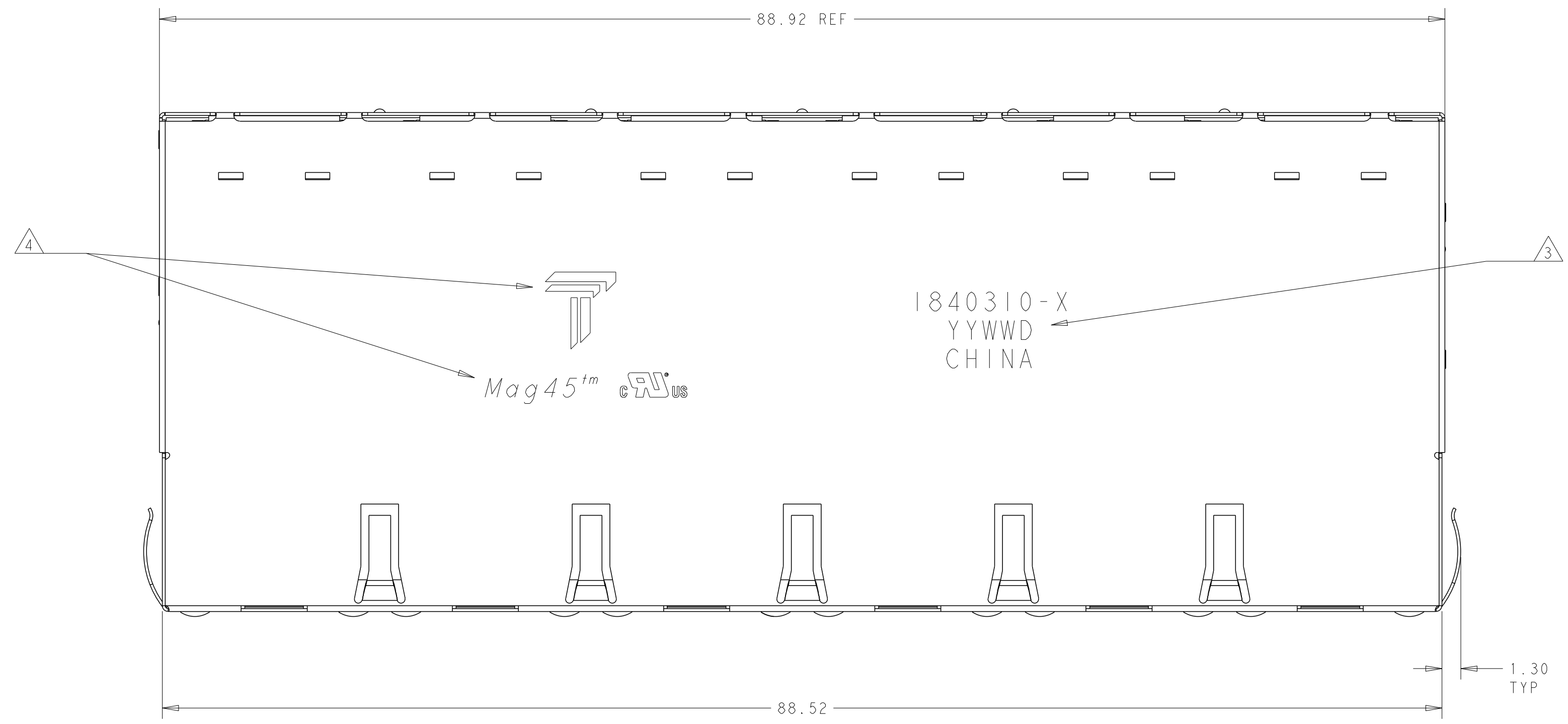
Pin Designations
 (Repeat for each vertical pair of ports.)



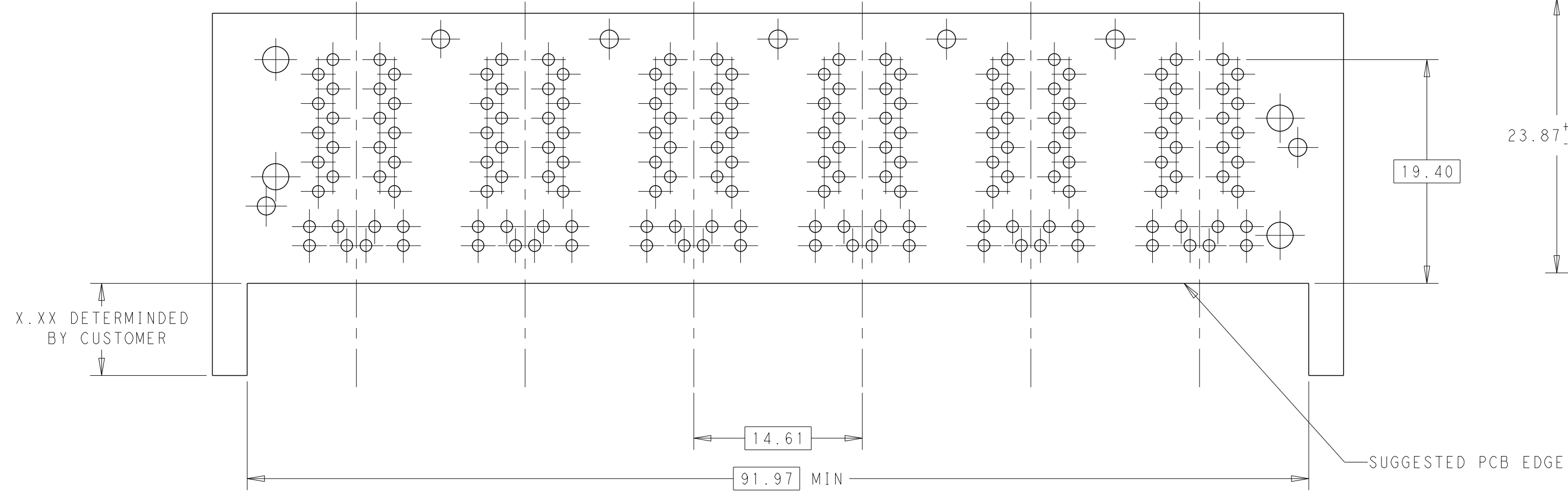
- MATERIALS:
 - HOUSING: THERMOPLASTIC, BLACK, FLAMMABILITY RATING UL 94V-0.
 - SHIELD: BRASS, PREPLATED WITH 0.76um MIN SEMI-BRIGHT NICKEL, POST DIPPED WITH 2.54um MIN SAC SOLDER ON SOLDER TAILS.
 - CONTACTS: PHOSPHOR BRONZE, PLATED WITH 1.27um MIN OVERALL NICKEL UNDERPLATE, SELECTIVE 1.27um MIN GOLD AT MATING INTERFACE AND 2.54um MIN MATTE TIN ON SOLDER TAILS.
 - LED: DIFFUSED EPOXY LENS, CARBON STEEL LEAD FRAME LEADS, PREPLATED WITH 2.03um MIN SILVER OVER 1.02um MIN NICKEL OVER 1.02um MIN COPPER UNDERPLATE, POST PLATED WITH 2.54um MIN MATTE TIN AND/OR 2.54um MIN SAC SOLDER DIP OR PURE TIN SOLDER DIP.
- MAGNETICS
 - APPLICATION: 10/100/1000 BASE-T
 - IMPEDANCE: 100 Ohms
 - TURNS RATIO (CHIP:CABLE): 1:1 ALL FOUR PAIRS
 - OPEN CIRCUIT INDUCTANCE (OCL): 350uH MIN @100kHz, 0.1VRMS, 8mADC BIAS FROM 0 °C TO 70 °C, ALL FOUR PAIRS
 - ALL FOUR PAIRS BI-DIRECTIONAL
 - PERFORMANCE @ 25 °C:
 - INSERTION LOSS (IL): 1.1dB MAX FROM 0.5MHz TO 100MHz
 - RETURN LOSS (RL): 18dB MIN FROM 0.5MHz TO 40MHz
 - 12-20LOG(f/80)dB MIN FROM 40.1MHz TO 100MHz
 - CROSSTALK ATTENUATION: 35dB MIN FROM 0.5MHz TO 40MHz
 - 33-20LOG(f/50)dB MIN FROM 40.1MHz TO 100MHz
 - COMMON MODE REJECTION RATIO (CMRR):
 - 30dB MIN FROM 0.5MHz TO 100MHz
 - ISOLATION VOLTAGE: COMPLIES WITH IEEE802.3 2002, PARA, 40.6.1.1 ITEM b.
- PART NUMBER, DATE CODE AND COUNTRY OF ORIGIN ARE LOCATED IN THE APPROXIMATE AREA SHOWN. DATE CODE YYWD WHERE "YY" IS YEAR, "WW" IS WORK WEEK, "D" IS DAY OF WEEK WITH SUNDAY=1
- TRP CONNECTOR LOGO AND AGENCY APPROVAL LOGO ARE LOCATED IN THE APPROXIMATE AREA SHOWN.
- RJ45 CAVITIES CONFORM TO FCC RULES AND REGULATION PART 68 SUBPART F.
- LEDS ARE DRIVEN WITH CONSTANT CURRENT AT APPROX. 20mA.
 LED COLOR: DOMINANT WAVELENGTH (λ D)
 GREEN 568nm TYP AT IF=20mA
 YELLOW 588nm TYP AT IF=20mA
 FORWARD VOLTAGE(VF)
 GREEN, 2.2V TYP AT IF=20mA
 YELLOW, 2.1V TYP AT IF=20mA
- INDICATED MAGNETIC CONNECTIONS ARE SYMMETRICAL AND SUPPORT AUTO-MDI/MDIX.
- OPERATING TEMPERATURE: FROM 0 °C TO 70 °C
- THE PARTS ARE RECOMMENDED FOR WAVE SOLDERING PROCESS, PEAK TEMPERATURE 265 °C MAX. 10 SECONDS MAX.

YELLOW/GREEN	YELLOW/GREEN	YELLOW/GREEN	YELLOW/GREEN	1840310-4
TOP LEFT LED	TOP RIGHT LED	BOTTOM LEFT LED	BOTTOM RIGHT LED	PART NO.

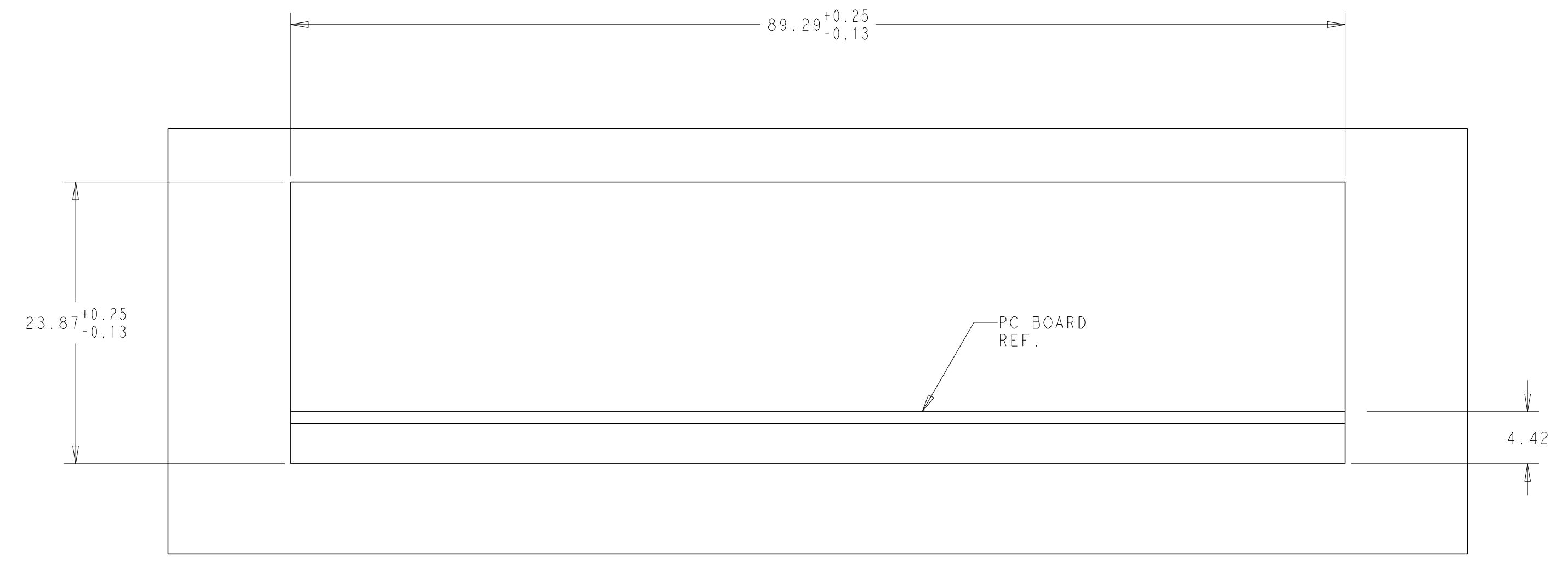
THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN: DALE PAN 30 JUL 2008	TRP connector Dongguan China
DIMENSIONS: mm		CHK: KEITH ZHU 30 JUL 2008	
TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PL ± 1 PL ± 2 PL ±0.25 3 PL ± 4 PL ± ANGLES ±		APVD: TEDDY XIANG 30 JUL 2008	
MATERIAL: -		FINISH: -	PRODUCT SPEC: 108-101004 APPLICATION SPEC: - NAME: STACKED, S8G56 GIGABIT CIRCUIT, 10 PIN VERTICAL, 2X6 OFFSET SIZE: A1 CAGE CODE: - DRAWING NO: C=1840310 WEIGHT: - CUSTOMER DRAWING: - SCALE: NTS SHEET: 1 OF 4 REV: C



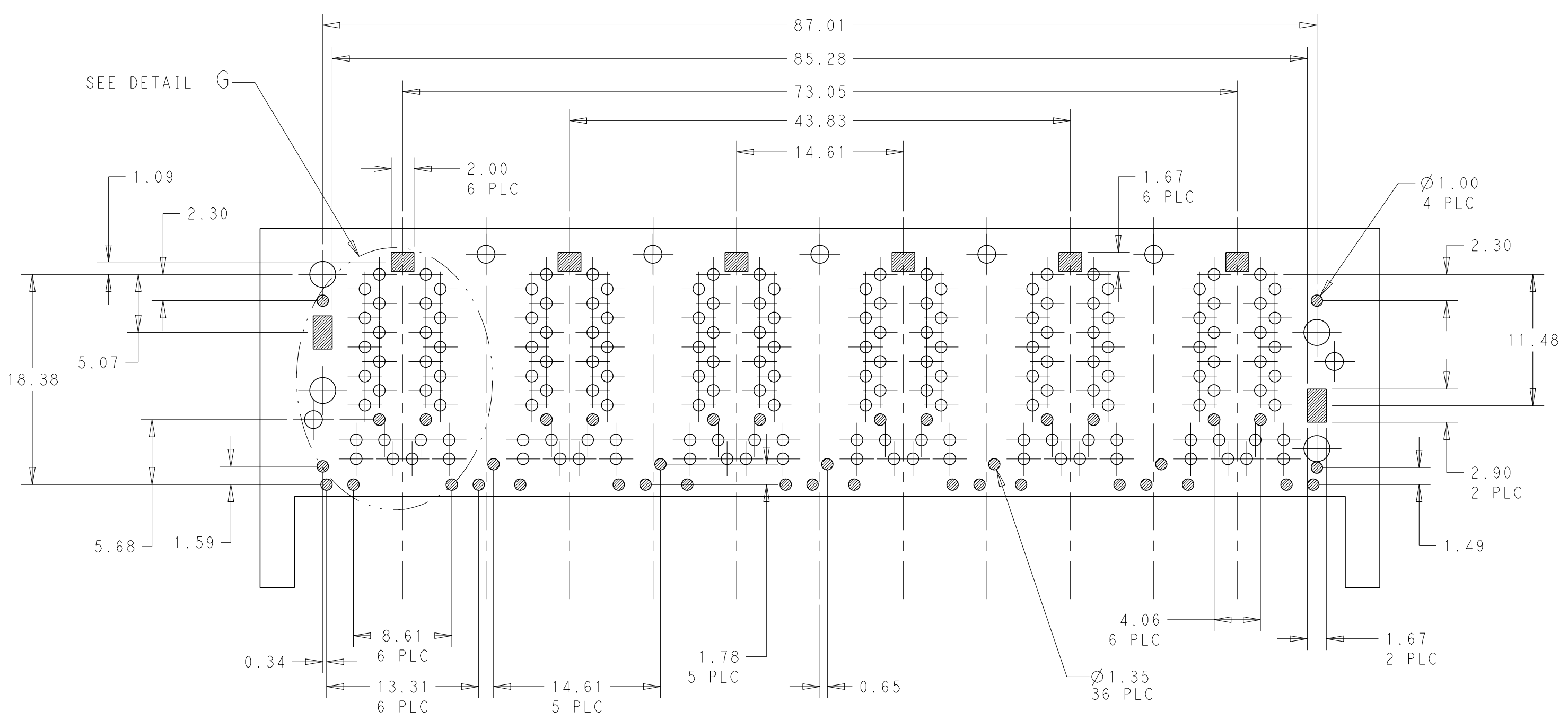
THIS DRAWING IS A CONTROLLED DOCUMENT.		OWN: DALE PAN 30JUL2008	TRP connector Dongguan China				
DIMENSIONS: mm		CHK: KEITH ZHU 30JUL2008					
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD: TEDDY XIANG 30JUL2008					
0 PLC ±	1 PLC ±	2 PLC ±0.25	3 PLC ±	4 PLC ±	ANGLES ±	FINISH	
MATERIAL	PRODUCT SPEC		NAME	RESTRICTED TO			
	108-101004		STACKED, S8G56 GIGABIT CIRCUIT, 10 PIN VERTICAL, 2X6 OFFSET	SIZE	CAGE CODE	DRAWING NO	
	APPLICATION SPEC		A1	C=1840310			
	WEIGHT						
CUSTOMER DRAWING			SCALE	SHEET	OF	REV	
			NTS	2	4	C	



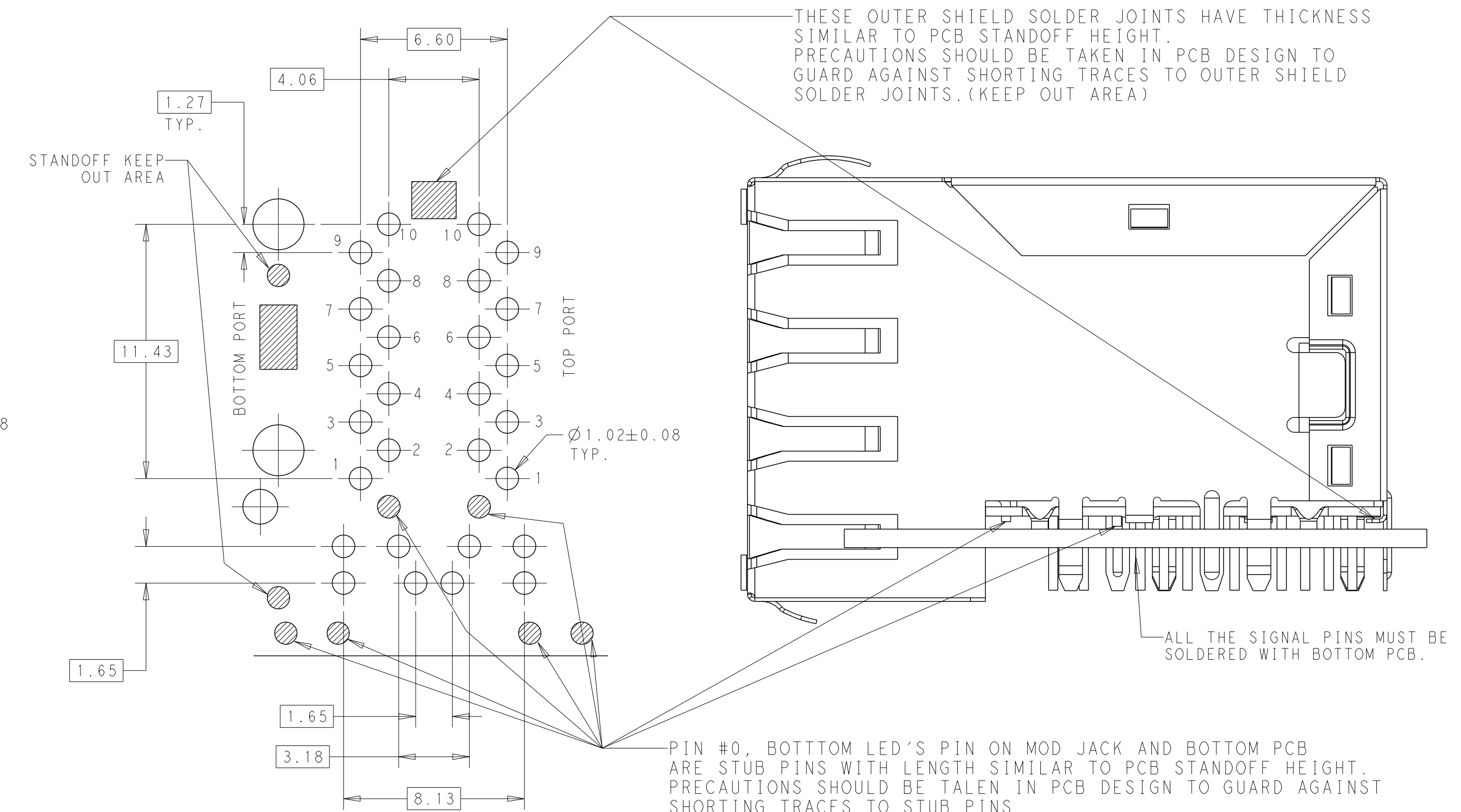
RECOMMENDED PC BOARD LAYOUT VIEWED FROM COMPONENT SIDE



RECOMMENDED PANEL CUT-OUT

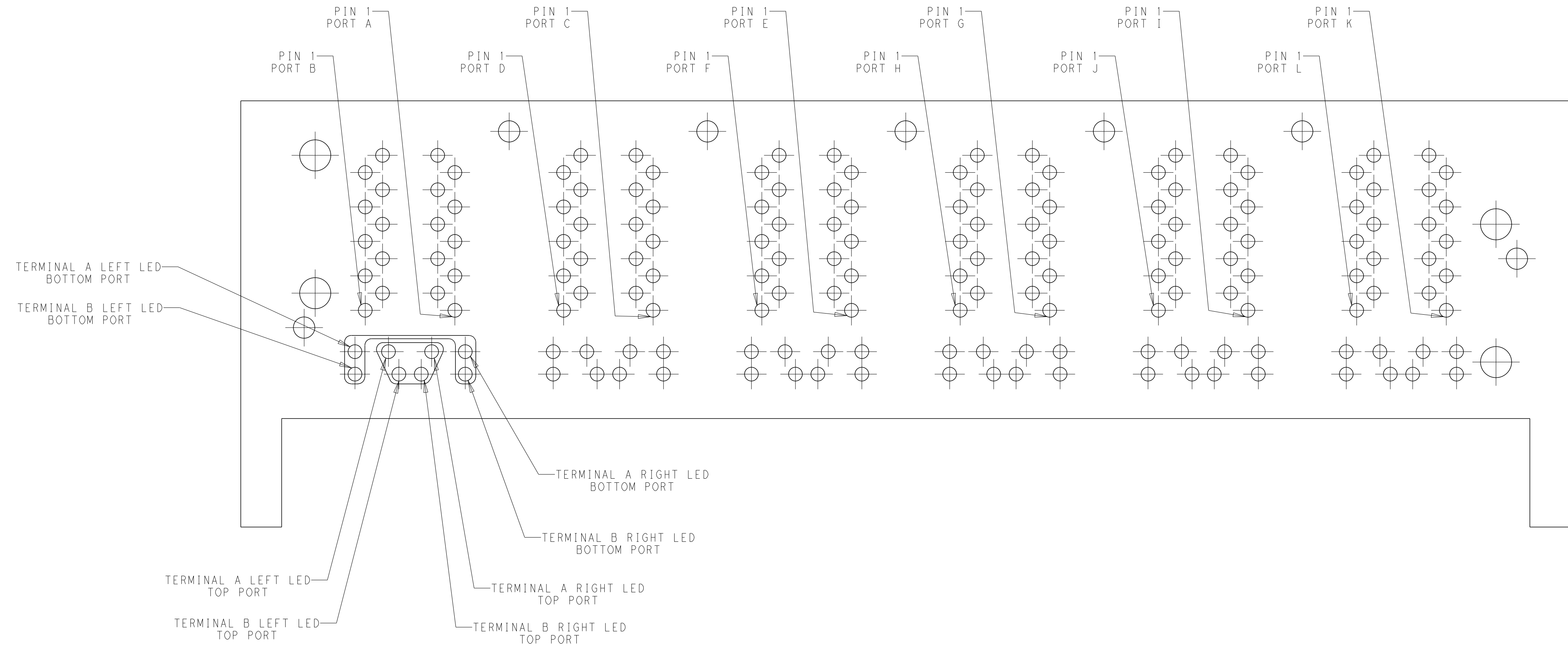


RECOMMENDED PC BOARD LAYOUT VIEWED FROM COMPONENT SIDE



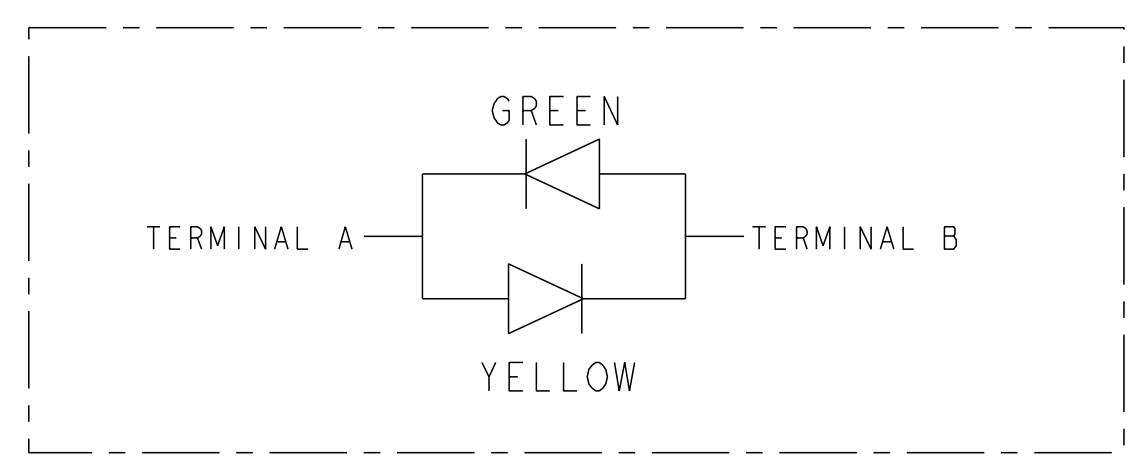
DETAIL G SCALE 6:1

THIS DRAWING IS A CONTROLLED DOCUMENT.		OWN: DALE PAN 30JUL2008	 TRP connector Dongguan China
DIMENSIONS: mm		CHK: KEITH ZHU 30JUL2008	
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD: TEDDY XIANG 30JUL2008	
0 PLC ± 1 PLC ± 2 PLC ±0.25 3 PLC ± 4 PLC ± ANGLES ± FINISH -	PRODUCT SPEC: 108-101004 APPLICATION SPEC:	NAME: STACKED, S8G56 GIGABIT CIRCUIT, 10 PIN VERTICAL, 2X6 OFFSET SIZE: A1 CAGE CODE: 1840310 DRAWING NO:	RESTRICTED TO: - SCALE: NTS SHEET: 3 OF 4 REV: C
MATERIAL: -	FINISH: -	WEIGHT: -	CUSTOMER DRAWING



LED HOLE DESIGNATIONS VIEWED FROM COMPONENT SIDE
 SCALE 5:1

BI COLOR LED



THIS DRAWING IS A CONTROLLED DOCUMENT.		OWN: DALE PAN 30JUL2008	TRP connector Dongguan China
DIMENSIONS: mm		CHK: KEITH ZHU 30JUL2008	
		APVD: TEDDY XIANG 30JUL2008	
TOLERANCES UNLESS OTHERWISE SPECIFIED:		PRODUCT SPEC	NAME: STACKED, S8G56 GIGABIT CIRCUIT, 10 PIN VERTICAL, 2X6 OFFSET
0 PLC ±	1 PLC ±	APPLICATION SPEC	SIZE: A1
2 PLC ±0.25	3 PLC ±	WEIGHT: -	CAGE CODE: -
4 PLC ±	ANGLES ±	FINISH: -	DRAWING NO: C=1840310
MATERIAL: -	FINISH: -	CUSTOMER DRAWING	RESTRICTED TO: -
SCALE: NTS		SHEET: 4	OF: 4
REV: C			