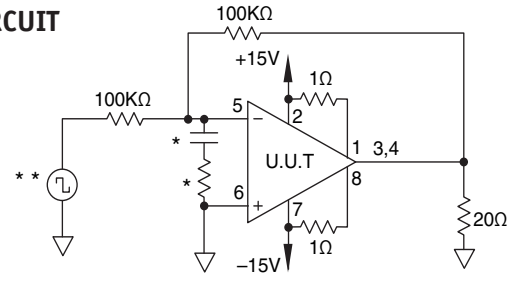
Table 4 Group A Inspection

SG	PARAMETER	SYMBOL	TEMP.	POWER	TEST CONDITIONS	MIN	MAX	UNITS
1	Quiescent current	I_Q	25°C	±15V	V_{IN} = 0, A_V = 100, R_{CL} = .2Ω		40	mA
1	Input offset voltage	V_{OS}	25°C	±15V	V_{IN} = 0, A_V = 100		10	mV
1	Input offset voltage	V_{OS}	25°C	±7V	V_{IN} = 0, A_V = 100		11.6	mV
1	Input offset voltage	V_{OS}	25°C	±19V	V_{IN} = 0, A_V = 100		10.8	mV
1	Input bias current, +IN	$+I_B$	25°C	±15V	V_{IN} = 0		200	pA
1	Input bias current, –IN	$–I_B$	25°C	±15V	V_{IN} = 0		200	pA
1	Input offset current	I_{OS}	25°C	±15V	V_{IN} = 0		100	pA
3	Quiescent current	I_Q	–55°C	±15V	V_{IN} = 0, A_V = 100, R_{CL} = .2Ω		60	mA
3	Input offset voltage	V_{OS}	–55°C	±15V	V_{IN} = 0, A_V = 100		14	mV
3	Input offset voltage	V_{OS}	–55°C	±7V	V_{IN} = 0, A_V = 100		15.6	mV
3	Input offset voltage	V_{OS}	–55°C	±19V	V_{IN} = 0, A_V = 100		14.8	mV
3	Input bias current, +IN	$+I_B$	–55°C	±15V	V_{IN} = 0		200	pA
3	Input bias current, –IN	$–I_B$	–55°C	±15V	V_{IN} = 0		200	pA
3	Input offset current	I_{OS}	–55°C	±15V	V_{IN} = 0		100	pA
2	Quiescent current	I_Q	125°C	±15V	V_{IN} = 0, A_V = 100, R_{CL} = .2Ω		60	mA
2	Input offset voltage	V_{OS}	125°C	±15V	V_{IN} = 0, A_V = 100		15	mV
2	Input offset voltage	V_{OS}	125°C	±7V	V_{IN} = 0, A_V = 100		16.6	mV
2	Input offset voltage	V_{OS}	125°C	±19V	V_{IN} = 0, A_V = 100		15.8	mV
2	Input bias current, +IN	$+I_B$	125°C	±15V	V_{IN} = 0		30	nA
2	Input bias current, –IN	$–I_B$	125°C	±15V	V_{IN} = 0		30	nA
2	Input offset current	I_{OS}	125°C	±15V	V_{IN} = 0		10	nA
4	Output voltage, I_O = 5A	V_O	25°C	±9V	R_L = 1Ω, R_{CL} = 0Ω	5		V
4	Output voltage, I_O = 36mA	V_O	25°C	±19V	R_L = 500Ω	18		V
4	Output voltage, I_O = 2A	V_O	25°C	±12V	R_L = 5Ω, R_{CL} = 0Ω	10		V
4	Current limits	I_{CL}	25°C	±9V	R_L = 5Ω, R_{CL} = 1Ω	.54	.86	A
4	Stability/noise	E_N	25°C	±15V	R_L = 500Ω, A_V = 1, C_L = 1.5nF		1	mV
4	Slew rate	SR	25°C	±18V	R_L = 500Ω	13	100	V/µs
4	Open loop gain	A_{OL}	25°C	±15V	R_L = 500Ω, F = 10Hz	86		dB
4	Common mode rejection	CMR	25°C	±8.25V	R_L = 500Ω, F = DC, V_{CM} = ±2.25V	70		dB
6	Output voltage, I_O = 5A	V_O	–55°C	±9V	R_L = 1Ω, R_{CL} = 0Ω	5		V
6	Output voltage, I_O = 36mA	V_O	–55°C	±19V	R_L = 500Ω	18		V
6	Output voltage, I_O = 2A	V_O	–55°C	±12V	R_L = 5Ω, R_{CL} = 0Ω	10		V
6	Stability/noise	E_N	–55°C	±15V	R_L = 500Ω, A_V = 1, C_L = 1.5nF		1	mV
6	Slew rate	SR	–55°C	±18V	R_L = 500Ω	13	100	V/µs
6	Open loop gain	A_{OL}	–55°C	±15V	R_L = 500Ω, F = 10Hz	86		dB
6	Common mode rejection	CMR	–55°C	±8.25V	R_L = 500Ω, F = DC, V_{CM} = ±2.25V	70		dB
5	Output voltage, I_O = 3A	V_O	125°C	±7V	R_L = 1Ω, R_{CL} = 0Ω	3		V
5	Output voltage, I_O = 36mA	V_O	125°C	±19V	R_L = 500Ω	18		V
5	Output voltage, I_O = 2A	V_O	125°C	±12V	R_L = 5Ω, R_{CL} = 0Ω	10		V
5	Stability/noise	E_N	125°C	±15V	R_L = 500Ω, A_V = 1, C_L = 1.5nF		1	mV
5	Slew rate	SR	125°C	±18V	R_L = 500Ω	8.5	100	V/µs
5	Open loop gain	A_{OL}	125°C	±15V	R_L = 500Ω, F = 10Hz	86		dB
5	Common mode rejection	CMR	125°C	±8.25V	R_L = 500Ω, F = DC, V_{CM} = ±2.25V	70		dB

BURN IN CIRCUIT



* These components are used to stabilize device due to poor high frequency characteristics of burn in board.

** Input signals are calculated to result in internal power dissipation of approximately 2.1W at case temperature = 125°C.

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