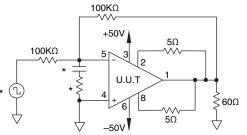


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Table 4 Group A Inspection

SG	PARAMETER	SYMBOL	TEMP.	POWER	TEST CONDITIONS	MIN	MAX	UNITS
1 1 1 1 1 1	Quiescent Current Input Offset Voltage Input Offset Voltage Input Offset Voltage Input Bias Current, +IN Input Bias Current, –IN Input Offset Current	$\begin{matrix} I_{o} \\ V_{os} \\ V_{os} \\ V_{os} \\ +I_{B} \\ -I_{B} \\ I_{os} \end{matrix}$	25°C 25°C 25°C 25°C 25°C 25°C 25°C	±100V ±100V ±15V ±150V ±100V ±100V ±100V	$ \begin{split} & V_{_{\rm IN}}=0,A_{_{\rm V}}=100 \\ & V_{_{\rm IN}}=0 \\ & V_{_{\rm IN}}=0 \\ & V_{_{\rm IN}}=0 \end{split} $		8.5 2 3.7 3 50 50 50	mA mV mV pA pA pA
3 3 3 3 3 3 3 3	Quiescent Current Input Offset Voltage Input Offset Voltage Input Offset Voltage Input Bias Current, +IN Input BiasCurrent, -IN Input Offset Current	$\begin{matrix} I_{o} \\ V_{os} \\ V_{os} \\ V_{os} \\ +I_{B} \\ -I_{B} \\ I_{os} \end{matrix}$	-55°C -55°C -55°C -55°C -55°C -55°C -55°C	$\pm 100V \\ \pm 100V \\ \pm 15V \\ \pm 150V \\ \pm 100V \\ \pm 100V \\ \pm 100V \\ \pm 100V$	$\begin{array}{l} V_{_{\rm IN}}=0,A_{_{\rm V}}=100\\ V_{_{\rm IN}}=0,A_{_{\rm V}}=100\\ V_{_{\rm IN}}=0,A_{_{\rm V}}=100\\ V_{_{\rm IN}}=0,A_{_{\rm V}}=100\\ V_{_{\rm IN}}=0\\ V_{_{\rm IN}}=0\\ V_{_{\rm IN}}=0\\ V_{_{\rm IN}}=0 \end{array}$		9.5 4.4 6.1 5.4 50 50 50	mA mV mV pA pA pA
2 2 2 2 2 2 2 2 2 2	Quiescent Current Input Offset Voltage Input Offset Voltage Input Offset Voltage Input Bias Current, +IN Input Bias Current, -IN Input Offset Current	$\begin{matrix} I_{o} \\ V_{os} \\ V_{os} \\ V_{os} \\ +I_{B} \\ -I_{B} \\ I_{os} \end{matrix}$	125°C 125°C 125°C 125°C 125°C 125°C 125°C 125°C	$\pm 100V \\ \pm 100V \\ \pm 15V \\ \pm 150V \\ \pm 100V \\ \pm 100V \\ \pm 100V \\ \pm 100V$	$\begin{array}{l} V_{_{\rm IN}}=0,A_{_{\rm V}}=100\\ V_{_{\rm IN}}=0,A_{_{\rm V}}=100\\ V_{_{\rm IN}}=0,A_{_{\rm V}}=100\\ V_{_{\rm IN}}=0,A_{_{\rm V}}=100\\ V_{_{\rm IN}}=0\\ V_{_{\rm IN}}=0\\ V_{_{\rm IN}}=0\\ V_{_{\rm IN}}=0 \end{array}$		12 5 6.7 6 10 10 10	mA mV mV nA nA nA
4 4 4 4 4 4 4	Output Voltage, $I_0 = 150$ mA Output Voltage, $I_0 = 29$ mA Output Voltage, $I_0 = 80$ mA Current Limits Stability/Noise Slew Rate Open Loop Gain Common Mode Rejection	V _° V _° V ₀ L _{cL} E R A _o R CMR	25°C 25°C 25°C 25°C 25°C 25°C 25°C 25°C	±31V ±150V ±90V ±30V ±100V ±100V ±100V ±32.5V	$\begin{array}{l} R_{L} = 100\Omega \\ R_{L} = 5K \\ R_{L} = 1K \\ R_{L} = 100\Omega \\ R_{L} = 5K, A_{V} = 1, C_{L} = 1nF \\ R_{L} = 5K \\ R_{L} = 5K \\ R_{L} = 5K, F = 10Hz \\ R_{L} = 5K, F = DC, V_{CM} = \pm 22.5V \end{array}$	15 145 80 75 20 96 90	125 1 100	V V mA mV V/µs dB dB
6 6 6 6 6 6	Output Voltage, $I_0 = 100mA$ Output Voltage, $I_0 = 29mA$ Output Voltage, $I_0 = 70mA$ Stability/Noise Slew Rate Open Loop Gain Common Mode Rejection	V _o V _o E _N SR A _{oL} CMR	-55°C -55°C -55°C -55°C -55°C -55°C -55°C	±31V ±150V ±90V ±100V ±100V ±100V ±32.5V	$\begin{array}{l} {R_{_L}} = 100\Omega \\ {R_{_L}} = 5K \\ {R_{_L}} = 1K \\ {R_{_L}} = 5K, {A_{_V}} = 1, {C_{_L}} = 1nF \\ {R_{_L}} = 5K \\ {R_{_L}} = 5K, F = 10Hz \\ {R_{_L}} = 5K, F = DC, V_{_{CM}} = \pm 22.5V \end{array}$	10 145 70 20 96 90	1 100	V V mV V/µs dB dB
5 5 5 5 5 5 5 5 5	Output Voltage, $I_0 = 150mA$ Output Voltage, $I_0 = 29mA$ Output Voltage, $I_0 = 80mA$ Stability/Noise Slew Rate Open Loop Gain Common Mode Rejection	V _o V _o E _⊼ SR A _{oL} CMR	125°C 125°C 125°C 125°C 125°C 125°C 125°C 125°C	±31V ±150V ±90V ±100V ±100V ±100V ±32.5V	$\begin{split} R_{L} &= 100\Omega \\ R_{L} &= 5K \\ R_{L} &= 1K \\ R_{L} &= 5K, A_{V} = 1, \ C_{L} &= 1nF \\ R_{L} &= 5K \\ R_{L} &= 5K, \ F &= 10Hz \\ R_{L} &= 5K, \ F &= DC, \ V_{CM} &= \pm 22.5V \end{split}$	15 145 80 20 96 90	1 100	V V mV V/µs dB dB





These components are used to stabilize device due to poor high frequency characteristics of burn in board.

** Input signals are calculated to result in internal power dissipation of approximately 2.1W at case temperature = 125°C.



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