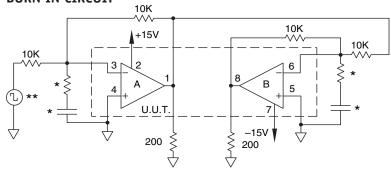




Table 4 Group A Inspection

SG	PARAMETER	SYMBOL	TEMP.	POWER	TEST CONDITIONS	MIN	MAX	UNITS
1 1 1 1 1 1 1	Quiesent Current Input Offset Voltage Input Offset Voltage Input Offset Voltage Input Bias Current + IN Input Bias Current –IN Input Offset Current	I	25°C 25°C 25°C 25°C 25°C 25°C 25°C	±15 ±2.5 ±15 ±20 ±15 ±15 ±15	$V_{IN} = 0, A_{V} = 100$ $V_{IN} = 0$ $V_{IN} = 0$ $V_{IN} = 0$ $V_{IN} = 0$		30 10 10 14 1000 1000 500	mA mV mV mV nA nA
3 3 3 3 3 3	Quiesent Current Input Offset Voltage Input Offset Voltage Input Offset Voltage Input Bias Current + IN Input Bias Current -IN Input Offset Current	I _O	-55°C -55°C -55°C -55°C -55°C -55°C	±15 ±2.5 ±15 ±20 ±15 ±15 ±15	$\begin{aligned} &V_{IN} = 0, A_{V} = 100 \\ &V_{IN} = 0 \\ &V_{IN} = 0 \\ &V_{IN} = 0 \end{aligned}$		30 14 14 18 1000 1000 500	mA mV mV mV nA nA
2 2 2 2 2 2 2	Quiesent Current Input Offset Voltage Input Offset Voltage Input Offset Voltage Input Bias Current + IN Input Bias Current -IN Input Offset Current	I _o	125°C 125°C 125°C 125°C 125°C 125°C 125°C	±15 ±2.5 ±15 ±20 ±15 ±15 ±15	$\begin{aligned} &V_{IN} = 0, A_{V} = 100 \\ &V_{IN} = 0 \\ &V_{IN} = 0 \end{aligned}$		40 15 15 19 1000 1000 500	mA mV mV mV nA nA
4 4 4 4 4 4	Output Voltage I _o = 2A Output Voltage I _o = 100mA Output Voltage I _o = 1A Stability/Noise Crosstalk Slew Rate Open Loop Gain Common-mode Rejection	V _o V _o E _N XTLK SR A _{OL} CMR	25°C 25°C 25°C 25°C 25°C 25°C 25°C 25°C	±9.5 ±11 ±4.8 ±15 ±15 ±15 ±15	$\begin{split} & \mathbf{R_L} = 3\Omega \\ & \mathbf{R_L} = 100\Omega \\ & \mathbf{R_L} = 3\Omega \\ & \mathbf{R_L} = 500\Omega, \mathbf{A_V} = 1 \mathbf{C_L} = 1.5 \text{nF} \\ & \mathbf{R_L} = 3\Omega \\ & \mathbf{R_L} = 500\Omega, \mathbf{F_L} = 500\Omega, \mathbf{F_L} = 500\Omega, \mathbf{F_L} = 500\Omega, \mathbf{F_L} = 500\Omega, \mathbf{V_{CM}} = \pm 14 \text{V} \end{split}$	6.0 9.9 2.8 50 .5 75 60	1.0	V V V dB V/μS dB dB
6 6 6 6 6	Output Voltage I_0 = 2A Output Voltage I_0 = 100mA Output Voltage I_0 = 1A Stability/Noise Slew Rate Open Loop Gain Common-mode Rejection	V _o V _o E _N SR A _{OL} CMR	-55°C -55°C -55°C -55°C -55°C -55°C	±9.5 ±11 ±4.8 ±15 ±15 ±15 ±17	$\begin{aligned} & \mathbf{R_L} = 3\Omega \\ & \mathbf{R_L} = 100\Omega \\ & \mathbf{R_L} = 3\Omega \\ & \mathbf{R_L} = 500\Omega, \mathbf{A_V} = 1, \mathbf{C_L} = 1.5 \text{nF} \\ & \mathbf{R_L} = 500\Omega, \mathbf{F} = 10 \text{Hz} \\ & \mathbf{R_L} = 500\Omega, \mathbf{V_{CM}} = \pm 14 \text{V} \end{aligned}$	6.0 9.9 2.8 .5 75 60	1.0	V V V mV V/μS dB dB
5 5 5 5 5 5 5	Output Voltage I _o = 1A Output Voltage I _o = 100mA Output Voltage I _o = 750mA Stability/Noise Slew Rate Open Loop Gain Common-mode Rejection	V° V° V° EN SR A° CMR	125°C 125°C 125°C 125°C 125°C 125°C 125°C	±4.8 ±11 ±4.0 ±15 ±15 ±17	$\begin{split} R_L &= 3\Omega \\ R_L &= 100\Omega \\ R_L &= 3\Omega \\ R_L &= 500\Omega, \ A_V = 1, \ C_L = 1.5nF \\ R_L &= 500\Omega \\ R_L &= 500\Omega, \ F = 10Hz \\ R_L &= 500\Omega, \ V_{CM} = \pm 14V \end{split}$	2.8 9.9 2.25 .5 75 60	1.0	V V V mV V/μS dB dB

BURN IN CIRCUIT



- These components are used to stabilize device due to poor high frequency characteristics of burn in board.
- Input signals are calculated to result in internal power dissipation of approximately 2.1W at case temperature = 125°C.



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