



## ELECTRICAL CHARACTERISTICS

### DC Characteristics

Ta = 25°C

PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNITS
Drain Cut-Off Current	Idss	Vds= 20V, Vgs= 0V	-	-	10	μA
Gate-Source Leak Current	Igss	Vgs= ±8V, Vds= 0V	-	-	±10	μA
Gate-Source Cut-Off Voltage	Vgs(off)	Id= 1mA, Vds= 10V	0.5	-	1.2	V
Drain-Source On-State Resistance *1	Rds(on)	Id= 0.5A, Vgs= 4.5V	-	0.075	0.100	Ω
		Id= 0.5A, Vgs= 2.5V	-	0.10	0.14	Ω
		Id= 0.1A, Vgs= 1.5V	-	0.17	0.25	Ω
Forward Transfer Admittance *1	Yfs	Id= 0.5A, Vds= 10V	-	4.2	-	S
Body Drain Diode Forward Voltage	Vf	If= 1A, Vgs= 0V	-	0.8	1.1	V

\*1 Effective during pulse test.

### Dynamic Characteristics

Ta = 25°C

PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNITS
Input Capacitance	Ciss	Vds= 10V, Vgs=0V f= 1MHz	-	220	-	pF
Output Capacitance	Coss		-	120	-	pF
Feedback Capacitance	Crss		-	45	-	pF

### Switching Characteristics

Ta = 25°C

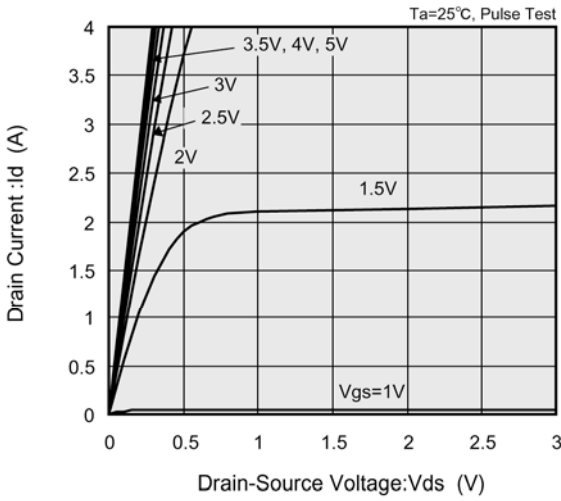
PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNITS
Turn-On Delay Time	td (on)	Vgs= 5V, Id= 0.5A Vdd= 10V	-	10	-	ns
Rise Time	tr		-	15	-	ns
Turn-Off Delay Time	td (off)		-	75	-	ns
Fall Time	tf		-	65	-	ns

### Thermal Characteristics

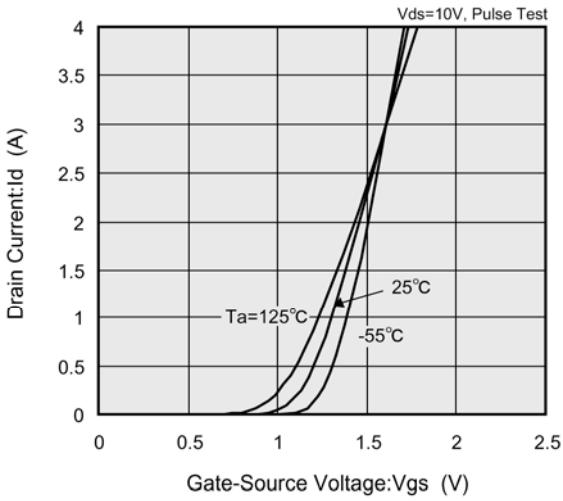
PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNITS
Thermal Resistance (Channel-Ambience)	Rth (ch-a)	Implement on a ceramic PCB	-	250	-	°C/W

**TYPICAL PERFORMANCE CHARACTERISTICS**

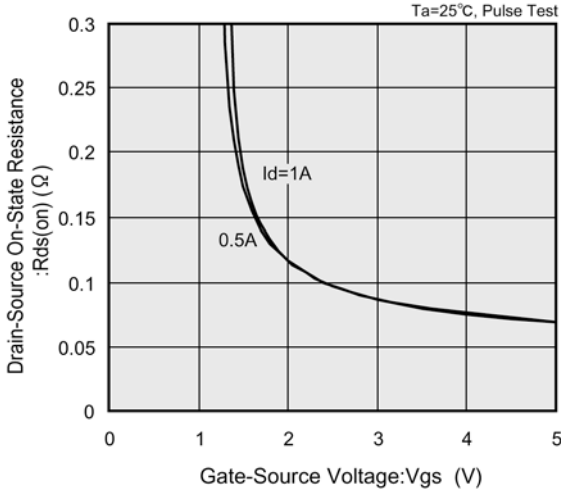
(1) Drain Current vs. Drain-Source Voltage



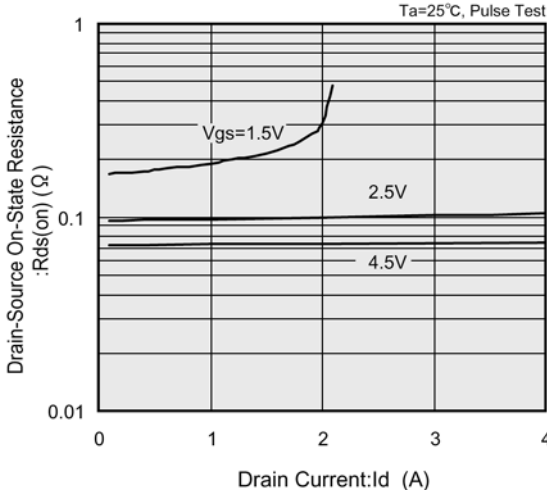
(2) Drain Current vs. Gate-Source Voltage



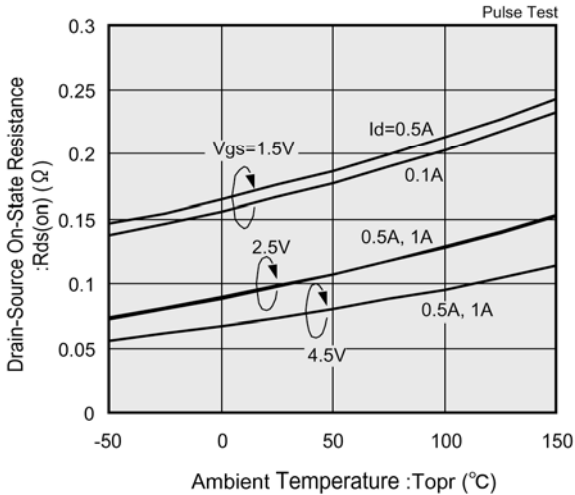
(3) Drain-Source On-State Resistance vs. Gate-Source Voltage



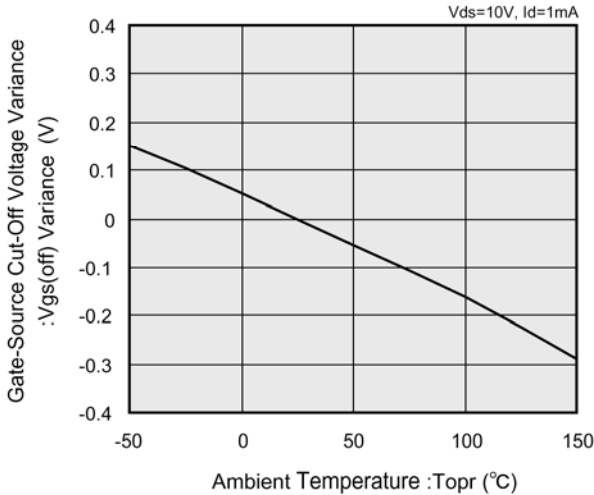
(4) Drain-Source On-State Resistance vs. Drain Current



(5) Drain-Source On-State Resistance vs. Ambient Temperature

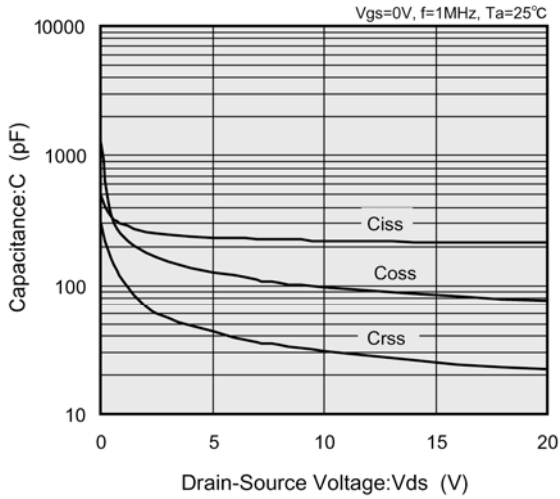


(6) Gate-Source Cut-Off Voltage Variance vs. Ambient Temperature

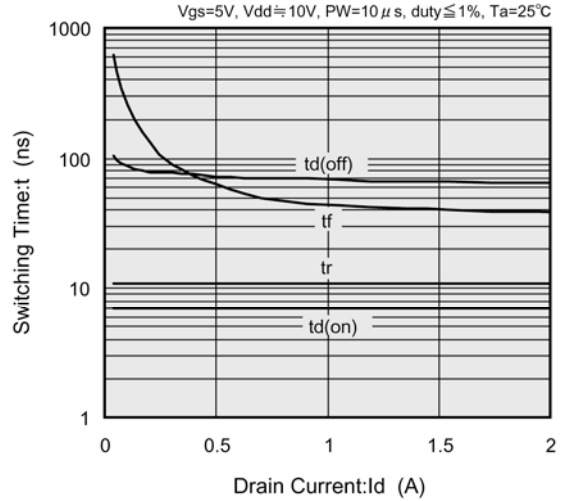


## TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

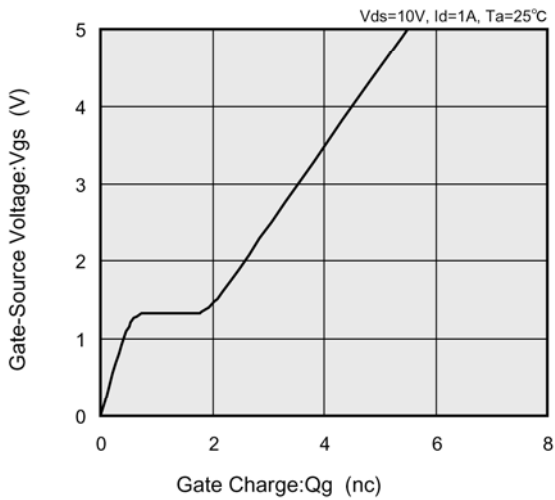
(7) Capacitance vs. Drain-Source Voltage



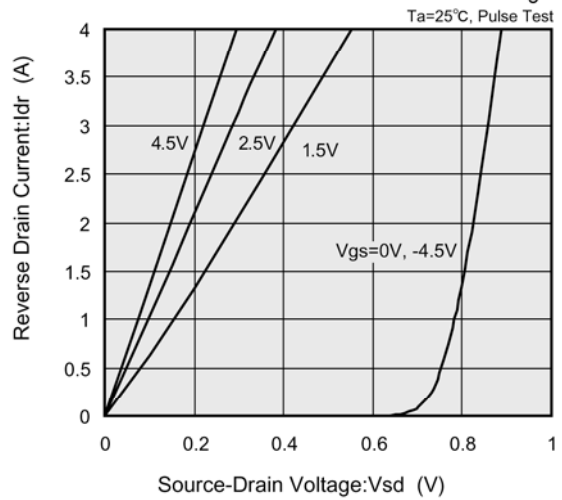
(8) Switching Time vs. Drain Current



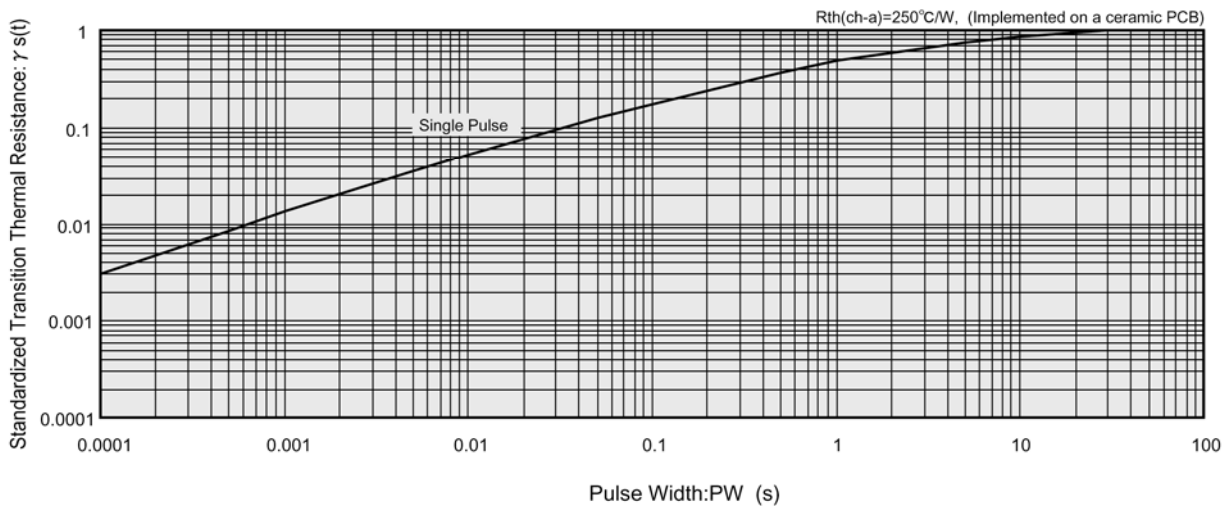
(9) Gate-Source Voltage vs. Gate Charge



(10) Reverse Drain Current vs. Source-Drain Voltage



(11) Standardized transition Thermal Resistance vs. Pulse Width



1. The products and product specifications contained herein are subject to change without notice to improve performance characteristics. Consult us, or our representatives before use, to confirm that the information in this datasheet is up to date.
2. We assume no responsibility for any infringement of patents, patent rights, or other rights arising from the use of any information and circuitry in this datasheet.
3. Please ensure suitable shipping controls (including fail-safe designs and aging protection) are in force for equipment employing products listed in this datasheet.
4. The products in this datasheet are not developed, designed, or approved for use with such equipment whose failure or malfunction can be reasonably expected to directly endanger the life of, or cause significant injury to, the user.  
(e.g. Atomic energy; aerospace; transport; combustion and associated safety equipment thereof.)
5. Please use the products listed in this datasheet within the specified ranges.  
Should you wish to use the products under conditions exceeding the specifications, please consult us or our representatives.
6. We assume no responsibility for damage or loss due to abnormal use.
7. All rights reserved. No part of this datasheet may be copied or reproduced without the prior permission of TOREX SEMICONDUCTOR LTD.

**TOREX SEMICONDUCTOR LTD.**