

# High Temperature Silicon Carbide Power Schottky Diode

V <sub>RRM</sub>	=	1200 V
<b>V</b> <sub>F</sub>	=	1.6 V
I <sub>F</sub>	=	10 A
Qc	=	95 nC

### **Features**

- 1200 V Schottky rectifier
- 250 °C maximum operating temperature
- Electrically isolated base-plate
- Zero reverse recovery charge
- · Superior surge current capability
- Positive temperature coefficient of V<sub>F</sub>
- Temperature independent switching behavior
- Lowest figure of merit Qc/IF
- Available screened to Mil-PRF-19500

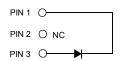
# **Advantages**

- High temperature operation
- Improved circuit efficiency (Lower overall cost)
- · Low switching losses
- · Ease of paralleling devices without thermal runaway
- Smaller heat sink requirements
- Industry's lowest reverse recovery charge
- Industry's lowest device capacitance
- Ideal for output switching of power supplies
- Best in class reverse leakage current at operating temperature

# **Package**

RoHS Compliant





TO - 257 (Isolated Base-plate Hermetic Package)

### **Applications**

- Down Hole Oil Drilling, Geothermal Instrumentation
- High Temperature DC/DC Converters
- High Temperature Motor and Servo Drives
- High Temperature Inverters
- High Temperature Actuator Control
- Military Power Supplies

# Maximum Ratings at T<sub>i</sub> = 250 °C, unless otherwise specified

Parameter	Symbol	Conditions	Values	Unit
Repetitive peak reverse voltage	$V_{RRM}$		1200	V
Continuous forward current	l <sub>F</sub>	T <sub>C</sub> ≤ 225 °C	9.4	Α
RMS forward current	I <sub>F(RMS)</sub>	T <sub>C</sub> ≤ 225 °C	16	Α
Surge non-repetitive forward current, Half Sine Wave	I <sub>F,SM</sub>	$T_C$ = 25 °C, $t_P$ = 10 ms	45	Α
Non-repetitive peak forward current	$I_{F,max}$	$T_C = 25 ^{\circ}\text{C},  t_P = 10 \mu\text{s}$	tbd	Α
<sup>2</sup> t value	∫i² dt	$T_C$ = 25 °C, $t_P$ = 10 ms	tbd	A <sup>2</sup> S
Power dissipation	P <sub>tot</sub>	T <sub>C</sub> = 25 °C	208	W
Operating and storage temperature	T <sub>i</sub> , T <sub>sta</sub>		-55 to 250	°C

# Electrical Characteristics at $T_j$ = 250 °C, unless otherwise specified

Parameter	O. mahad	Conditions m			Values		Limit
	Symbol			min.	typ.	max.	Unit
Diode forward voltage	$V_{F}$	$I_F = 10 A, T_j =$	I <sub>F</sub> = 10 A, T <sub>j</sub> = 25 °C		1.6		V
	٧F	I <sub>F</sub> = 10 A, T <sub>j</sub> = 210 °C			2.3		
Reverse current	1	V <sub>R</sub> = 1200 V, T <sub>j</sub> = 25 °C		1.2	20	μΑ	
	I <sub>R</sub>	$V_R = 1200 \text{ V}, T_i = 250 ^{\circ}\text{C}$		56	300		
Total capacitive charge	0	V <sub>R</sub> = 400 V			58		nC
	$Q_{C}$	$I_F \le I_{F,MAX}$ $V_R =$	$V_{R} = 960 \text{ V}$		95		IIC
Switching time	4	dl <sub>F</sub> /dt = 200 A/μs T <sub>i</sub> = 210 °C	V <sub>R</sub> = 400 V		< 49	20	
	ts	V <sub>R</sub> = 960 V			\ 49		ns
		$V_R = 1 \text{ V, f} = 1 \text{ MHz, T}_j = 25 ^{\circ}\text{C}$		884			
Total capacitance	С	$V_R = 400 \text{ V}, f = 1 \text{ MHz}, T_j = 25 ^{\circ}\text{C}$		79		pF	
		V <sub>R</sub> = 1000 V, f = 1 MH	Iz, T <sub>j</sub> = 25 °C		63		

# **Thermal Characteristics**

Thermal resistance, junction - case	$R_{thJC}$	1.08	°C/W

### **Mechanical Properties**

Mounting torque	M	0.6	Nm



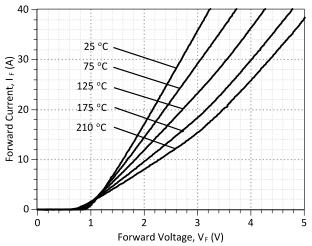


Figure 1: Typical Forward Characteristics

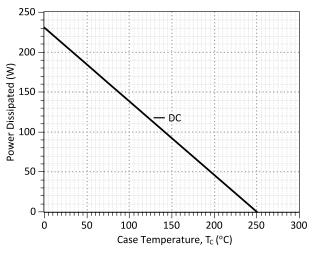


Figure 3: Power Derating Curve

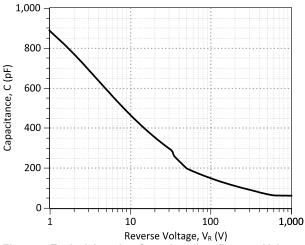


Figure 5: Typical Junction Capacitance vs Reverse Voltage Characteristics

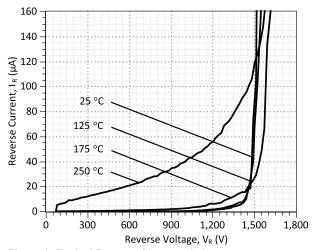


Figure 2: Typical Reverse Characteristics

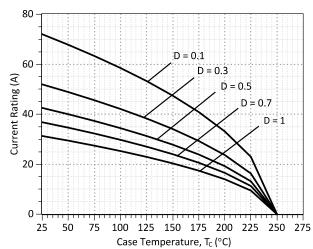


Figure 4: Current Derating Curves (D =  $t_P/T$ ,  $t_P$ = 400  $\mu$ s) (Considering worst case  $Z_{th}$  conditions )

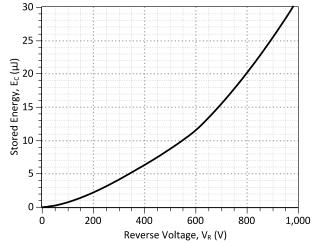


Figure 6: Typical Switching Energy vs Reverse Voltage Characteristics



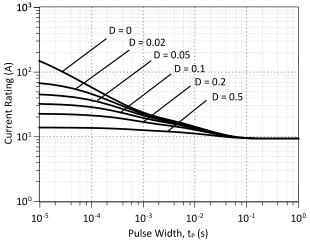


Figure 7: Current vs Pulse Duration Curves at  $T_c$  = 225 °C

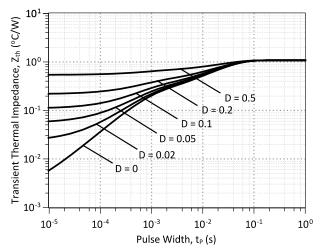
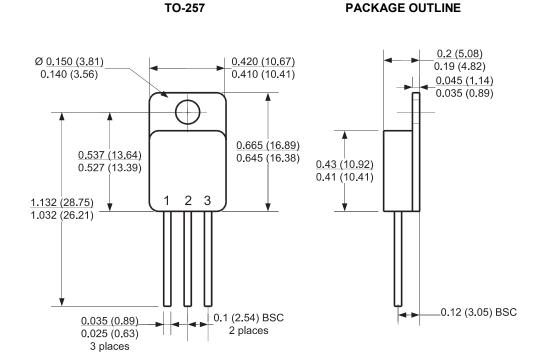


Figure 8: Transient Thermal Impedance

# **Package Dimensions:**



- 1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.
  2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS



Revision History				
Date	Revision	Comments	Supersedes	
2012/04/24	0	Initial release		

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### **SPICE Model Parameters**

Copy the following code into a SPICE software program for simulation of the 1N8028-GA device.

```
MODEL OF GeneSiC Semiconductor Inc.
    $Revision: 1.0
    $Date: 05-SEP-2013
    GeneSiC Semiconductor Inc.
    43670 Trade Center Place Ste. 155
    Dulles, VA 20166
    httphttp://www.genesicsemi.com/index.php/sic-products/schottky
   COPYRIGHT (C) 2013 GeneSiC Semiconductor Inc.
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* These models are provided "AS IS, WHERE IS, AND WITH NO WARRANTY
* OF ANY KIND EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED
* TO ANY IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A
* PARTICULAR PURPOSE."
* Models accurate up to 2 times rated drain current.
* Start of 1N8028-GA SPICE Model
.SUBCKT 1N8028 ANODE KATHODE
D1 ANODE KATHODE 1N8028 25C; Call the Schottky Diode Model
D2 ANODE KATHODE 1N8028 PIN; Call the PiN Diode Model
.MODEL 1N8028 25C D
     1.74E-13
+ IS
                        RS
                                  0.05105
        0.005
+ TRS1
                                  1.68E-5
                        TRS2
+ N
         1.2637323
                        IKF
                                  1.884319
         1.2
                        XTI
+ EG
+ CJO
        1.15E-09
                                   0.44
                        VJ
+ M
         1.5
                        FC
                                   0.5
        1.00E-10
1.00E-03
+ TT
                        BV
                                   1500
+ IBV
                                  1200
                        VPK
+ IAVE
         20
                                   SiC Schottky
                         TYPE
     GeneSiC_Semiconductor
+ MFG
.MODEL 1N8028 PIN D
      5.15E-15
+ IS
                        RS
                                  0.2
+ N
         3.1605
                                  0.00055844
                        IKF
+ EG
         3.23
                        XTI
                                   3
         0.5
                        TT
+ FC
                                   0
+ BV
         1500
                        IBV
                                   1.00E-03
+ VPK
         1200
                        IAVE
                                   20
+ TYPE SiC_PiN
.ENDS
```

\* End of 1N8028-GA SPICE Model