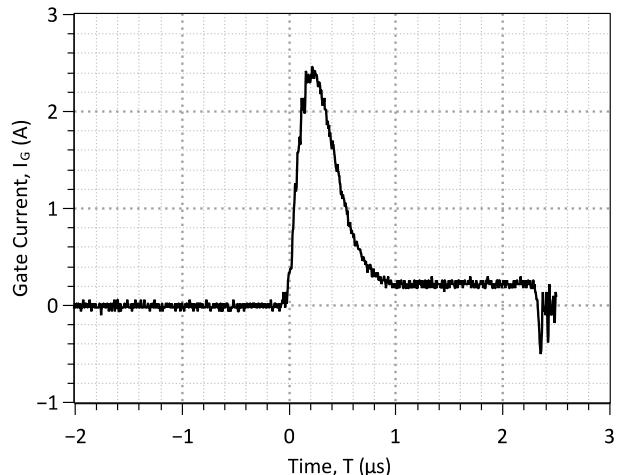
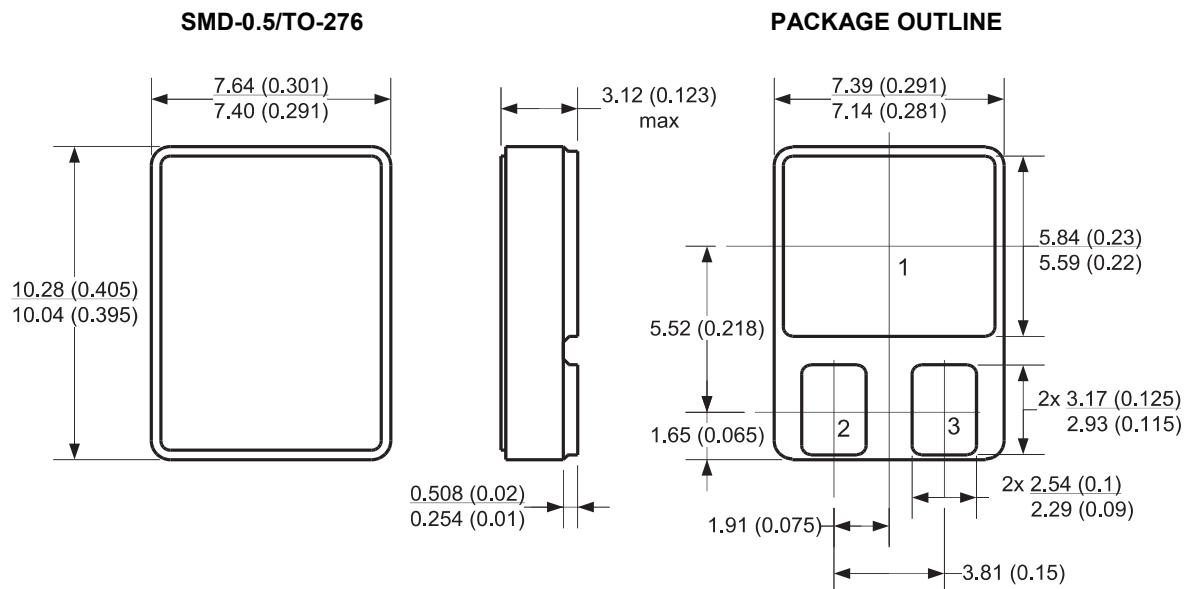


**Figure 9: Typical Turn Off Energy Losses and Switching Times vs. Temperature**



**Figure 10: Typical Gate-Source Switching Waveforms**

### Package Dimensions:



#### NOTE

1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.
2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS

<b>Revision History</b>			
Date	Revision	Comments	Supersedes
2012/08/24	0	Initial release	

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## SPICE Model Parameters

Copy the following code into a SPICE software program for simulation of the 2N7636-GA device.

```
* MODEL OF GeneSiC Semiconductor Inc.  
*  
* $Revision: 1.0      $  
* $Date: 06-SEP-2013 $  
*  
* GeneSiC Semiconductor Inc.  
* 43670 Trade Center Place Ste. 155  
* Dulles, VA 20166  
* http://www.genesicsemi.com/index.php/sic-products/sjt  
*  
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* ALL RIGHTS RESERVED  
*  
* These models are provided "AS IS, WHERE IS, AND WITH NO WARRANTY  
* OF ANY KIND EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED  
* TO ANY IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A  
* PARTICULAR PURPOSE."  
* Models accurate up to 2 times rated drain current.  
*  
.model 2N7636 NPN  
+ IS      1.22E-47  
+ ISE     3.91E-27  
+ EG      3.23  
+ BF      110  
+ BR      0.55  
+ IKF     999  
+ NF      1  
+ NE      2.022  
+ RB      0.26  
+ RE      0.231  
+ RC      0.16  
+ CJC     1.37E-10  
+ VJC     3.173990516  
+ MJC     0.436428533  
+ CJE     3.36E-10  
+ VJE     2.944816511  
+ MJE     0.493905327  
+ XTI     3  
+ XTB     -0.45  
+ TRC1    1.50E-02  
+ VCEO    800  
+ ICRATING 4  
+ MFG     GeneSiC_Semiconductor  
*  
* End of 2N7636-GA SPICE Model
```