Date code

500 mW 2. Dielectric strength; 500 VDC 1 minute

Status

REVISION -

06/12/92 TS

REVISION A

REFORMATTED

05/10/96 TS

REVISION B

FLIPPED RADII & CLARIFIED

MARKING

06/24/05 YS

REVISION C

RoHS COMPLIANT

11/03/05 MP

3. Insulation resistance; 10.000 MΩ MIN @ 500 VDC

4. Turns ratio;

(1-2):(3-4):(5-6)=1:1:1:5%

5. Primary open circuit inductance; 50 μ H MIN @ 1 KHz, 40 mV (1-2)

6. Primary ET-constant $5.2V - \mu s$ MIN

7. Rise time: 5.6 ns MAX

8. Interwinding capacitance between Primary and Secondary; 18.0 PF MAX @ 100 KHz

9. Primary leakage inductance with shorted Secondary; $0.5~\mu\text{H}$ MAX @ 100 KHz

10. DC Resistance;

PRI

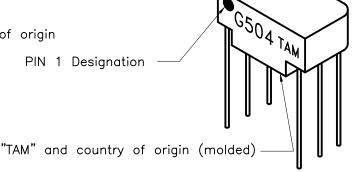
Primary $(1-2) 1.3 \Omega MAX$ Secondary (3-4) 1.3 Ω MAX

Tertiary (5-6) 1.3Ω MAX B. Marking;

G504, TAM, date code and country of origin

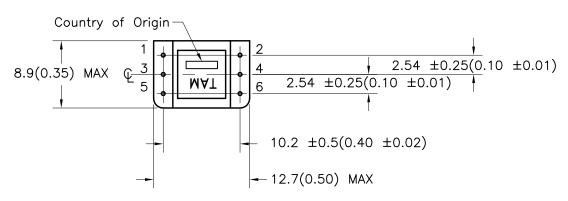
C. Schematic diagram

10-



<u>SEC</u> 30 04 **TERT** ∘6

D. Mechanical Specification G504 TAM 6.35(0.25) MAX Ø 0.5 (0.02)-(Solderable 24 AWG) $9.5 \pm 2.5(0.374 \pm 0.100)$



PREPARED BY:

K. BRENNAN

ENGINEER: CONTROL NO. REV **PULSE** P-A1-10630 G504 **TRANSFORMER** M. PITCHAI ACAD\G-SER\A1106301.DWG QUALITY CONTROL: CONTENTS OF THIS DRAWING ARE SUBJECT TO CHANGE WITHOUT PRIOR NOTICE TAMURA CORPORATION OF AMERICA MODEL SPECIFICATION 43352 BUSINESS PARK DRIVE, TEMECULA, CA. 92590-6624 (951) 699-1270 FAX 9516769482 T. CLEM DIM: mm(In) SCL: 2/1 SH: 1 0F

APPROVED:

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