PAMIRA

*020

1

Date Code

10

White dot

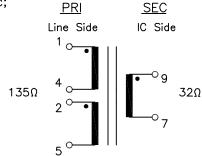
indicates

Pin 1

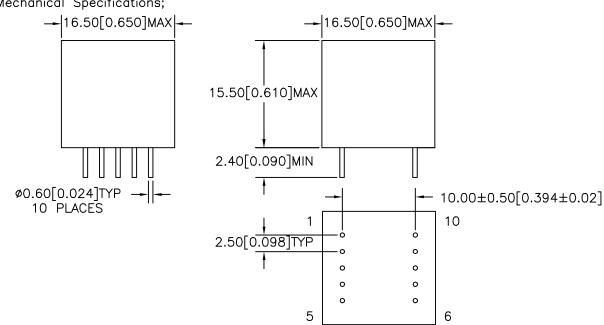
REV. Status

PRELIMINARY 02/03/00 MP HDSL transformer designed for use with Brooktree HDSL IC chips BT8921/BT8970 at 1168kbps rates

- A. Electrical Specifications (@ 25°C)
 - 1. Primary (Line side) Impedance; 135Ω
 - 2. Secondary (IC side) Impedance; 32Ω
 - 3. Longitudinal Balance; 50dBm MIN @ 40KHz TO 300KHz, 0dBm (Per ITU method pin 7 grounded)
 - 4. Frequency Response; ±0.1dB @ 33KHz to 110KHz, 0dBm
 - 5. Insertion Loss; 0.5dB MAX @ 40KHz, 0dBm
 - 6. Total Harmonic Distortion; −70dB MAX @ 20KHz, 4.5Vp−p
 - 7. Return Loss; 16.5dB MIN @ 40KHz to 300KHz, 0dBm
 - 8. Primary Open Circuit Inductance; 2.0mH ±6% @ 10KHz, 0.1Vrms Measured (1-5) with 2 and 4 shorted
 - Leakage Inductance; 11 μ H MAX @ 100KHz, 0.1Vrms Measured (1-5) with 2 & 4, and 7 & 9 shorted
 - 10. DC Current; 160mA
 - 11. DC Resistance; $(1-4)+(2-5)=2.50\Omega$ MAX $(7-9)=1.00\Omega$ MAX
 - 12. Turns Ratio; $(1-4):(2-5)=1:1.00\pm2\%$ $(7-9):(1-5)=1:2.00\pm2\%$
 - 2 & 4 shorted 13. Dielectric Strength; 1875Vrms 1 second @ Pri to Sec
- B. Marking; TTC-4020, TAMURA, date code and country of origin
- C. Safety; Complies with UL1950 3rd Edition and IEC950
- D. Schematic;



E. Mechanical Specifications;



PREPARED BY:

D. Rund

D. Italia	1		
ENGINEER: M. Pitchai	DWG CONTROL NO. REV P-A1-12348 ACAD\TTC\A1123481.DWG PRE	HDSL TRANSFORMER	TTC-4020
QUALITY CONTROL:	ICONTENTS OF THIS DRAWING ARE	TAMURA CORPORATION OF AMERICA	MODEL SPECIFICATION
D. Kelley	PRIOR NOTICE	43352 BUSINESS PARK DRIVE, TEMECULA, CA. 92590-6624 (909) 699-1270 FAX 9096769482	DIM: mm(In) SCL: 2/1 SH: 1 0F 1
APPROVED:	DECEDETARY MOTION THE DRAWING D	DINT OF POOLINENT AND CHRIEGE MATTER DICOLOCER HE	DEIN ARE RECEDENTARY ITEMS TO WILLOW TANKER

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