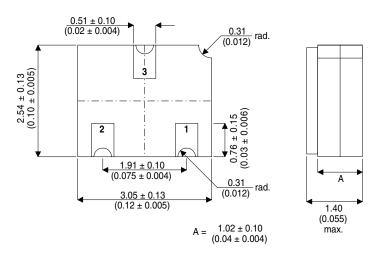
### 2N7000CSM



MECHANICAL DATA Dimensions in mm (inches)



### SOT23 CERAMIC (LCC1 PACKAGE)

#### **Underside View**

PAD 1 – Gate PAD 2 – Source PAD 3 – Drain

### ABSOLUTE MAXIMUM RATINGS (T<sub>CASE</sub> = 25°C unless otherwise stated)

			,
V <sub>DS</sub>	Drain – Source Voltage		60V
V <sub>GS</sub>	Gate – Source Voltage		±40V
I <sub>D</sub>	Drain Current	@ T <sub>CASE</sub> = 25°C	200mA
I <sub>DM</sub>	Pulsed Drain Current *		500mA
P <sub>D</sub>	Power Dissipation	@ T <sub>CASE</sub> = 25°C	300mW
Тj	Operating Junction Temperature Range		–55 to 150°C
T <sub>stg</sub>	Storage Temperature Range		–55 to 150°C

\* Pulse width limited by maximum junction temperature.

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# MOS TRANSISTOR

# • V<sub>(BR)DSS</sub> = 60V

- $RDS_{(ON)} = 5\Omega$
- I<sub>D</sub> = 200mA
- Hermetic Ceramic Surface Mount
  package

N-CHANNEL ENHANCEMENT MODE

Screening Options Available

## 2N7000CSM



### ELECTRICAL CHARACTERISTICS (T<sub>CASE</sub> = 25°C unless otherwise stated)

	Parameter	Test Co	Test Conditions		Тур.	Max.	Unit				
	STATIC CHARACTERISTICS										
V <sub>(BR)DSS</sub>	Drain – Source Breakdown Voltage	$V_{GS} = 0V$	I <sub>D</sub> = 10μA	60	70		v				
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS} = V_{GS}$	I <sub>D</sub> = 0.25mA	0.8		3.0	v				
I <sub>GSS</sub>	Gate – Body Leakage Current	$V_{GS} = \pm 20V$	$V_{DS} = 0V$			-10	nA				
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 60V	$V_{GS} = 0V$			1.0	μA				
			T <sub>CASE</sub> = 125°C			1.0	mA				
I <sub>D(on)*</sub>	On–State Drain Current	V <sub>DS</sub> ≥2V <sub>DS(ON</sub>	$V_{\rm GS} = 4.5 V$	75			mA				
R <sub>DS(on)*</sub>	Drain – Source On Resistance	V <sub>GS</sub> = 10V				5	Ω				
		I <sub>D</sub> = 0.5A	T <sub>CASE</sub> = 125°C			9					
V <sub>DS(on)*</sub>	Drain – Source On Voltage	$V_{GS} = 4.5V$	I <sub>D</sub> = 75mA			0.4	V				
		$V_{GS} = 10V$	I <sub>D</sub> = 0.5A			2.5					
9 <sub>FS*</sub>	Forward Transconductance	V <sub>GS</sub> = 10V	I <sub>D</sub> = 0.5A	100			ms				
	DYNAMIC CHARACTERISTICS										
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = 25V				60	pF				
C <sub>oss</sub>	Output Capacitance	$V_{GS} = 0V$	-			25					
C <sub>rss</sub>	Reverse Transfer Capacitance	f = 1MHz	-			5					
	SWITCHING CHARACTERISTICS										
t <sub>ON</sub>	Turn–On Time		V <sub>GEN</sub> = 10V			10	ne				
t <sub>OFF</sub>	Turn–Off Time	R <sub>L</sub> = 150Ω I <sub>D</sub> = 0.2A	$R_{G} = 25\Omega$			10	ns				

\* Pulse Test: PW = 80  $\mu s$  ,  $\delta \leq$  1%

	Parameter	Min.	Тур.	Max.	Unit
$R_{\thetaJA}$	Thermal Resistance, Junction to Ambient			416	°C/W

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