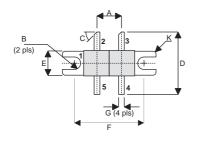
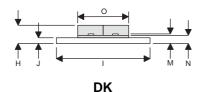


ROHS COMPLIANT METAL GATE RF SILICON FET

MECHANICAL DATA





PIN 1 SOURCE (COMMON) PIN 2 DRAIN 1

DRAIN 2 PIN 4 PIN₃ GATE 2

PIN 5 GATE 1

DIM	mm	Tol.	Inches	Tol.
Α	6.45	0.13	0.254	0.005
В	1.65R	0.13	0.065R	0.005
С	45°	5°	45°	5°
D	16.51	0.76	0.650	0.03
Е	6.47	0.13	0.255	0.005
F	18.41	0.13	0.725	0.005
G	1.52	0.13	0.060	0.005
Н	4.82	0.25	0.190	0.010
I	24.76	0.13	0.975	0.005
J	1.52	0.13	0.060	0.005
K	0.81R	0.13	0.032R	0.005
М	0.13	0.02	0.005	0.001
N	2.16	0.13	0.085	0.005

GOLD METALLISED MULTI-PURPOSE SILICON DMOS RF FET 125W - 28V - 400MHz**PUSH-PULL**

FEATURES

- SIMPLIFIED AMPLIFIER DESIGN
- SUITABLE FOR BROAD BAND APPLICATIONS
- LOW C_{rss}
- SIMPLE BIAS CIRCUITS
- LOW NOISE
- HIGH GAIN 13 dB MINIMUM

APPLICATIONS

 HF/VHF/UHF COMMUNICATIONS from 1 MHz to 400 MHz

ABSOLUTE MAXIMUM RATINGS (T_{case} = 25° C unless otherwise stated)

$\overline{P_D}$	Power Dissipation	350W
BV_DSS	Drain – Source Breakdown Voltage *	70V
BV_GSS	Gate – Source Breakdown Voltage *	±20V
I _{D(sat)}	Drain Current *	20A
T _{stg}	Storage Temperature	−65 to 150°C
Tj	Maximum Operating Junction Temperature	200°C

Per Side

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ELECTRICAL CHARACTERISTICS (T_{case} = 25° C unless otherwise stated)

Parameter		Test Conditions		Min.	Тур.	Max.	Unit
PER SIDE							
BV _{DSS}	Drain-Source	V _{GS} = 0	I _D = 100mA	70			V
	Breakdown Voltage	VGS = 0		70			, v
I _{DSS}	Zero Gate Voltage	V 20V	V _{GS} = 0			1	mΛ
	Drain Current	$V_{DS} = 28V$				4	mA
I _{GSS}	Gate Leakage Current	V _{GS} = 20V	V _{DS} = 0			1	μΑ
V _{GS(th)}	Gate Threshold Voltage *	I _D = 10mA	$V_{DS} = V_{GS}$	1		7	V
9 _{fs}	Forward Transconductance *	V _{DS} = 10V	I _D = 4A	3.2			S
		TOTA	L DEVICE				
G _{PS}	Common Source Power Gain	P _O = 125W		13			dB
η	Drain Efficiency	V _{DS} = 28V	I _{DQ} = 1.6A	50			%
VSWR	Load Mismatch Tolerance	f = 400MHz		20:1			_
		PE	R SIDE				
C _{iss}	Input Capacitance	$V_{DS} = 28V V_{OS}$	_{GS} = -5V f = 1MHz			240	pF
C _{oss}	Output Capacitance	$V_{DS} = 28V V_{C}$	_{GS} = 0			120	pF
C _{rss}	Reverse Transfer Capacitance	$V_{DS} = 28V$ V_{C}	GS = 0 f = 1MHz			10	pF

^{*} Pulse Test: Pulse Duration = 300 μs , Duty Cycle \leq 2%

HAZARDOUS MATERIAL WARNING

The ceramic portion of the device between leads and metal flange is beryllium oxide. Beryllium oxide dust is highly toxic and care must be taken during handling and mounting to avoid damage to this area.

THESE DEVICES MUST NEVER BE THROWN AWAY WITH GENERAL INDUSTRIAL OR DOMESTIC WASTE.

THERMAL DATA

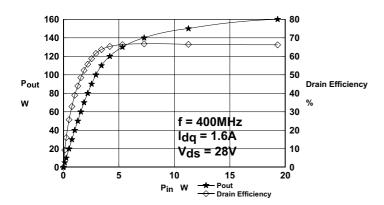
R _{THj-case}	Thermal Resistance Junction – Case	Max. 0.5° C / W
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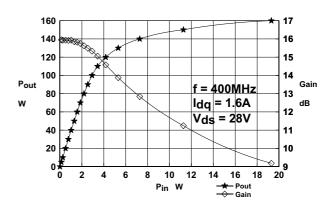


Figure 1.Power Output and Efficiency vs. Input Power

Figure 2.Power Output and Gain vs. Input Power

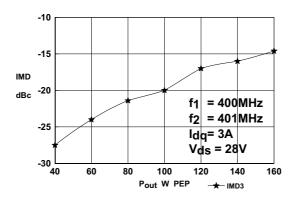


Figure 3
IMD vs.Power Output

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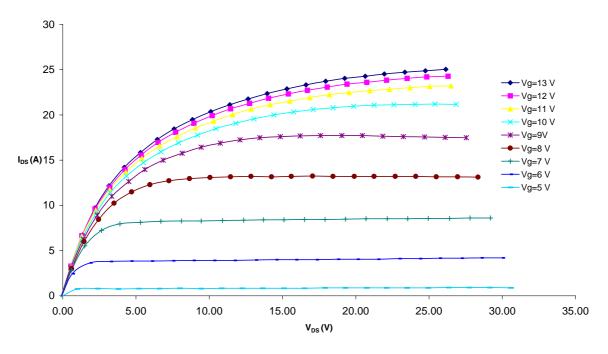


Figure 4 – Typical IV Characteristics.

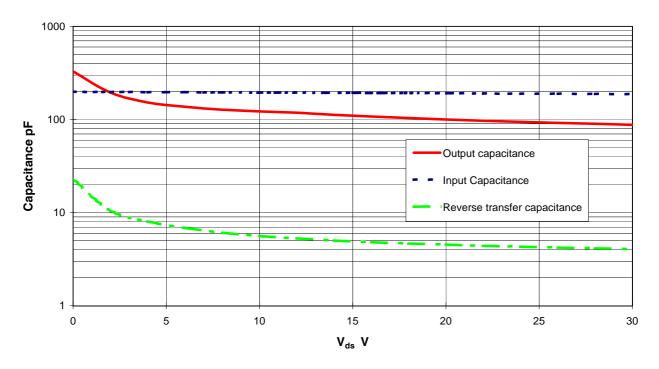


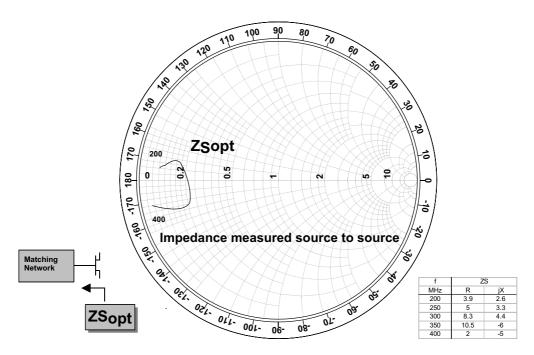
Figure 5 - Typical CV Characteristics.

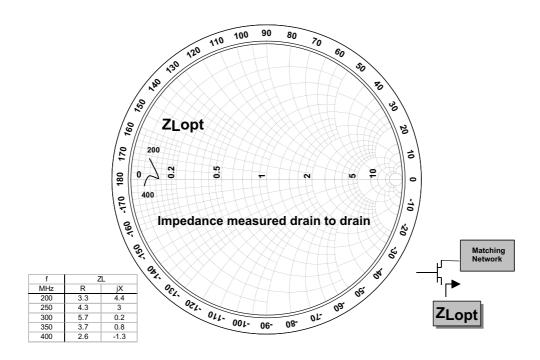
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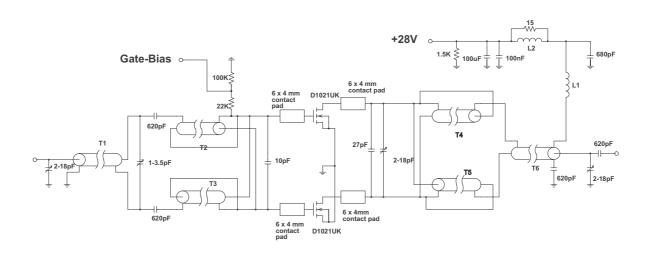




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400MHz Test Fixture

T1 11cm 50 Ohm UT47 semi-rigid coax on Siemens B62152A1X1 2 hole ferrite core

T2,3,4,5 9cm 15 Ohm UT85-15 semi-rigid coax

T6 9.7cm 50 Ohm UT85 semi-rigid coax

L1 7 Turns 19swg enamelled copper wire 3.5mm internal diameter

L2 5.5 Turns 19swg enamelled copper wire on Fair-rite FT50 ferrite core

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