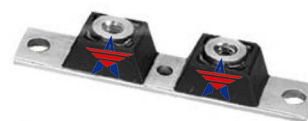


Features

- High Surge Capability
- Types up to 600 V V_{RRM}

Twin Tower Package



Maximum ratings, at $T_j = 25\text{ }^{\circ}\text{C}$, unless otherwise specified ("R" devices have leads reversed)

Parameter	Symbol	Conditions	MUR10005CT (R)	MUR10010CT (R)	MUR10020CT (R)	Unit
Repetitive peak reverse voltage	V_{RRM}		50	100	200	V
RMS reverse voltage	V_{RMS}		35	70	140	V
DC blocking voltage	V_{DC}		50	100	200	V
Continuous forward current	I_F	$T_C \leq 140\text{ }^{\circ}\text{C}$	100	100	100	A
Surge non-repetitive forward current, Half Sine Wave	$I_{F,SM}$	$T_C = 25\text{ }^{\circ}\text{C}$, $t_p = 8.3\text{ ms}$	400	400	400	A
Operating temperature	T_j		-40 to 175	-40 to 175	-40 to 175	$^{\circ}\text{C}$
Storage temperature	T_{stg}		-40 to 175	-40 to 175	-40 to 175	$^{\circ}\text{C}$

Electrical characteristics, at $T_j = 25\text{ }^{\circ}\text{C}$, unless otherwise specified

Parameter	Symbol	Conditions	MUR10005CT (R)	MUR10010CT (R)	MUR10020CT (R)	Unit
Diode forward voltage	V _F	I _F = 50 A, T _j = 25 °C	1.3	1.3	1.3	V
Reverse current	I _R	V _R = 50 V, T _j = 25 °C	25	25	25	μA
		V _R = 50 V, T _j = 125 °C	1	1	1	mA
Recovery Time						
Maximum reverse recovery time	T _{RR}	I _F =0.5 A, I _R =1.0 A, I _{RR} = 0.25 A	75	75	75	nS



America Semiconductor

MUR10005CT thru MUR10020CTR

