



MSL1

* Pb Free Part

Customer Name	Standard specification	TAIYO YUDEN Mobile Technology Co.,Ltd.	
System	Low Loss Diversity CDMA Rx (50/100ohm)	DATE	Nov. 12, 2012
Part Number	FAR-F5QB-881M50-P2BG	Version 1.0a	Final

Table 1. Electrical specifications

Passband: 869 ~ 894 MHz

Item	Condition (MHz)	Specification			Unit	Remarks
		Min.	Typ.	Max.		
Insertion Loss	869~894	-	1.5	2.2	dB	(*1)
Ripple	869~894	-	0.5	1.2	dB	
Attenuation	DC~824	50	65	-	dB	
	824~849	50	60	-	dB	
	914~960	25	35	-	dB	
	960~2000	40	50	-	dB	
	2000~6000	30	48	-	dB	
VSWR (Input)	869~894	-	1.5	2.0	-	
VSWR (Output)	869~894	-	1.6	2.0	-	
Amplitude balance ($ S_{21}/S_{31} $)	869~894	-1.0	-0.1/+0.2	+1.0	dB	
Phase balance ($(\angle S_{21} - \angle S_{31}) + 180$)	869~894	-10	-0.2/+3.6	+10	deg.	
Input Impedance	Unbalanced	50			Ohm	
Output Impedance	Balance	100			Ohm	
Operating Temperature		-30 ~ +85			°C	
Device size		1.1typ. x 0.9typ. x 0.5max.			mm	

(*1) Specification of insertion loss includes loss that comes from the test board.(0.05dB)

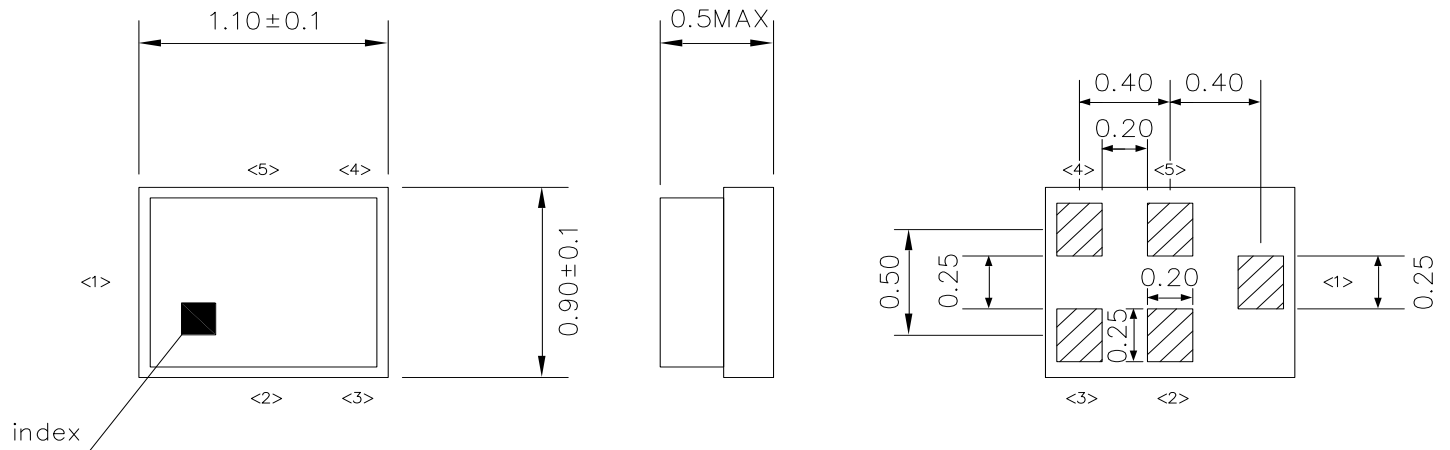


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Dimensions

Device size: 1.1yp. x 0.9typ. x 0.5max.

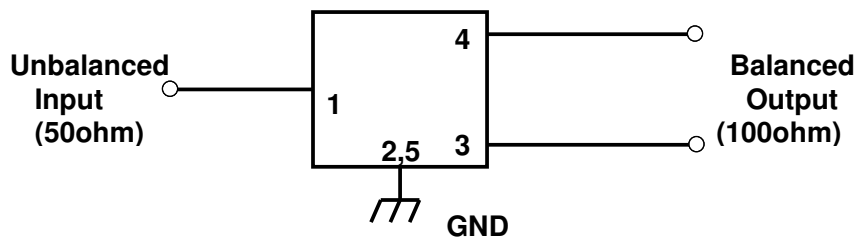


Unit : mm

Pin Configuration

Pin No.	Symbol	Function
1	IN	Unbalanced pin
2	GND	Ground
3	OUT	Balanced pin
4	OUT	Balanced pin
5	GND	Ground

Evaluation Circuit



1 ~ 5 : Pin No.



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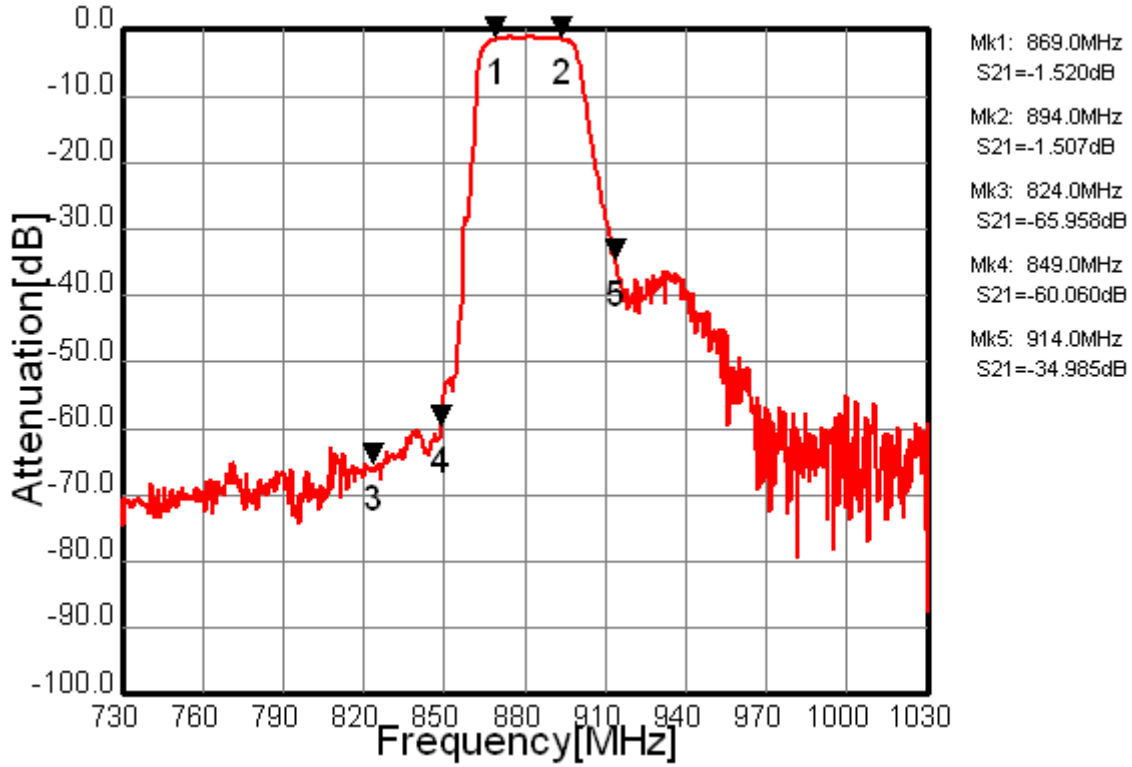


Fig.1 Pass-band Characteristics

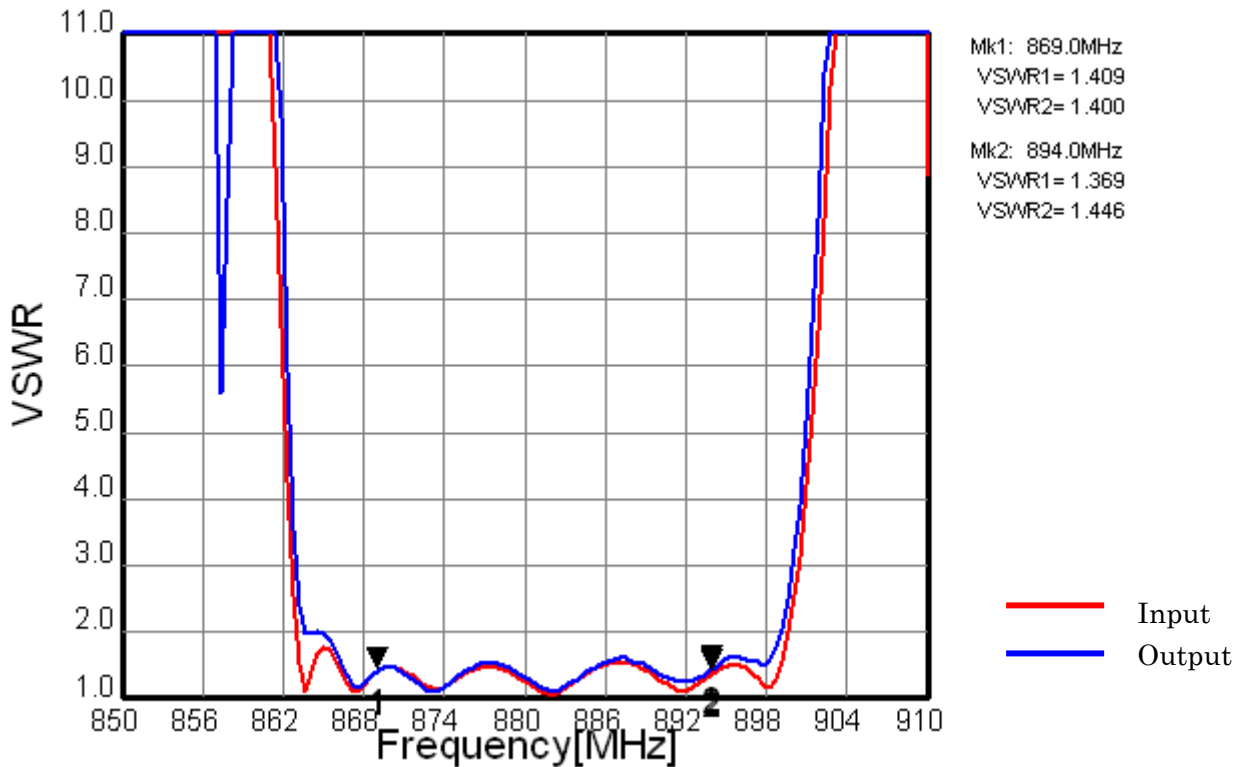


Fig.2 VSWR



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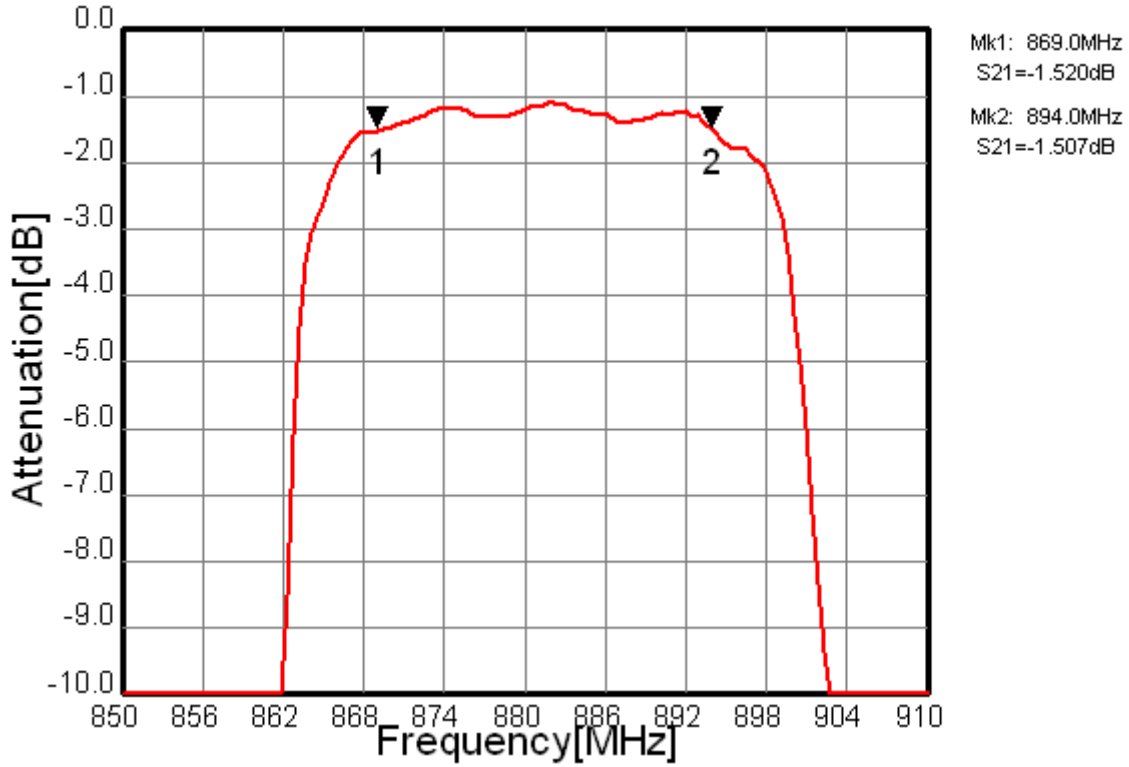


Fig.3 In-band Characteristics

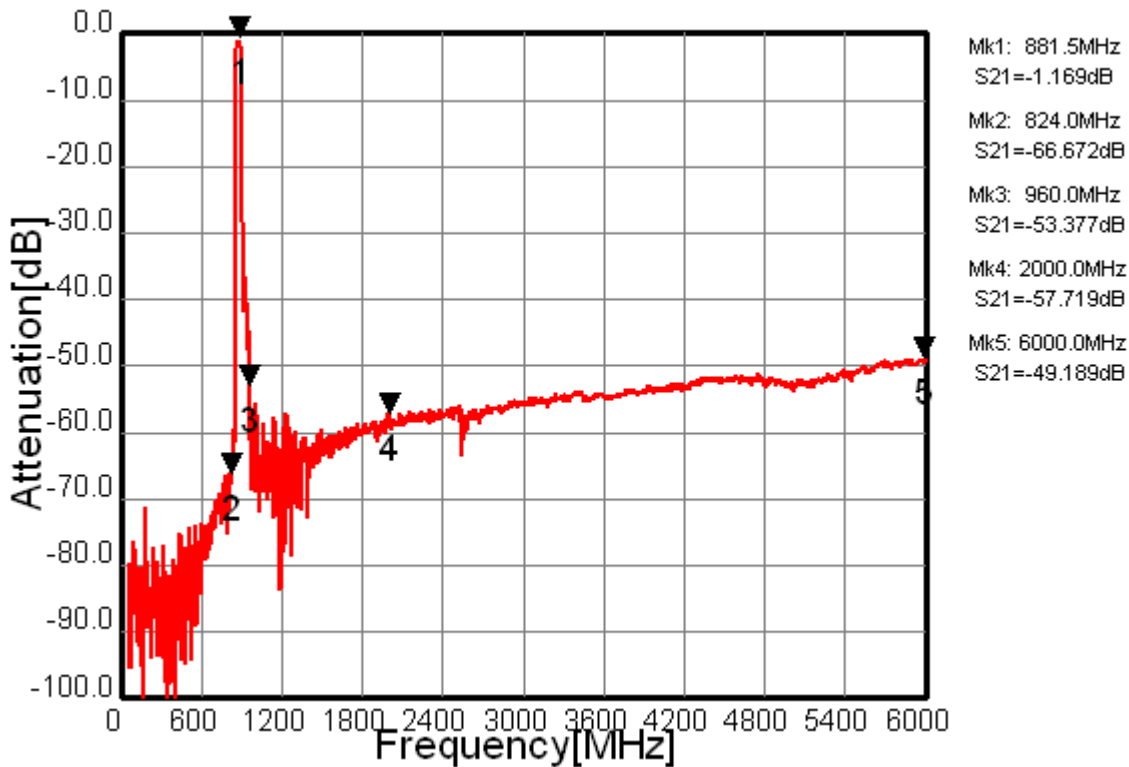


Fig.4 Wide-band Characteristics



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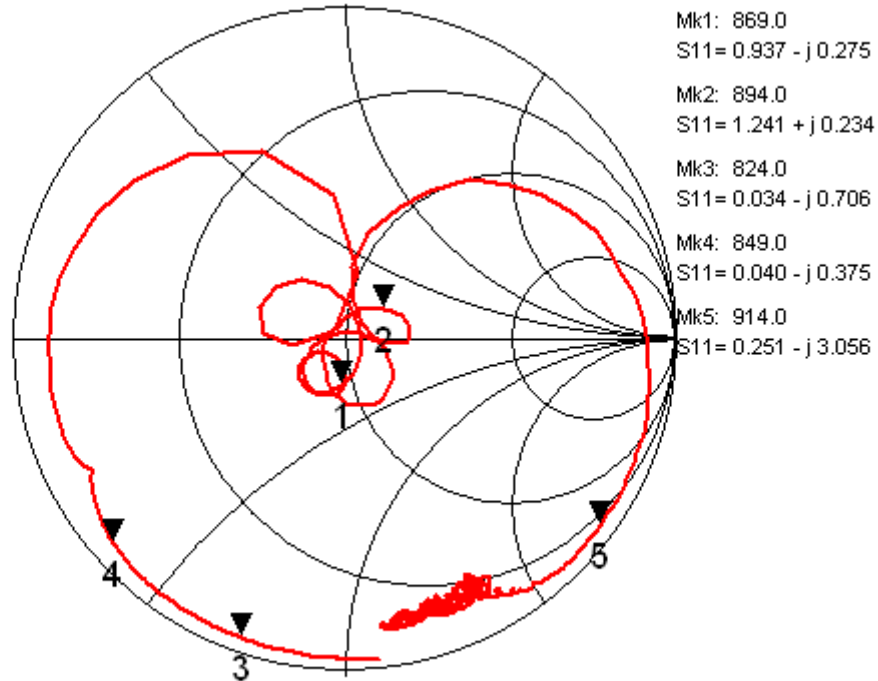


Fig.5 Input Impedance (Unbalance)

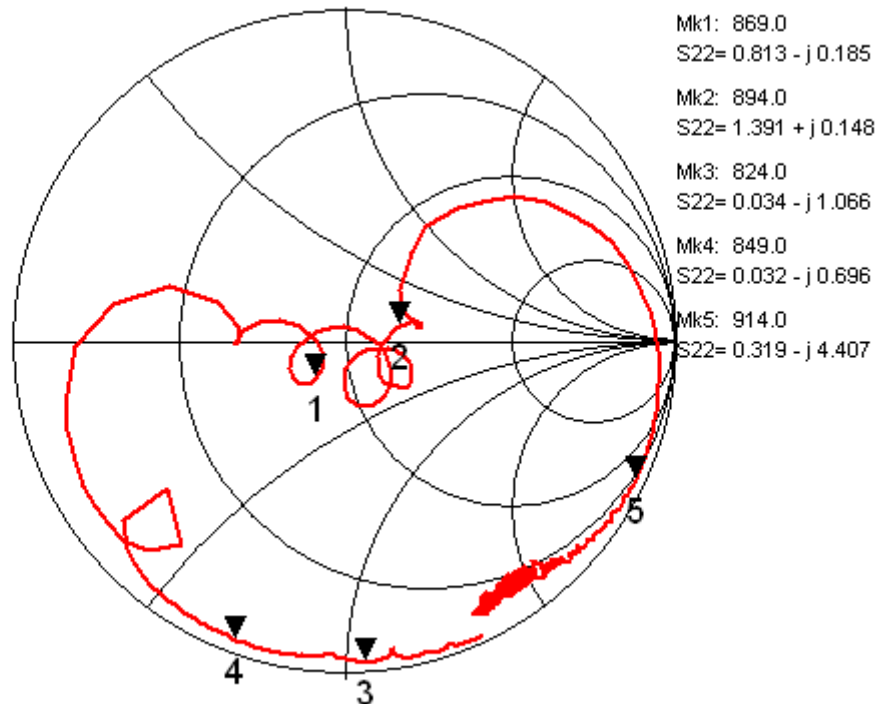


Fig.6 Output Impedance (Balance)



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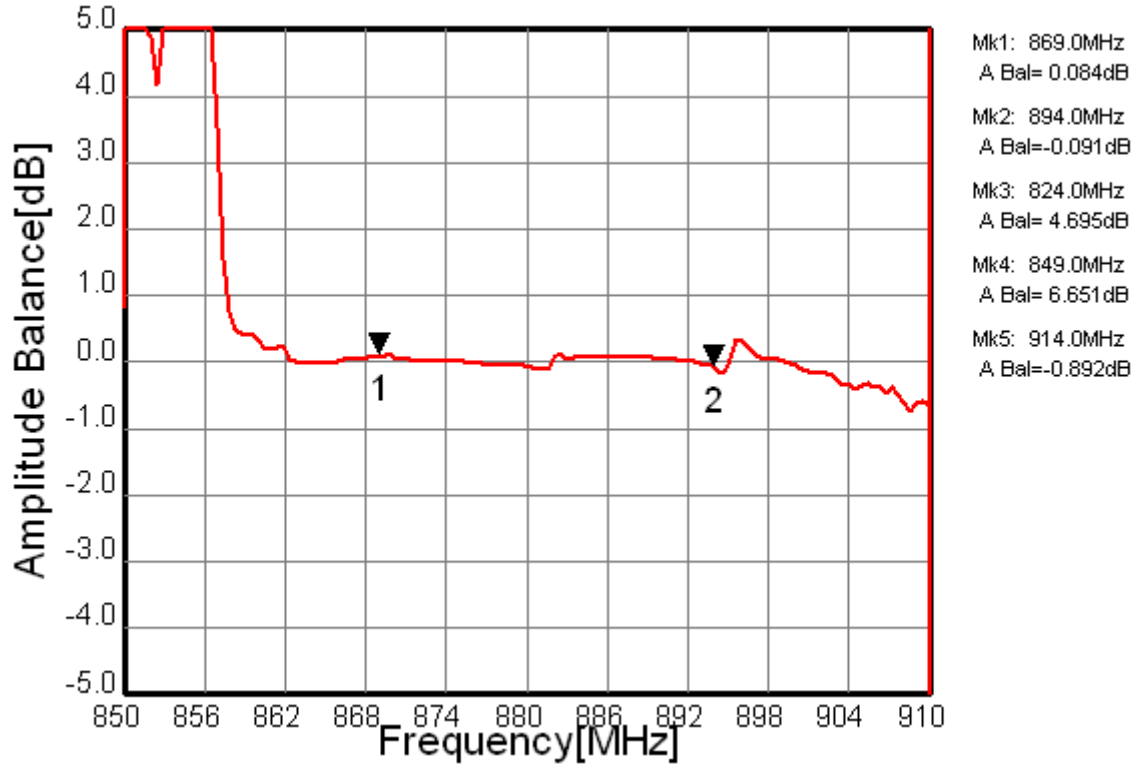


Fig.7 Amplitude Balance

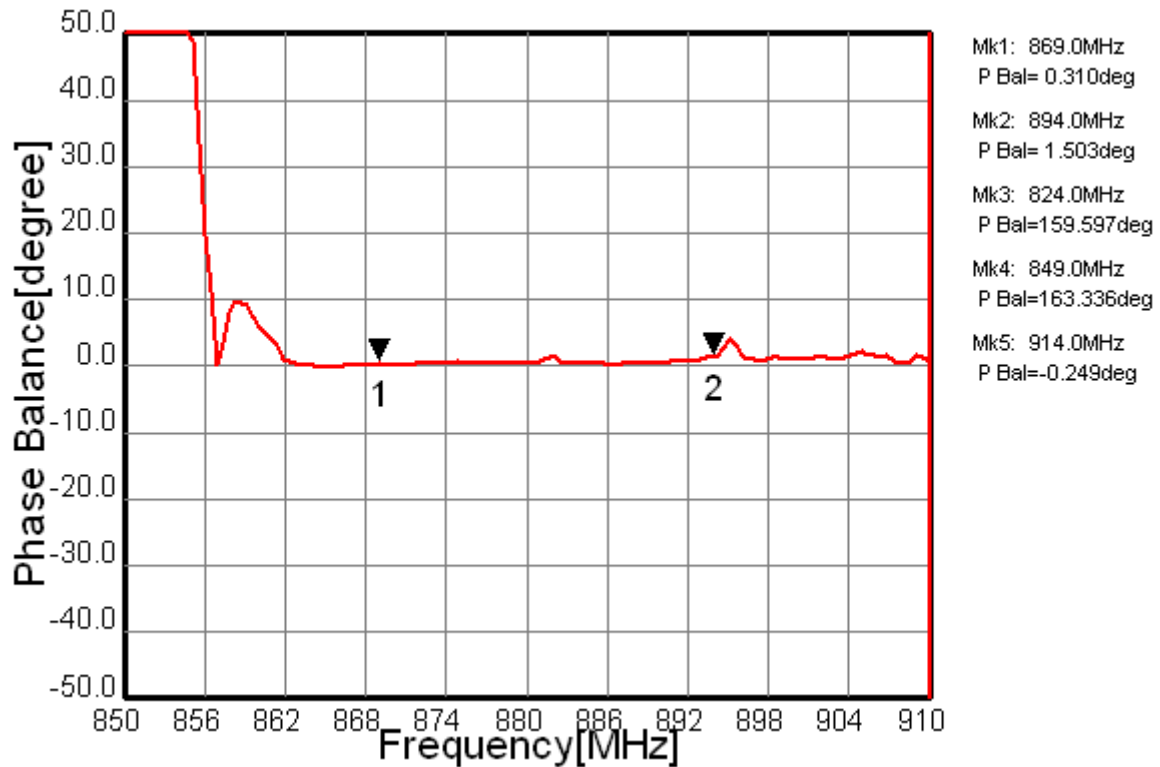


Fig.8 Phase Balance