

HALO SMD Single T-1/E-1 Line Interface Modules

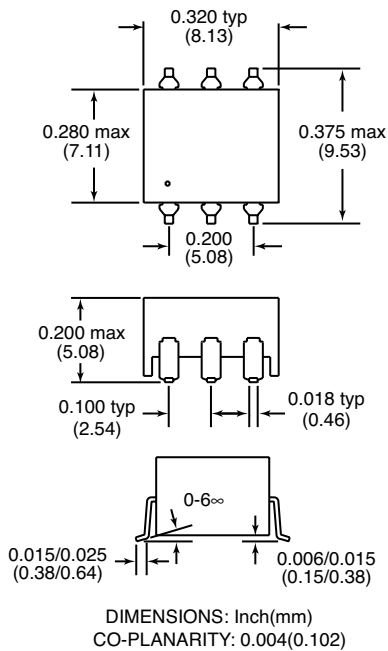
ELECTRONICS, INC.

Halo Electronics has developed this standard series of 6 pin SMD Line Interface Modules to meet the isolation and impedance matching requirements for T1, E1, CEPT, and PRI ISDN applications. UL60950 and EN60950 recognized. To select the correct Line Interface device, please contact your local [HALO Representative](#).

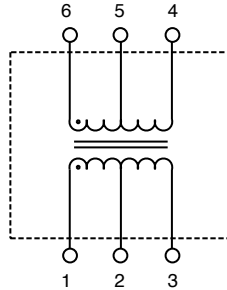


SMD Single T-1/E-1
Line Interface Modules

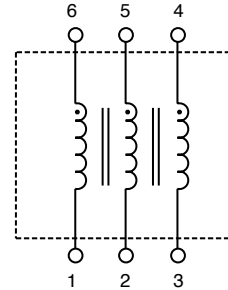
NS Package



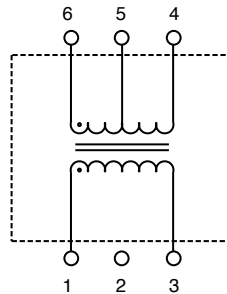
Schematic A



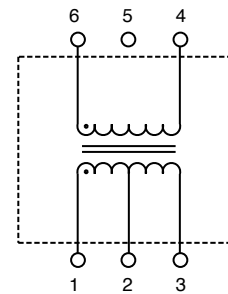
Schematic B



Schematic C



Schematic D



Electrical Specifications @ 25°C

Isolation Voltage: 1,500 Vrms (Except TG10-NS)
 Leakage Inductance: 0.4µH Max.
 Operating Temp: 0 to +70° C

Patented Construction

US Pat Nbrs: 5,656,985 6,297,721 B1
 6,297,720 B1 6,320,489 B1
 6,344,785 B1 6,662,431 B1

Part Number	Schematic	Turns Ratio PRI : SEC	PRI OCL (mH typ)	Cw/w (pF max)	DCR PRI (Ω max)	Primary Pins
TG02-1205NSRL	D	1CT:1	1.2	30	0.70	1-3
TG04-1205NSRL	A	1CT:1CT	1.2	30	0.70	1-3
TG08-1205NSRL	A	1CT:2CT	1.2	30	0.70	1-3
TG10-1205NSRL	B	1:1:1	1.2	30	0.70	3-4
TG15-1205NSRL	D	1:2CT	1.2	30	0.70	4-6
TG37-1205NSRL	C	1:1.14CT	1.2	30	0.70	1-3
TG38-1205NSRL	D	1:1.15CT	1.2	30	0.70	4-6
TG42-1205NSRL	A	1CT:1.41CT	1.2	30	0.70	1-3
TG70-1205NSRL	C	1:1.36CT	1.2	30	0.70	1-3
TG71-1205NSRL	C	1:2.3CT	1.2	30	0.70	1-3