Product data sheet

1. Product profile

1.1 General description

The IP4223CZ6 is designed to protect I/O lines sensitive to capacitive load, such as USB 2.0, ethernet, Digital Video Interface (DVI) and so on, from damage due to ElectroStatic Discharge (ESD). It incorporates four pairs of ultra low capacitance rail-to-rail ESD protection diodes plus a Zener diode to provide protection to signal and supply components from ESD voltages up to ± 8 kV contact discharge. Protection is supply voltage independent due to the rail-to-rail diodes being connected to the Zener diode. The device is encapsulated in a small 6-lead SOT457 (SC-74) Surface-Mounted Device (SMD) plastic package.

1.2 Features and benefits

- Pb-free, Restriction of Hazardous Substances (RoHS) compliant and free of halogen and antimony (Dark Green compliant)
- ESD protection up to ±8 kV contact discharge; IEC 61000-4-2, level 4 compliant
- Four pairs of ultra low input capacitance (C_(I/O-GND) = 1.5 pF) ESD rail-to-rail protection diodes
- Low voltage clamping due to integrated Zener diode
- Small 6-lead TSOP6 (SOT457) SMD package

1.3 Applications

- General-purpose downstream ESD protection for high-frequency analog signal ports and high-speed serial data transmission ports in:
 - Cellular phone and Personal Communication System (PCS) mobile handsets
 - ◆ PC/notebook USB 2.0/IEEE1394 ports
 - DVI
 - Cordless telephones
 - Wireless data (WAN/LAN) systems
 - Mobile Internet Devices (MID)
 - Portable Media Players (PMP)
 - High-Definition Multimedia Interface (HDMI)



Dual USB 2.0 integrated ESD protection

2. Pinning information

Table 1. Pinning

Table 1.	i iiiiiiiig			
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	I/O 1	ESD protection		
2	GND	ground	<u> </u>	6 5 4
3	I/O 2	ESD protection	0	
4	I/O 3	ESD protection	1 2 3	
5	V_P	supply voltage		
6	I/O 4	ESD protection		
				018aaa142

3. Ordering information

Table 2. Ordering information

Type number	Package		
	Name	Description	Version
IP4223CZ6	SC-74	plastic surface-mounted package (TSOP6); 6 leads	SOT457

4. Marking

Table 3. Marking codes

Type number	Marking code
IP4223CZ6	21

5. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{I}	input voltage		0	5.5	V
V_{ESD}	electrostatic discharge voltage	IEC 61000-4-2, level 4, contact; all pins	-8	+8	kV
T _{stg}	storage temperature		-55	+125	°C

6. Recommended operating conditions

Table 5. Operating conditions

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb}	ambient temperature		-40	-	+85	°C

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7. Characteristics

Table 6. Characteristics

 $T_{amb} = 25$ °C; unless otherwise specified.

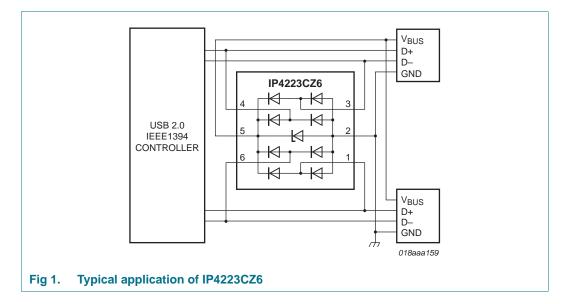
$ \begin{array}{c} C_{(I/O\text{-}GND)} & \text{input/output to} \\ \text{ground capacitance} & V_I = 0 \text{ V}; \text{f} = 1 \text{ MHz}; \\ V_P = 3 \text{ V} & & & & & & & & & & & & & & & & & & $	anno – •	-,					
$ \begin{array}{c} \text{ground capacitance} & V_P = 3 \text{ V} \\ \hline C_{(\text{zd-GND})} & \text{Zener diode to} & V_I = 0 \text{ V}; \text{ f} = 1 \text{ MHz}; \\ \text{ground capacitance} & V_P = 3 \text{ V} \\ \hline I_{RM} & \text{reverse leakage current} & V_I = 3 \text{ V} \\ \hline V_{BRzd} & \text{Zener diode breakdown} & I = 1 \text{ mA} \\ \hline V_{BRzd} & \text{Voltage} \\ \hline \end{array} \begin{array}{c} \text{[2]} & - & 40 & - & pF \\ \hline \end{array} \begin{array}{c} \text{[3]} & - & - & 100 & nA \\ \hline \end{array} $	Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	$C_{\text{(I/O-GND)}}$	•	'	[1] -	-	1.5	pF
V _{BRzd} Zener diode breakdown I = 1 mA [2] 6 - 9 V voltage	C _(zd-GND)		'	[2] _	40	-	pF
voltage	I_{RM}	reverse leakage current	$V_I = 3 V$	[3] _	-	100	nA
V _F forward voltage - 0.7 - V	V_{BRzd}		I = 1 mA	[2] 6	-	9	V
	V_{F}	forward voltage		-	0.7	-	V

^[1] Pins 1, 3, 4 and 6.

8. Application information

8.1 Universal serial bus 2.0 protection

The device is optimized to protect, for example, two USB 2.0 ports from ESD. Each device can protect both USB data lines and the V_{BUS} supply line. A typical application is shown in Figure 1.



^[2] Pin 5 to pin 2.

^[3] Pins 1, 3, 4 and 6 to ground.

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Package outline

Plastic surface-mounted package (TSOP6); 6 leads **SOT457** В = v M A 6 pin 1 index 3 ⊕ w M B detail X **DIMENSIONS** (mm are the original dimensions) UNIT Е Q Lp 0.40 1.1 0.26 3.1 1.7 3.0 0.6 0.33 0.95 0.2 0.2 0.1 mm 0.013 0.25 0.10 1.3 2.5 REFERENCES EUROPEAN PROJECTION OUTLINE **ISSUE DATE** VERSION IEC **JEDEC** JEITA

Package outline SOT457 (SC-74)

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-05-11-07

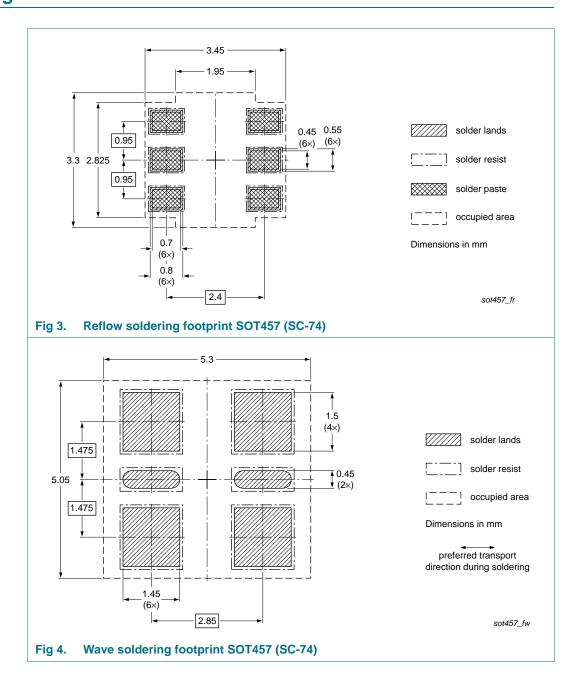
06-03-16

SC-74

SOT457

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10. Soldering



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11. Revision history

Table 7. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
IP4223CZ6 v.1	20111017	Product data sheet	-	-

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12. Legal information

12.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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