Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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DATA SHEET



MOS FIELD EFFECT TRANSISTOR

NP48N055ELE, NP48N055KLE NP48N055CLE, NP48N055DLE, NP48N055MLE, NP48N055NLE

SWITCHING N-CHANNEL POWER MOS FET

DESCRIPTION

These products are N-channel MOS Field Effect Transistors designed for high current switching applications.

ORDERING INFORMATION <R>

PART NUMBER	LEAD PLATING	PACKING	PACKAGE	
NP48N055ELE-E1-AY Note1, 2			TO 000 (MD 057.))	
NP48N055ELE-E2-AY Note1, 2	D 0 (Ti) T 000 (TO-263 (MP-25ZJ) typ. 1.4 g	
NP48N055KLE-E1-AY Note1	Pure Sn (Tin)	Tape 800 p/reel		
NP48N055KLE-E2-AY Note1			TO-263 (MP-25ZK) typ. 1.5 g	
NP48N055CLE-S12-AZ Note1, 2	Sn-Ag-Cu		TO-220 (MP-25) typ. 1.9 g	
NP48N055DLE-S12-AY Note1, 2		T by 50 /// by	TO-262 (MP-25 Fin Cut) typ. 1.8 g	
NP48N055MLE-S18-AY Note1	Pure Sn (Tin)	Tube 50 p/tube	TO-220 (MP-25K) typ. 1.9 g	
NP48N055NLE-S18-AY Note1			TO-262 (MP-25SK) typ. 1.8 g	

Notes 1. Pb-free (This product does not contain Pb in the external electrode.)

2. Not for new design

FEATURES

- Channel temperature 175 degree rated
- Super low on-state resistance

 $R_{DS(on)1}$ = 17 m Ω MAX. (Vgs = 10 V, ID = 24 A)

 $R_{DS(on)2}$ = 21 m Ω MAX. (VGS = 5 V, ID = 24 A)

 $R_{DS(on)3} = 24 \text{ m}\Omega$ MAX. (Vgs = 4.5 V, ID = 24 A)

• Low input capacitance

Ciss = 1970 pF TYP.

• Built-in gate protection diode





(TO-262)



(TO-263)



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ABSOLUTE MAXIMUM RATINGS ($T_A = 25$ °C)

Drain to Source Voltage (V _{GS} = 0 V)	VDSS	55	V
Gate to Source Voltage (V _{DS} = 0 V)	Vgss	±20	V
Drain Current (DC) (Tc = 25°C)	I _{D(DC)}	±48	Α
Drain Current (pulse) Note1	I _{D(pulse)}	±140	Α
Total Power Dissipation (T _A = 25°C)	PT	1.8	W
Total Power Dissipation (Tc = 25°C)	PT	85	W
Channel Temperature	Tch	175	°C
Storage Temperature	Tstg	-55 to +175	°C
Single Avalanche Current Note2	las	46/27/10	Α
Single Avalanche Energy Note2	Eas	2.1/73/100	mJ

Notes 1. PW \leq 10 μ s, Duty cycle \leq 1%

2. Starting T_{ch} = 25°C, R_G = 25 Ω , V_{GS} = 20 \rightarrow 0 V (see **Figure 4.**)

THERMAL RESISTANCE

Channel to Case Thermal Resistance	Rth(ch-C)	1.76	°C/W
Channel to Ambient Thermal Resistance	Rth(ch-A)	83.3	°C/W

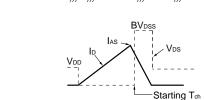


ELECTRICAL CHARACTERISTICS (TA = 25°C)

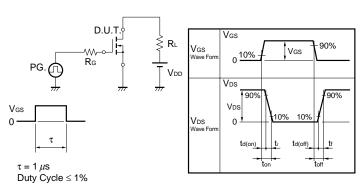
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V _{DS} = 55 V, V _{GS} = 0 V			10	μΑ
Gate Leakage Current	Igss	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$			±10	μΑ
Gate to Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu\text{A}$	1.5	2.0	2.5	٧
Forward Transfer Admittance	yfs	V _{DS} = 10 V, I _D = 24 A	13	25		S
Drain to Source On-state Resistance	RDS(on)1	V _{GS} = 10 V, I _D = 24 A		13	17	mΩ
	RDS(on)2	V _{GS} = 5 V, I _D = 24 A		16	21	mΩ
	RDS(on)3	V _{GS} = 4.5 V, I _D = 24 A		18	24	mΩ
Input Capacitance	Ciss	V _{DS} = 25 V,		1970	3000	pF
Output Capacitance	Coss	V _{GS} = 0 V,		250	380	pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		130	240	pF
Turn-on Delay Time	t _{d(on)}	V _{DD} = 28 V, I _D = 24 A,		17	38	ns
Rise Time	tr	V _{GS} = 10 V,		11	27	ns
Turn-off Delay Time	t _{d(off)}	$R_G = 1 \Omega$		54	110	ns
Fall Time	tf			9.3	23	ns
Total Gate Charge	Q _{G1}	V _{DD} = 44 V, V _{GS} = 10 V, I _D = 48 A		40	60	nC
	Q _{G2}	V _{DD} = 44 V,		21	32	nC
Gate to Source Charge	Qgs	V _{GS} = 5 V,		7		nC
Gate to Drain Charge	Q _{GD}	I _D = 48 A		10		nC
Body Diode Forward Voltage	V _{F(S-D)}	I _F = 48 A, V _{GS} = 0 V		1.0		V
Reverse Recovery Time	trr	IF = 48 A, VGS = 0 V,		40		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/μs		55		nC

TEST CIRCUIT 1 AVALANCHE CAPABILITY

$\begin{array}{c} \text{D.U.T.} \\ \text{Rg} = 25 \ \Omega \\ \text{Vgs} = 20 \rightarrow 0 \ \text{V} \end{array}$



TEST CIRCUIT 2 SWITCHING TIME



TEST CIRCUIT 3 GATE CHARGE

TYPICAL CHARACTERISTICS (TA = 25°C)

Figure1. DERATING FACTOR OF FORWARD BIAS SAFE OPERATING AREA

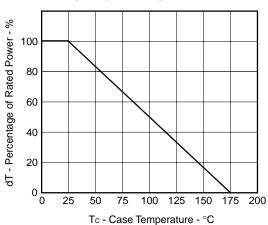


Figure 3. FORWARD BIAS SAFE OPERATING AREA

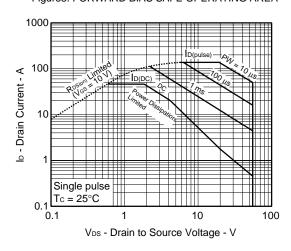


Figure2. TOTAL POWER DISSIPATION vs. CASE TEMPERATURE

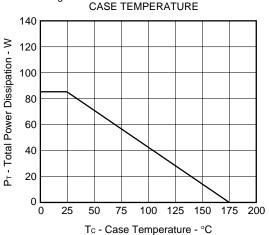


Figure 4. SINGLE AVALANCHE ENERGY DERATING FACTOR

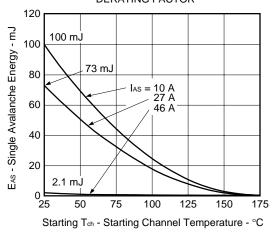


Figure 5. TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH

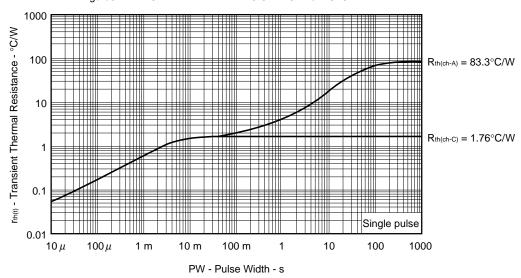


Figure 6. FORWARD TRANSFER CHARACTERISTICS

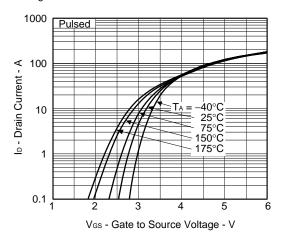
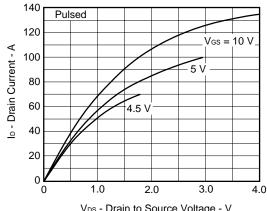


Figure 7. DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE Pulsed



V_{DS} - Drain to Source Voltage - V

Figure8. FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT

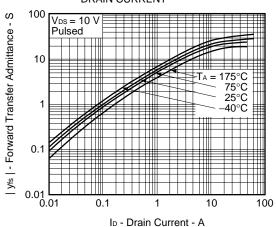


Figure9. DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE

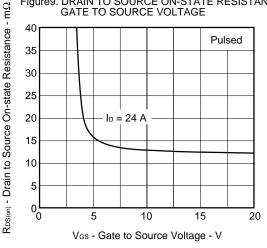


Figure 10. DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT

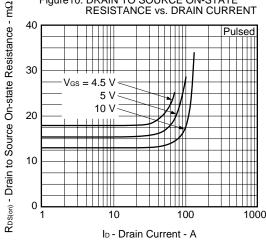
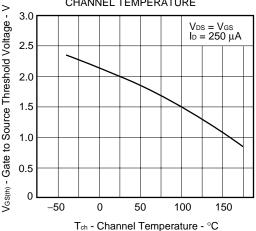
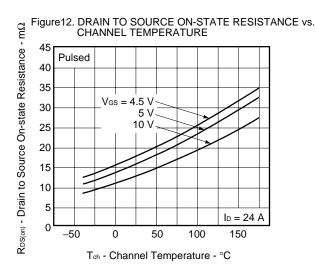
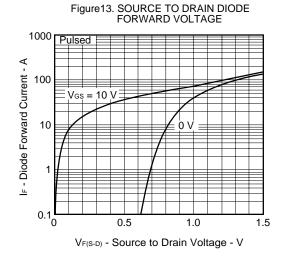
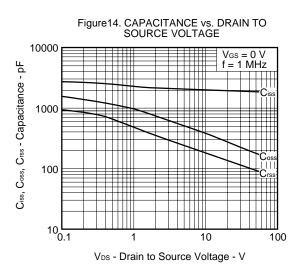


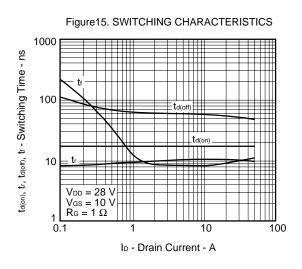
Figure 11. GATE TO SOURCE THRESHOLD VOLTAGE vs. CHANNEL TEMPERATURE

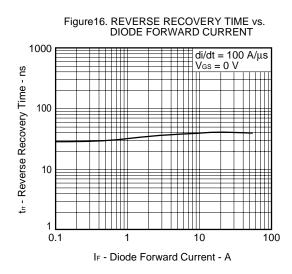


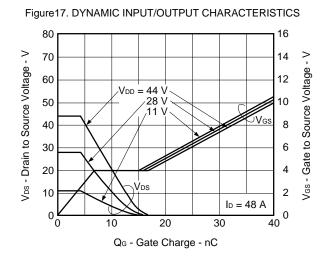




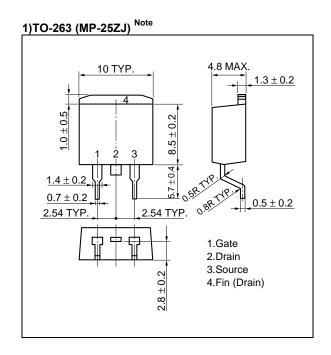


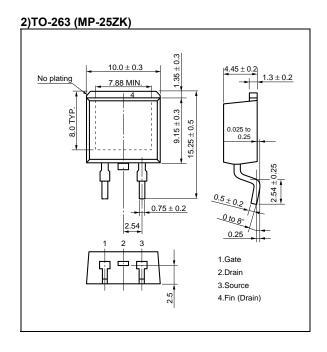


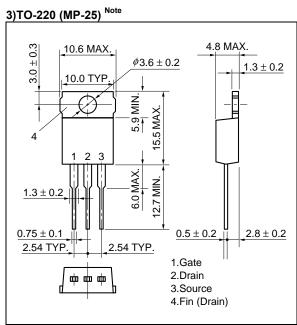


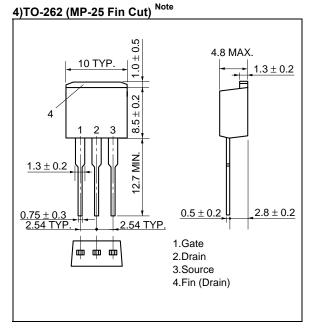


<R> PACKAGE DRAWINGS (Unit: mm)

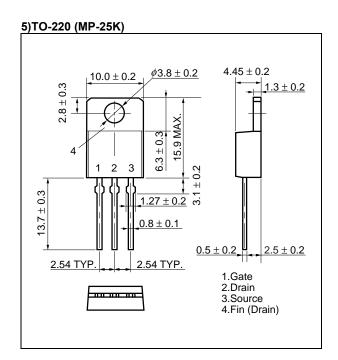


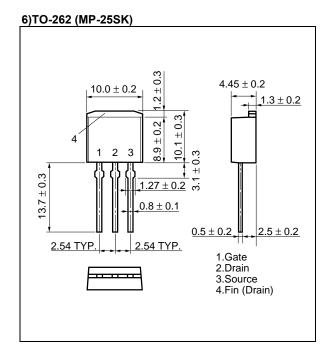




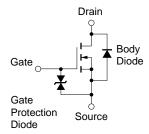


Note Not for new design





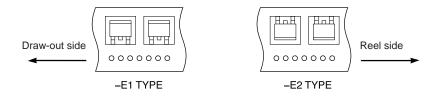
EQUIVALENT CIRCUIT



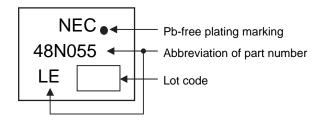
Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

<R> TAPE INFORMATION

There are two types (-E1, -E2) of taping depending on the direction of the device.



<R> MARKING INFORMATION



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These products should be soldered and mounted under the following recommended conditions.

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For technical information, see the following website.

Semiconductor Device Mount Manual (http://www.necel.com/pkg/en/mount/index.html)

Soldering Method	Soldering Conditions	Recommended Condition Symbol	
Infrared reflow	Maximum temperature (Package's surface temperature): 260°C or below		
MP-25ZJ, MP-25ZK	Time at maximum temperature: 10 seconds or less		
	Time of temperature higher than 220°C: 60 seconds or less		
	Preheating time at 160 to 180°C: 60 to 120 seconds	IR60-00-3	
	Maximum number of reflow processes: 3 times		
	Maximum chlorine content of rosin flux (percentage mass): 0.2% or less		
Wave soldering	Maximum temperature (Solder temperature): 260°C or below		
MP-25, MP-25K, MP-25SK,	Time: 10 seconds or less	THDWS	
MP-25 Fin Cut	Maximum chlorine content of rosin flux: 0.2% (wt.) or less		
Partial heating	Maximum temperature (Pin temperature): 350°C or below		
MP-25ZJ, MP-25ZK,	Time (per side of the device): 3 seconds or less	P350	
MP-25K, MP-25SK	Maximum chlorine content of rosin flux: 0.2% (wt.) or less		
Partial heating	Maximum temperature (Pin temperature): 300°C or below		
MP-25, MP-25 Fin Cut	Time (per side of the device): 3 seconds or less	P300	
	Maximum chlorine content of rosin flux: 0.2% (wt.) or less		

Caution Do not use different soldering methods together (except for partial heating).

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 - "Special": Transportation equipment (automobiles, trains, ships, etc.), traffic control systems, anti-disaster systems, anti-crime systems, safety equipment and medical equipment (not specifically designed for life support).
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