

DATA SHEET

BTA212X series D, E and F
Three quadrant triacs
guaranteed commutation

Product specification

June 2003



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BTA212X series D, E and F

GENERAL DESCRIPTION

Passivated guaranteed commutation triacs in a full pack, plastic envelope intended for use in motor control circuits or with other highly inductive loads. These devices balance the requirements of commutation performance and gate sensitivity. The "sensitive gate" E series and "logic level" D series are intended for interfacing with low power drivers, including micro controllers.

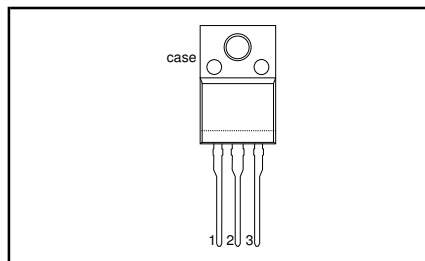
QUICK REFERENCE DATA

SYMBOL	PARAMETER	MAX.	MAX.	UNIT
V_{DRM}	Repetitive peak off-state voltages	600D	-	V
		600E	800E	
$I_{T(RMS)}$	RMS on-state current	12	12	A
I_{TSM}	Non-repetitive peak on-state current	95	95	A

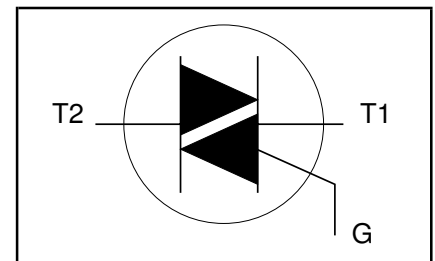
PINNING - SOT186A

PIN	DESCRIPTION
1	main terminal 1
2	main terminal 2
3	gate
case	isolated

PIN CONFIGURATION



SYMBOL



LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.		UNIT
				-600 600 ¹	-800 800	
V_{DRM}	Repetitive peak off-state voltages		-			V
$I_{T(RMS)}$	RMS on-state current	full sine wave; $T_{hs} \leq 56^\circ\text{C}$	-	12		A
I_{TSM}	Non-repetitive peak on-state current	full sine wave; $T_j = 25^\circ\text{C}$ prior to surge	-	95		A
I^2t	I^2t for fusing	$t = 20\text{ ms}$	-	105		A
dl_T/dt	Repetitive rate of rise of on-state current after triggering	$t = 16.7\text{ ms}$	-	45		A ² s
		$t = 10\text{ ms}$	-	100		A/ μs
I_{GM}	Peak gate current	$I_{TM} = 20\text{ A}; I_G = 0.2\text{ A}; dl_G/dt = 0.2\text{ A}/\mu\text{s}$	-	2		A
P_{GM}	Peak gate power		-	5		W
$P_{G(AV)}$	Average gate power	over any 20 ms period	-	0.5		W
T_{stg}	Storage temperature		-40	150		$^\circ\text{C}$
T_j	Operating junction temperature		-	125		$^\circ\text{C}$

¹ Although not recommended, off-state voltages up to 800V may be applied without damage, but the triac may switch to the on-state. The rate of rise of current should not exceed 15 A/ μs .

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ISOLATION LIMITING VALUE & CHARACTERISTIC

$T_{hs} = 25\text{ °C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V_{isol}	R.M.S. isolation voltage from all three terminals to external heatsink	$f = 50\text{-}60\text{ Hz}$; sinusoidal waveform; R.H. $\leq 65\%$; clean and dustfree	-	-	2500	V
C_{isol}	Capacitance from T2 to external heatsink	$f = 1\text{ MHz}$	-	10	-	pF

THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$R_{th\ j\text{-}hs}$	Thermal resistance junction to heatsink	full or half cycle with heatsink compound	-	-	4.0	K/W
$R_{th\ j\text{-}a}$	Thermal resistance junction to ambient	without heatsink compound in free air	-	55	5.5	K/W

STATIC CHARACTERISTICS

$T_j = 25\text{ °C}$ unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.			UNIT
				...D	...E	...F	
I_{GT}	Gate trigger current ²	BTA212X-					
		$V_D = 12\text{ V}$; $I_T = 0.1\text{ A}$ T2+ G+	-	5	10	25	mA
		T2+ G-	-	5	10	25	mA
I_L	Latching current	$V_D = 12\text{ V}$; $I_{GT} = 0.1\text{ A}$ T2- G-	-	5	10	25	mA
		T2+ G+	-	15	25	30	mA
		T2+ G-	-	25	30	40	mA
		T2- G-	-	25	30	40	mA
I_H	Holding current	$V_D = 12\text{ V}$; $I_{GT} = 0.1\text{ A}$	-	15	25	30	mA
V_T	On-state voltage	$I_T = 17\text{ A}$	-	1.6			V
V_{GT}	Gate trigger voltage	$V_D = 12\text{ V}$; $I_T = 0.1\text{ A}$	-	1.5			V
		$V_D = 400\text{ V}$; $I_T = 0.1\text{ A}$; $T_j = 125\text{ °C}$	0.25	-			V
I_D	Off-state leakage current	$V_D = V_{DRM(max)}$; $T_j = 125\text{ °C}$	-	0.5			mA

² Device does not trigger in the T2-, G+ quadrant.

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DYNAMIC CHARACTERISTICS

T_j = 25 °C unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.			MAX.	UNIT
			...D	...E	...F		
dV _D /dt	Critical rate of rise of off-state voltage	BTA212X- V _{DM} = 67% V _{DRM(max)} ; T _j = 110 °C; exponential waveform; gate open circuit	30	60	70	-	V/μs
di _{com} /dt	Critical rate of change of commutating current	V _{DM} = 400 V; T _j = 125 °C; I _{T(RMS)} = 12 A; dV _{com} /dt = 10 V/μs; gate open circuit	1.0	8.0	21	-	A/ms
di _{com} /dt	Critical rate of change of commutating current	V _{DM} = 400 V; T _j = 125 °C; I _{T(RMS)} = 12 A; dV _{com} /dt = 0.1 V/μs; gate open circuit	3.5	16	32	-	A/ms

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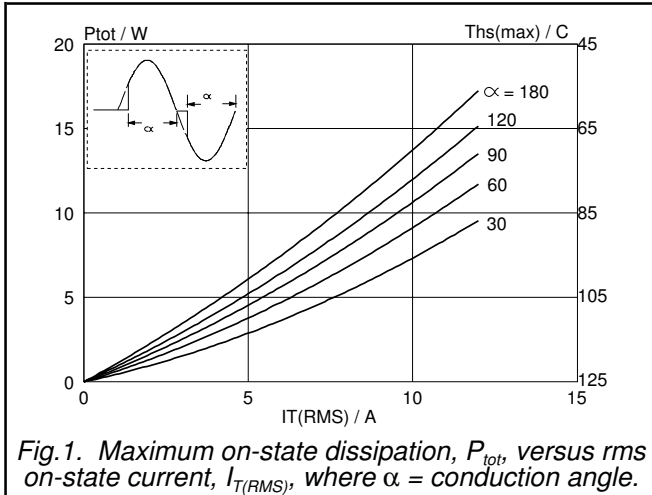


Fig. 1. Maximum on-state dissipation, P_{tot} , versus rms on-state current, $I_{T(RMS)}$, where α = conduction angle.

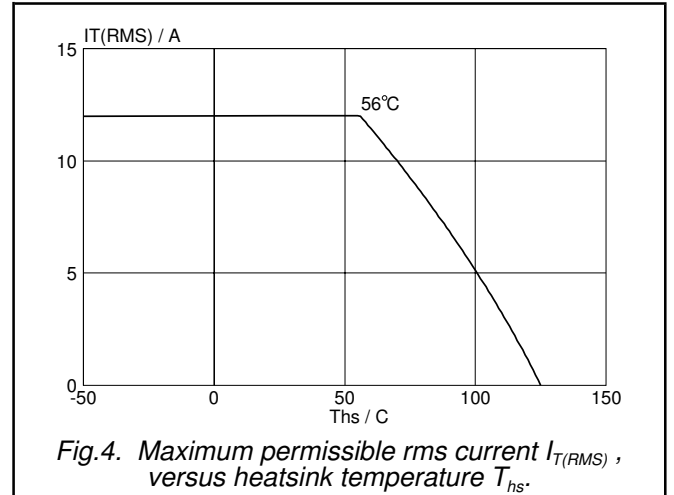


Fig. 4. Maximum permissible rms current $I_{T(RMS)}$, versus heatsink temperature T_{hs} .

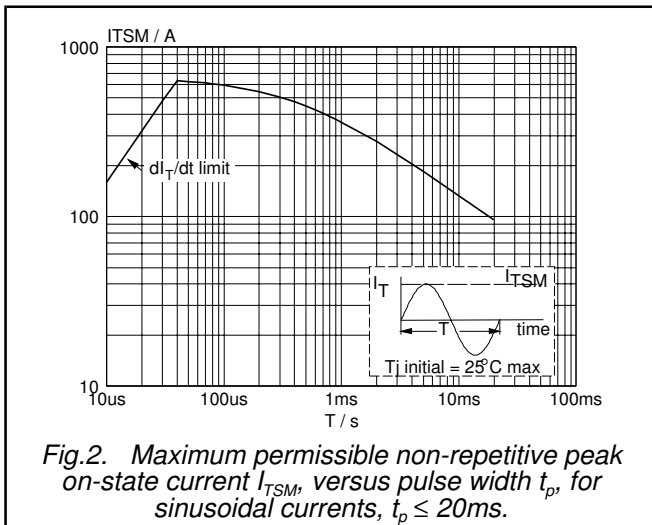


Fig. 2. Maximum permissible non-repetitive peak on-state current I_{TSM} , versus pulse width t_p , for sinusoidal currents, $t_p \leq 20$ ms.

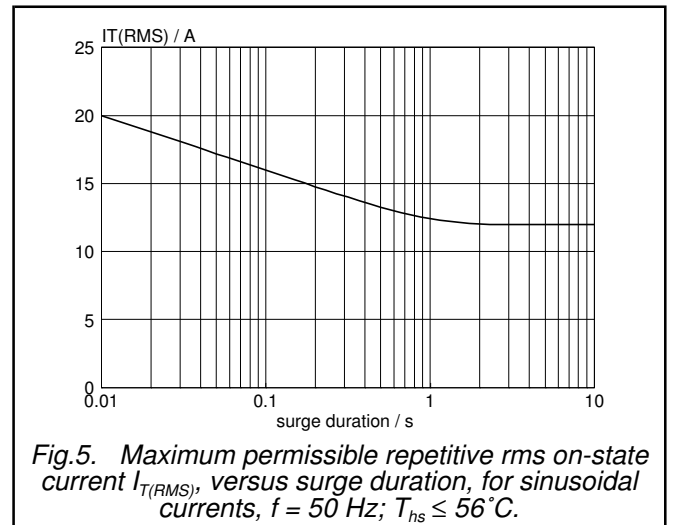


Fig. 5. Maximum permissible repetitive rms on-state current $I_{T(RMS)}$, versus surge duration, for sinusoidal currents, $f = 50$ Hz; $T_{hs} \leq 56$ C.

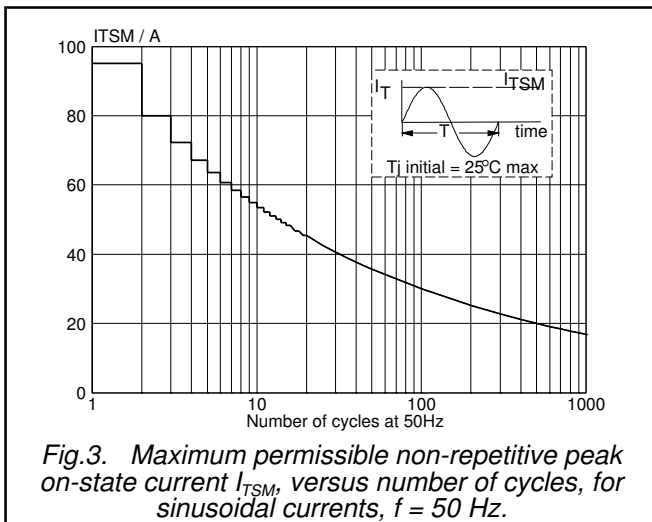


Fig. 3. Maximum permissible non-repetitive peak on-state current I_{TSM} , versus number of cycles, for sinusoidal currents, $f = 50$ Hz.

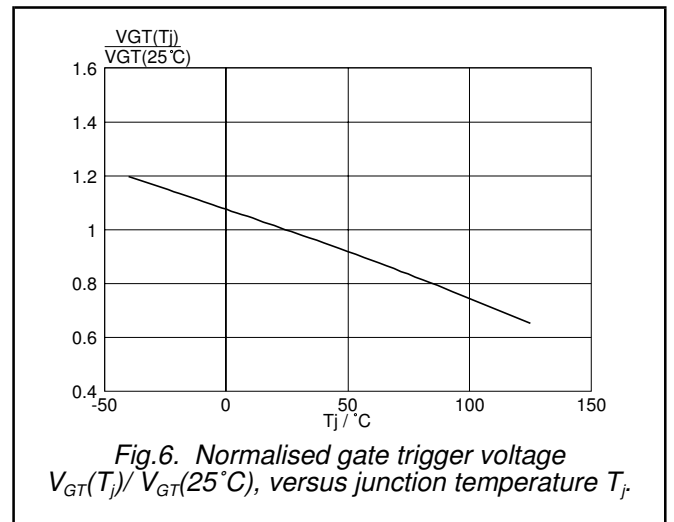
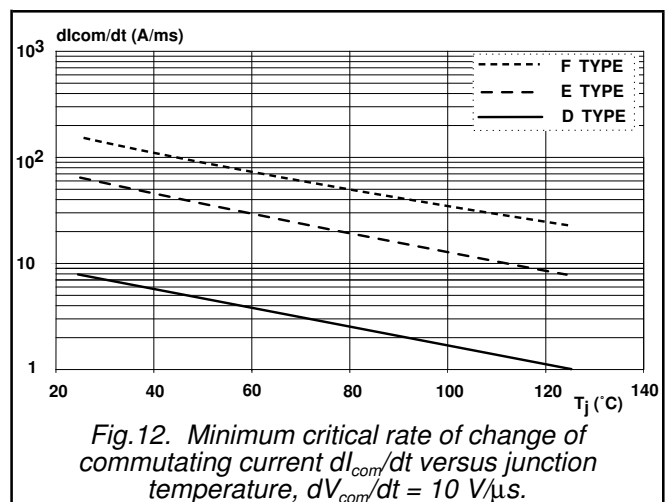
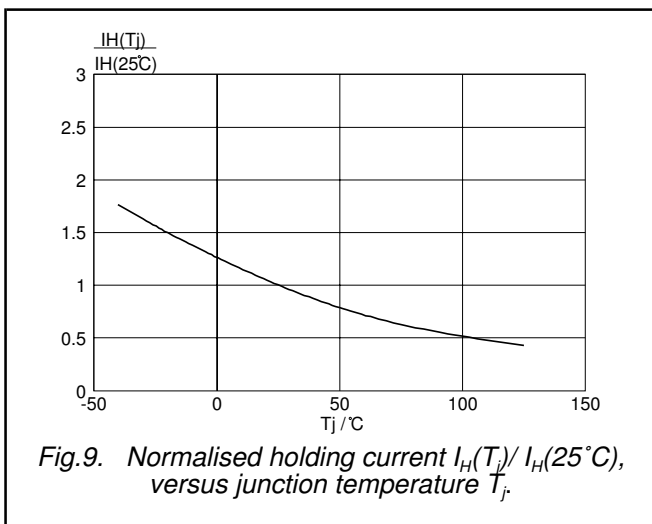
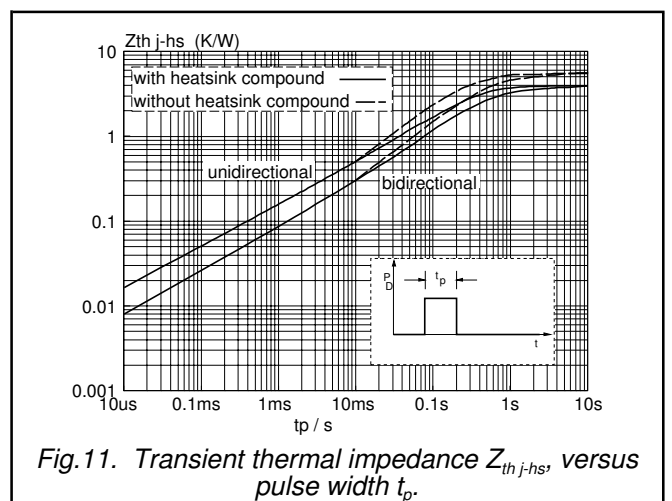
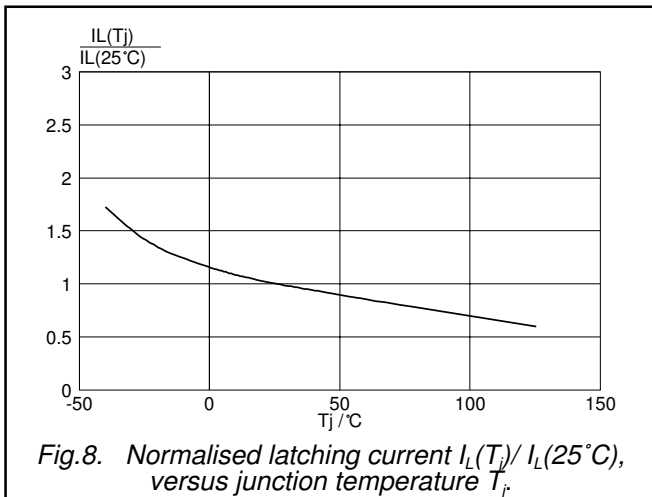
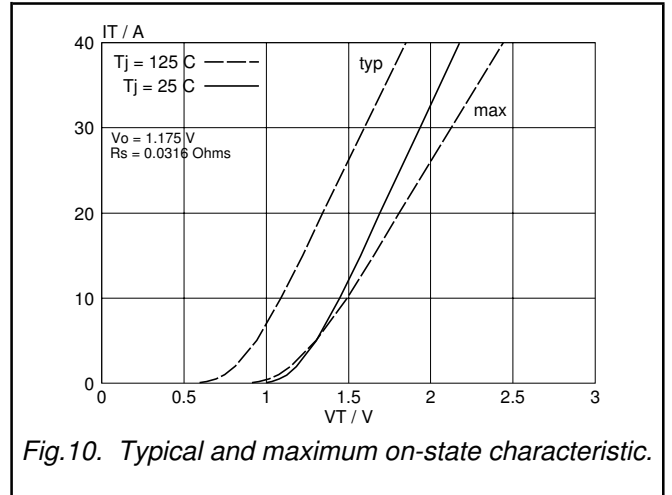
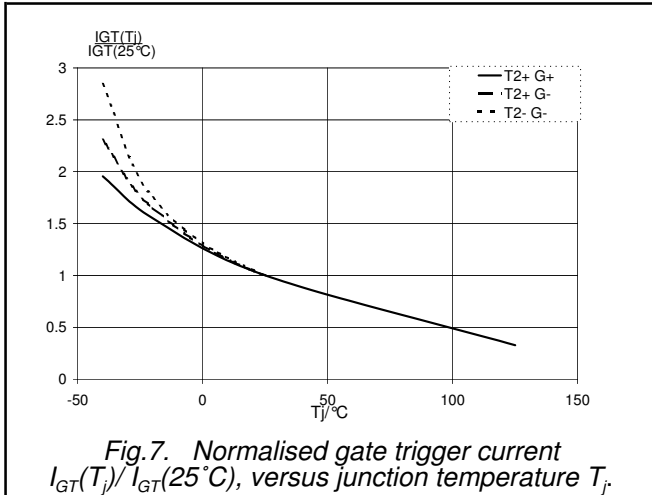


Fig. 6. Normalised gate trigger voltage $V_{GT}(T_j) / V_{GT}(25 C)$, versus junction temperature T_j .

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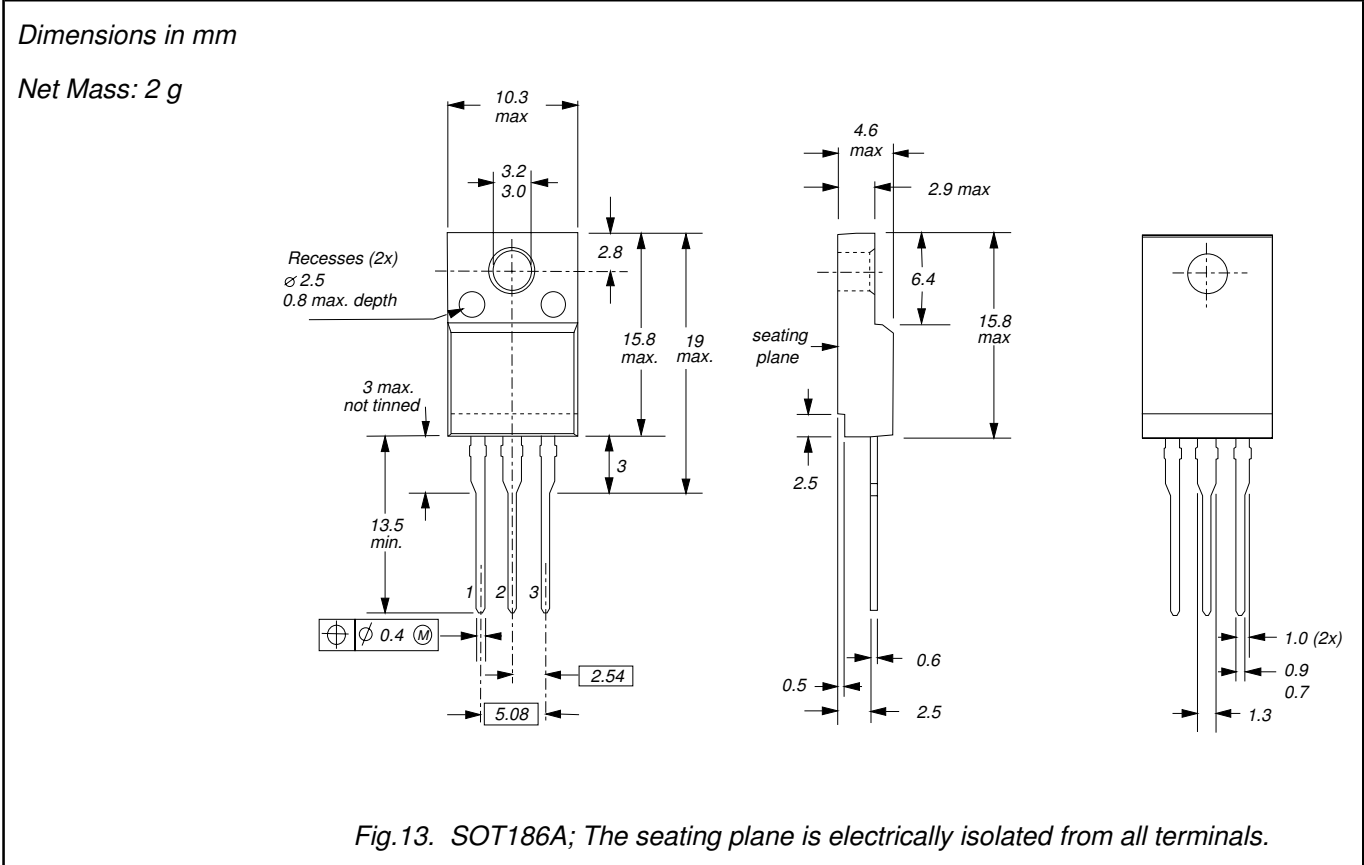
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MECHANICAL DATA



Notes

1. Refer to mounting instructions for F-pack envelopes.
2. Epoxy meets UL94 V0 at 1/8".

Legal information

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

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For sales offices addresses send e-mail to: salesaddresses@nxp.com

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